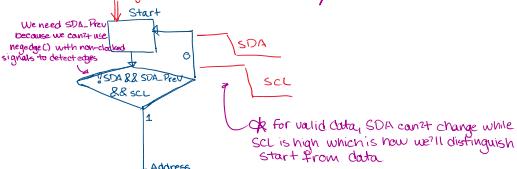
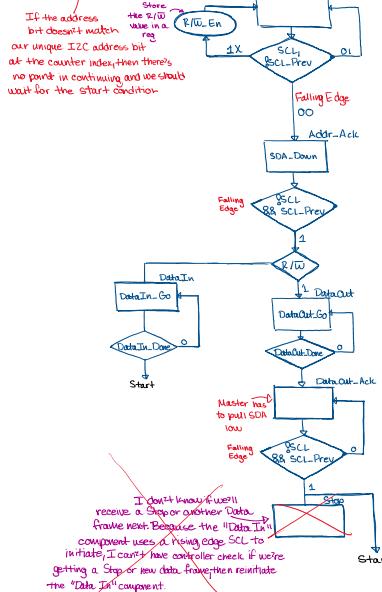


Starting simple with the slave receiver.



If the address bit doesn't match our unique I2C address bit at the counter index, then there's no point in continuing and we should wait for the start condition.



Just have "DataIn" check for stop and return execution

Break it up into controller, datapath, and other entities?

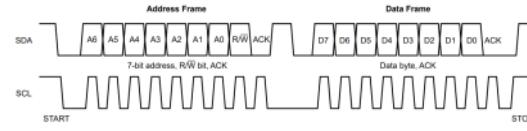
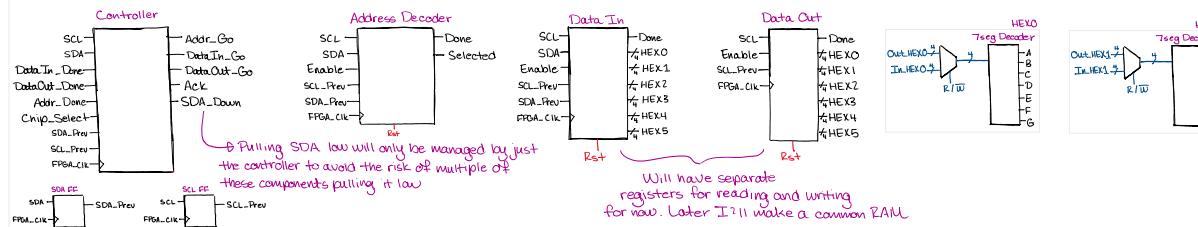
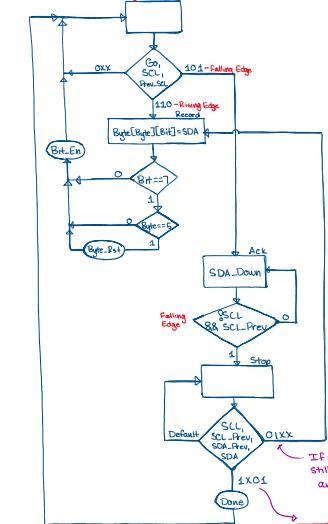
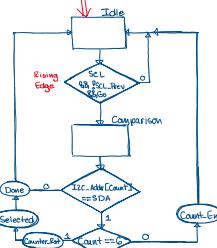
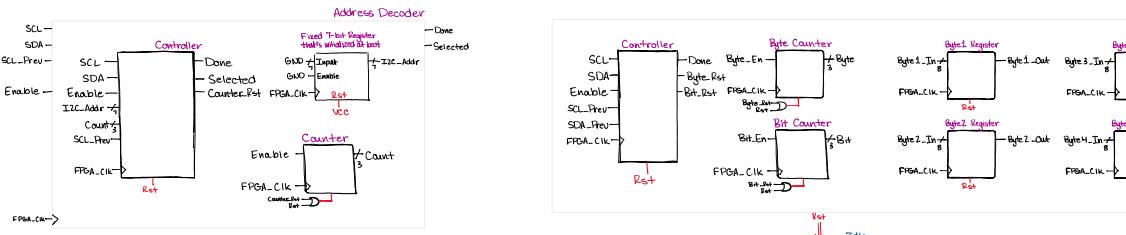


Figure 3-3. I<sup>2</sup>C Address and Data Frames

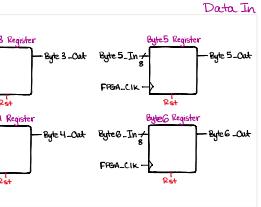
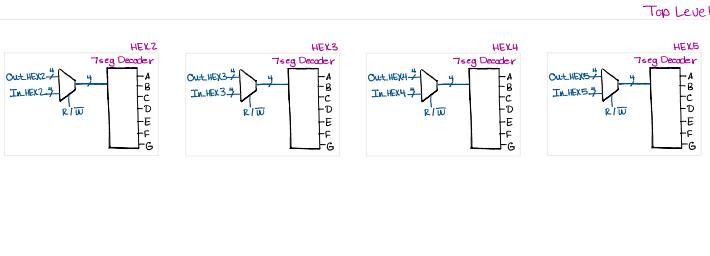
Stitt's methodology, design the hardware then the software



For these 3 components, I'll define their logic such that they assert done and return execution to the controller when SCL=0



EX1  
der  
-A  
-B  
-C  
-D  
-E  
-F  
-G



L and SDA  
that means  
started