PRE-LAB QUESTIONS OR EXERCISES

1. How late can you arrive for lab and still be admitted? **Twenty Minutes**
   How late can you arrive for lab and still be allowed to take the lab quiz? **Ten Minutes**
2. When are your prelab submissions to Canvas due? **Fifteen Minutes**
3. Can you drop this lab (Lab 0) if ...
   a. you overslept? **Yes**
   b. project is due for other class? **Yes**
4. Can any lab be dropped? **No**
5. What minimum lab average is required in order to be eligible to pass the course? **65%**
6. In electronic assembly, does neatness count? **Yes**
7. If you are very careful, is it okay to do all of the soldering (described below) and then show your finished board to your PI? **No**
8. Should the soldering iron touch the solder while soldering parts in place? **No**
9. Should the long or the short side of the headers get inserted into the breadboard? **Long Side**
10. When soldering each of the two 5-pin headers into J3 on PLD PCB, should the short pins point down or up? These headers are inserted from the top of the board, where the J3 is visible. **Point Down**

PROBLEMS ENCOUNTERED

N/A

REQUIREMENTS NOT MET

N/A

FUTURE WORK/APPLICATIONS

N/A

PRE-LAB REQUIREMENTS (Design, Schematic, ASM Chart, VHDL, etc.)

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EEL 3701C: Digital Logic and Computer Systems
Lab Rules & Policies
Revision 0

Dr. Eric M. Schwartz
21-Aug-19

9. Lab sessions are precisely **115 minutes** long in duration. Only with exception made for the event in which the course instructor and/or Peer Instructor(s) cause a delay for the student, the student must complete all requirements imposed within the lab session during the aforementioned time allotted.
10. Most labs will have a quiz. Quizzes will be used to assess the student's understanding of material related to the completed pre-lab and any completed lab, and may take as long as an hour and a half. The items permissible for use during a quiz may vary, where the course instructor or Peer Instructor(s) define this set of items. The student is responsible for completing all quizzes given. If the student arrives more than **ten minutes** after their lab begins, they will **NOT** be eligible to take the lab quiz. If the student arrives late, but prior to the ten-minute deadline, they may not receive any directions for the quiz.
11. **A weighted lab average of at least 65% must be achieved by the student in order for the student to be eligible to pass the course.**
12. It is the responsibility of the student to return all equipment and clean any pertinent work areas before leaving the lab. Failure to do so will result in at least a ten-point deduction from the relevant lab grade.
13. If the student is to perform any hardware construction with any equipment not provided within the lab, it is the responsibility of the student to verify with either the course instructor or any of the Peer Instructors that the chosen equipment is appropriate. Moreover, the use of the soldering iron and solder provided from the *Introduction to ECE* course within the Electrical & Computer Engineering Department at the University of Florida is **prohibited** (since it is inadequate for our purposes and may damage our PCBs).
14. In the event of a broken part, it is the student's responsibility to find an equivalent part. The *Lab Engineer*, Eric Liebner (whose office is located in NEB 236), or the *Engineering Supervisor*, Michael Stapleton (whose office is located in NEB 239), can help the student with replacement parts, but the student must always **consult with the course instructor and/or Peer Instructor(s) first**. If a replacement part is given to the student, the student may need to purchase the same (or similar) component, as to replace the one provided.
15. The student is **not** allowed to use any pre-built devices where the intention is to have you design them yourself.
16. It is **required** that the student read this entire document before submitting any pre-lab assignment and before attending any lab session. Failure to follow or correctly understand any of the above rules and policies may result in a point deduction of any amount from a lab grade, where this point deduction may be determined by either the course instructor or any Peer Instructor(s) deemed qualified by the course instructor.

By signing this, the undersigned student acknowledges and affirms that he/she has read and understood the same and hereby certifies and agrees that he/she will abide by all lab rules, policies, and guidelines set forth within this document.

Connor Dupuis — *Signature of student*

8/24/2019 — *Date*

Figure 1: Signed Copy of Labs and Rules

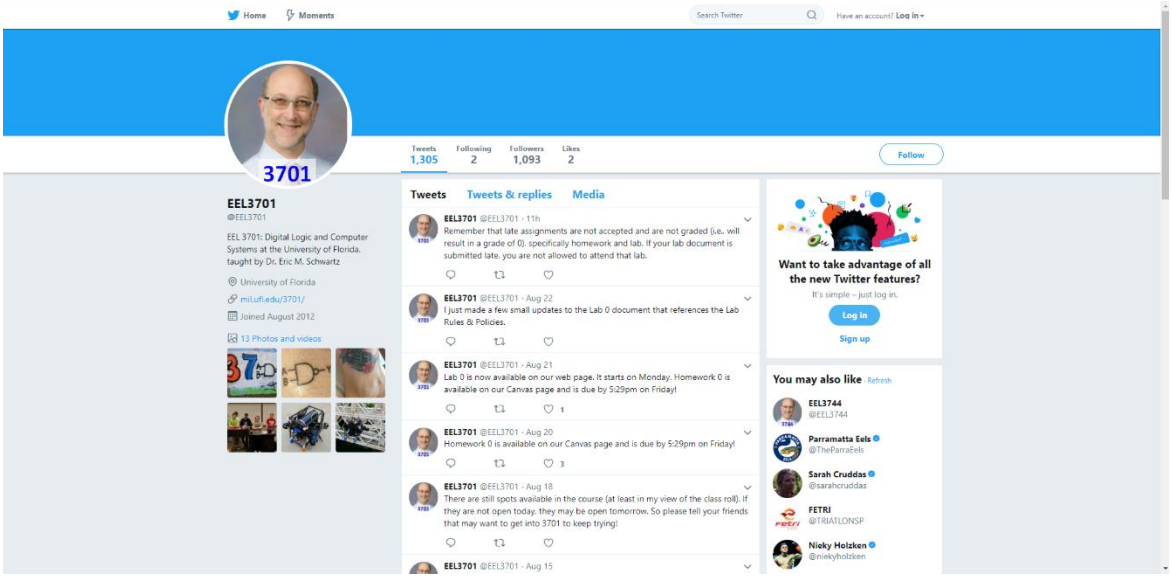

Figure 2: Screenshot of the Twitter page for 3701