Checklist:

- Display RMS Voltage on terminal and/or 7 segment display

- Display Peak Current on terminal and/or 7 segment display

- Display Power on terminal and/or 7 segment display

- Able to produce appropriate analog signals at the input of the ADCs

- Clear schematic and good PCB layout

- Modular and Readable code with comments

- Ltspice model is clear and has appropriate device values