



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

ALL ARTWORK VIEWED FROM TOP SIDE	PRJ TITLE = Core_STM32G4_JLC_PCB	REV = A
LAYER NAME = Top Bestlayer	PRJ # = None	DRAWN BY
CONTACT = john.kim@libmcu.org	GENERATED = 2023-02-12 18:50:52	JW

CONTACT = john.kim@dwginc.com	GENERATED = 2023-05-12 18:20:22	
	PRJ # = None	
LAYER NAME = Bottom Base	PRJ TITLE = Core_STM32F4_LC_PCB	
ALL ARTWORK VIEWED FROM TOP SIDE	REV = A	

COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

