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The image displays a detailed PCB layout for the NRF52840-QIAA-R module, showing both the top and bottom views. The top view (left) features the main microcontroller (U4A) and various peripheral components including a crystal oscillator (OSC1), a USB-to-UART bridge (U5), and several passive components like capacitors and resistors. The bottom view (right) shows the reverse side of the board, including the microcontroller (U4B), a USB-to-UART bridge (U5), and various passive components. The layout is color-coded with red and blue traces, and includes a legend for component values and pin connections.

The diagram shows two pin headers, J2 and J3, for the Double_Header_1x11_2_54 connector. Header J2 has 11 pins with the following connections: Pin 1 to +3V3, Pin 2 to P0_20, Pin 3 to P0_22, Pin 4 to P0_04/AIN2, Pin 5 to P0_05/AIN3, Pin 6 to P0_19, Pin 7 to P0_21, Pin 8 to P0_23/UART_TX, and Pin 9 to P0_25/UART_RX. Header J3 has 11 pins with the following connections: Pin 1 to +VIN_SV0, Pin 2 to +VSYS, Pin 3 to +VBAT, Pin 4 to P0_29/SPL_MISO, Pin 5 to P0_28/SPL_CLK, Pin 6 to P0_03/SPL_MOSI, Pin 7 to P1_15/SPI_nCS, Pin 8 to P1_13/I2C_SDA, and Pin 9 to P1_12/I2C_SCL. Pins 10 and 11 on both headers are unconnected.

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