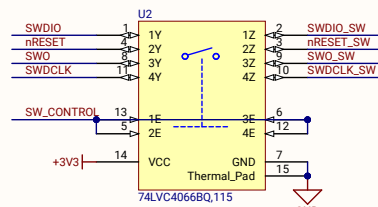
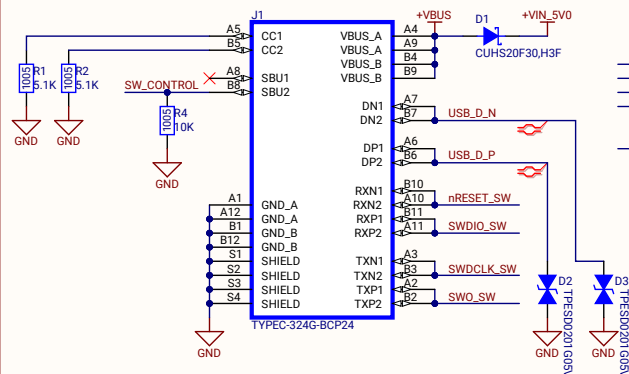
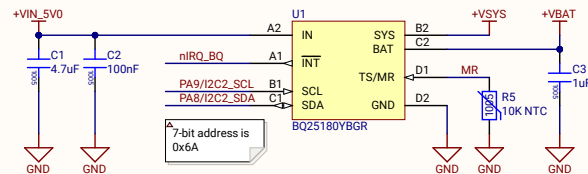


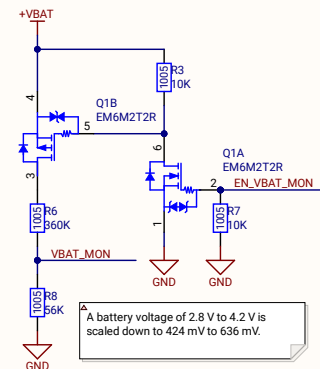
## IC Switch for Debug pins



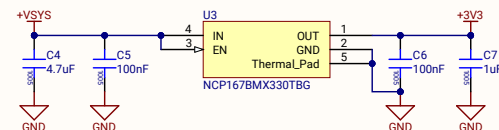
## BMS



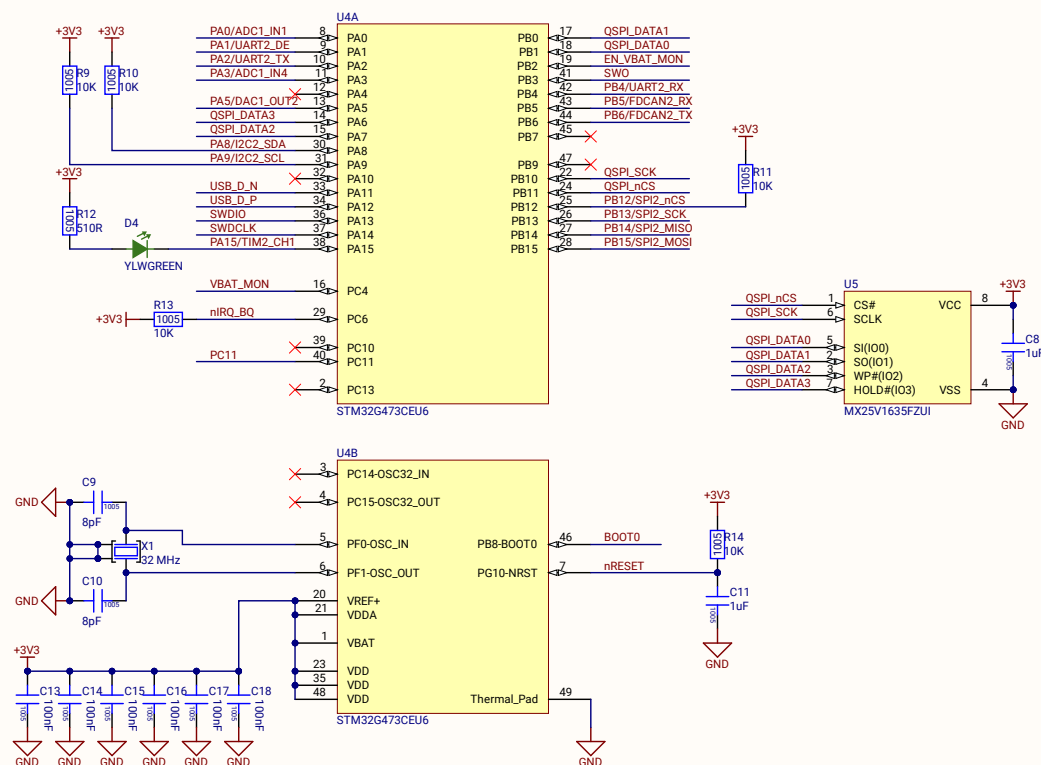
## VBAT Monitoring



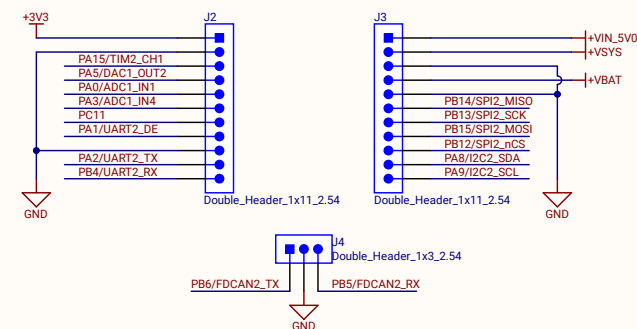
**LDO (3.3 V 700 mA)**



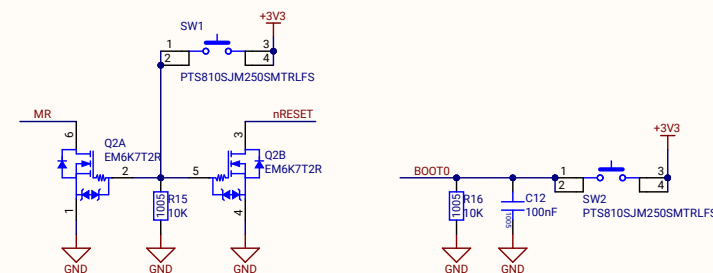
# STM32G473CEU6



## Header pins for connectivity



## Tactile Switches



Project Title: <a href="#">Core_STM32G4_JLC_PCB</a>			
Sheet Title: <a href="#">[03] - MCU and Conn SchDoc</a>			
Number: <a href="#">None</a>	Variant: <a href="#">[No Variations]</a>	Rev: <a href="#">A</a>	
Date: <a href="#">2023-02-12</a>	Sheet: <a href="#">3</a> of <a href="#">3</a>	Size: <a href="#">A3</a>	
Drawn By: <a href="#">JW</a>		Checked By: <a href="#">K</a>	
Contact: <a href="mailto:john.kim@libmcu.org">john.kim@libmcu.org</a>			

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