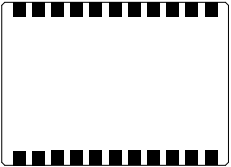


COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

ALL ARTWORK VIEWED FROM TOP SIDE	PRJ TITLE = Core_ESP32-S3_JLC_PCB	REV = A
LAYER NAME = Top Besegbator	PRJ # = None	DRAWN BY
CONTACT = john.kim@libmcu.org	GENERATED = 2023-02-12 18:52:15	JW



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

ALL ARTWORK VIEWED FROM TOP SIDE	PRJ TITLE = Core_Esp32-23_1TC_PCB	REV = A
LAYER NAME = Bottom Board	PRJ # = None	DRAWN BY JM
CONTACT = john.kim@pwmcu.org	GENERATED = 2023-02-12 18:22:12	