COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.

ALL ARTWORK VIEWED FROM TOP SIDE	PRJ TITLE = Core_STM32G4_JLC_PCB	REV = A
LAYER NAME = Top Assembly	PRJ # = None	DRAWN BY
CONTACT = john.kim@libmcu.org	GENERATED = 2023-02-12 18:50:52	JW

ALL ARTWORK VIEWED FROM TOP SIDE	PR1 TITLE = Core_STM32G4_JLC_PCB	REV = A
LAYER NAME = Bottom Resistor	PR1 # = None	DRAWN BY
CONTACT = john.kim@libmcu.org	GENERATED = 2023-02-12 18:50:52	JW