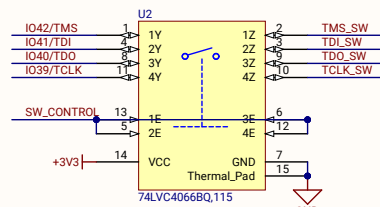
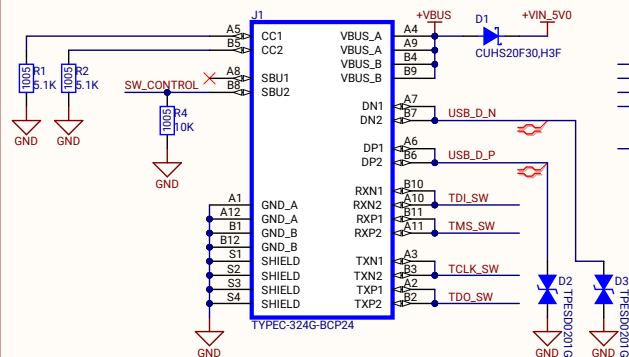
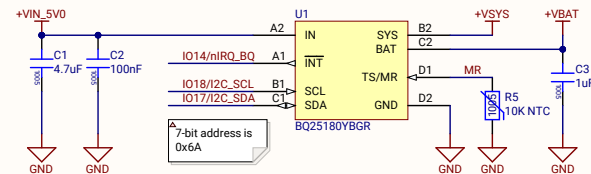


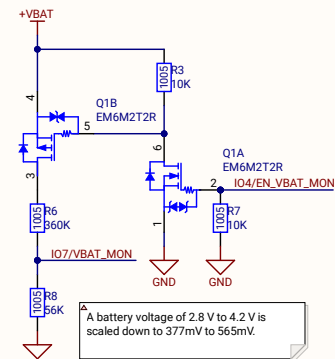
IC Switch for Debug pins



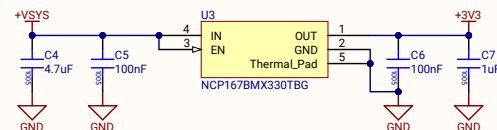
BMS



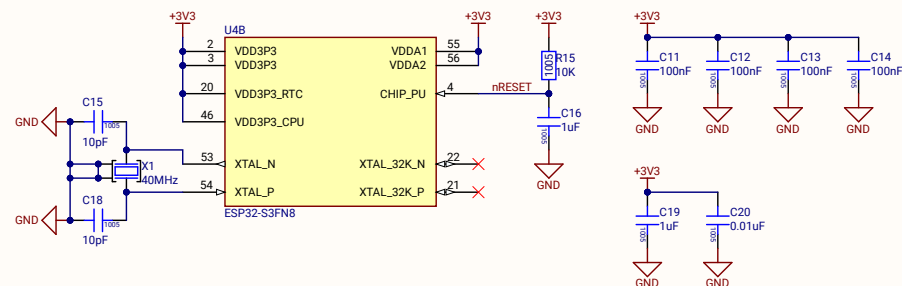
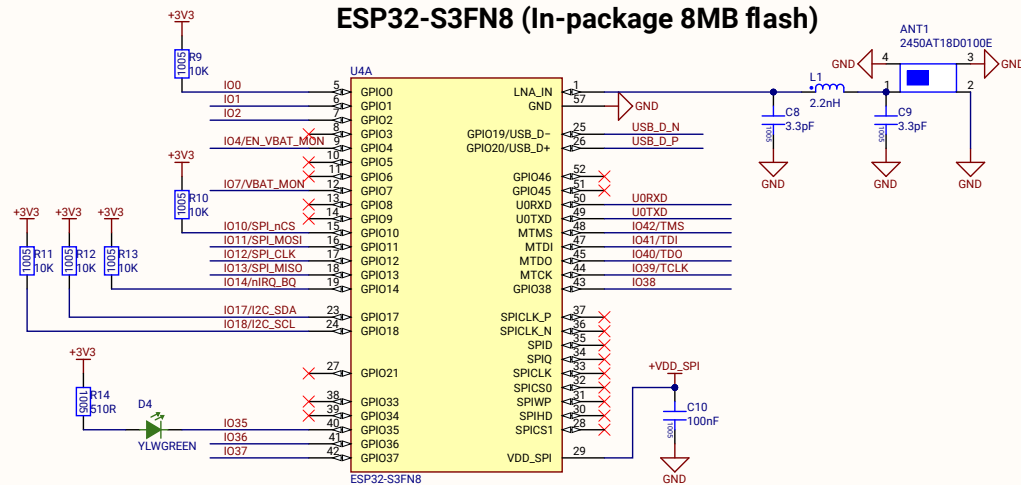
VBAT Monitoring



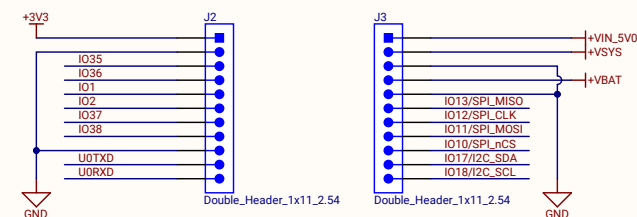
LDO (3.3 V 700 mA)



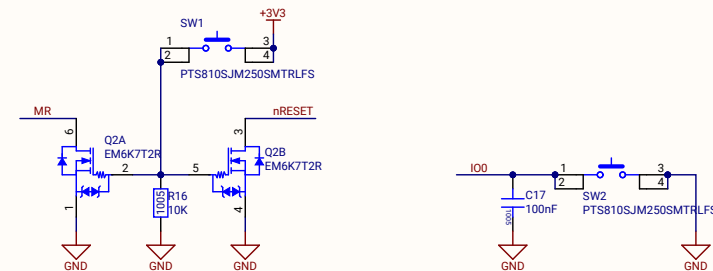
ESP32-S3FN8 (In-package 8MB flash)



Header pins for connectivity



Tactile Switches



Project Title: Core ESP32-S3 JLC PCB			
Sheet Title: [03] - MCU and Conn.SchDoc			
Number: None	Variant: [No Variations]		Rev: A
Date: 2023-02-13	Sheet: 3 of 3		Size: A3
Drawn By: JW		Checked By: K	
Contact: john.kim@libmcu.org			

libmcpu