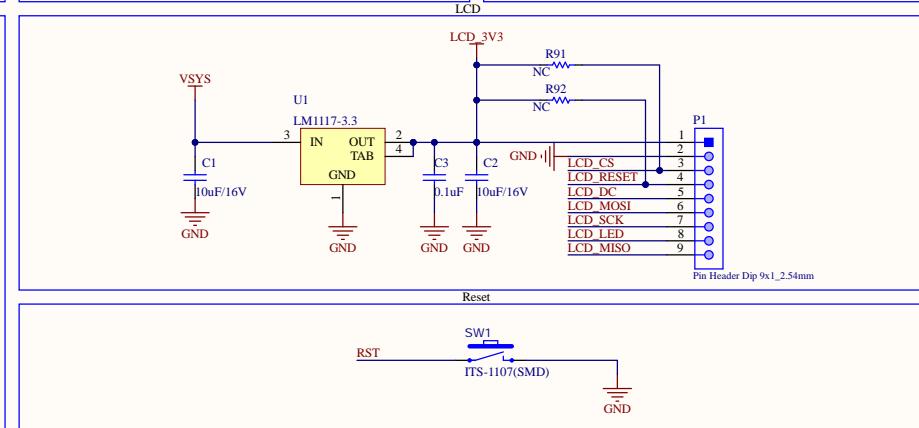
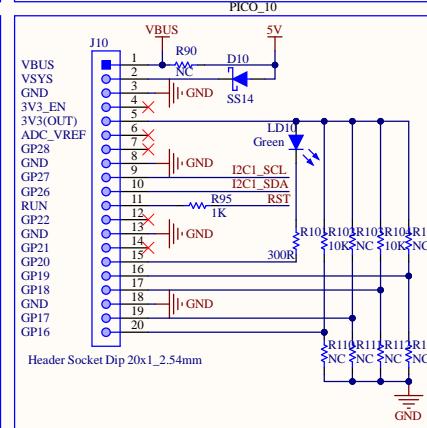
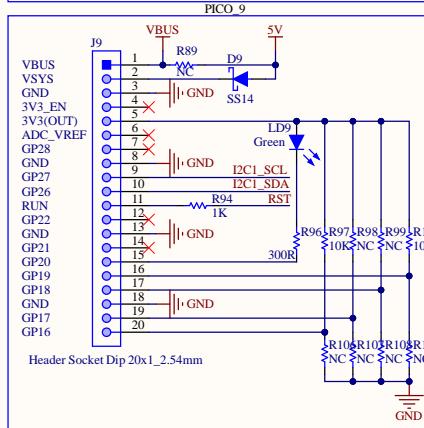
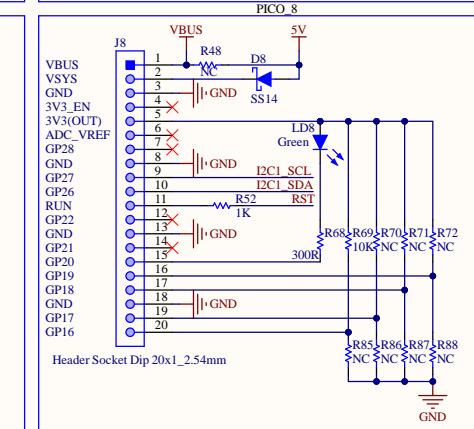
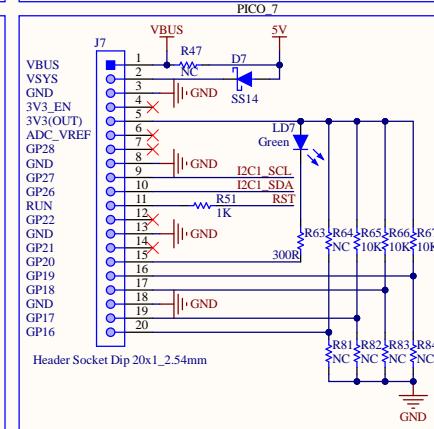
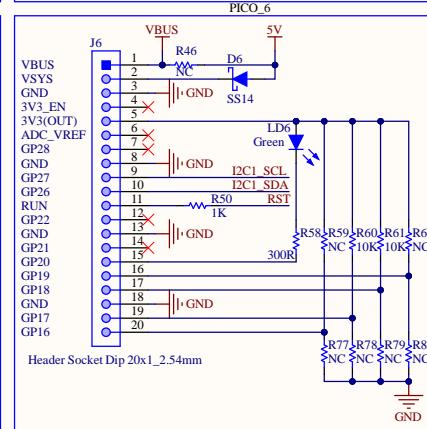
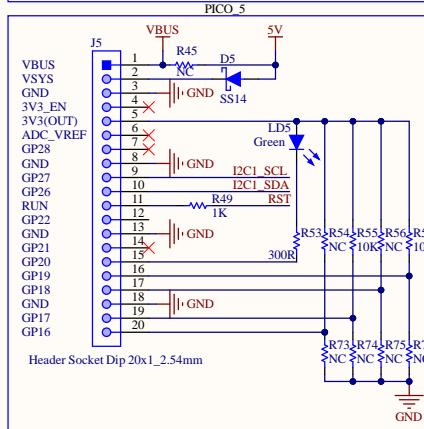
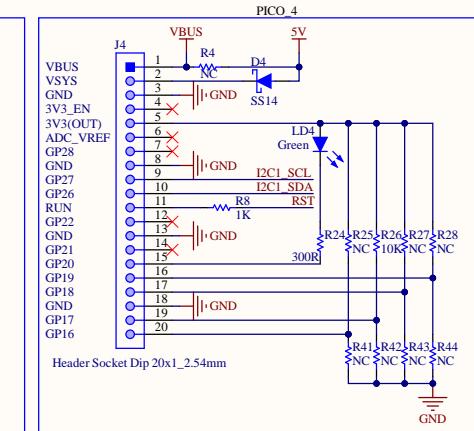
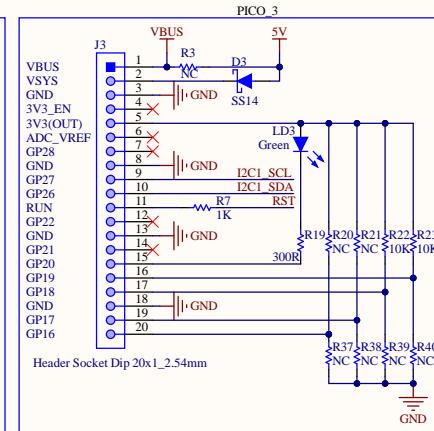
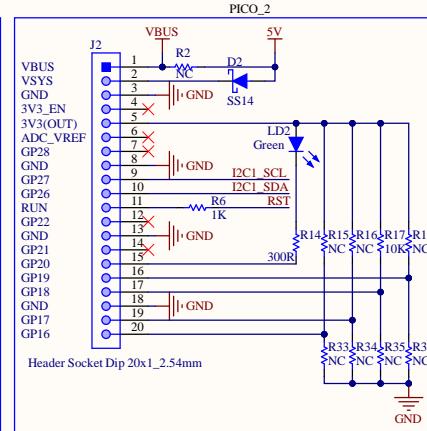
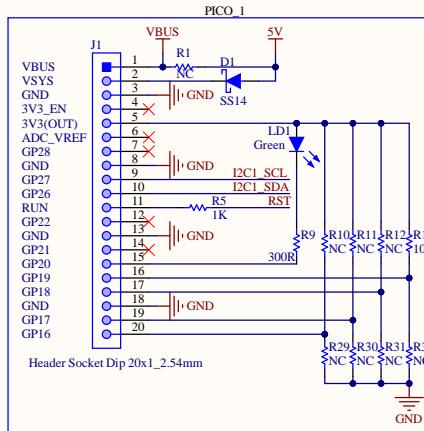
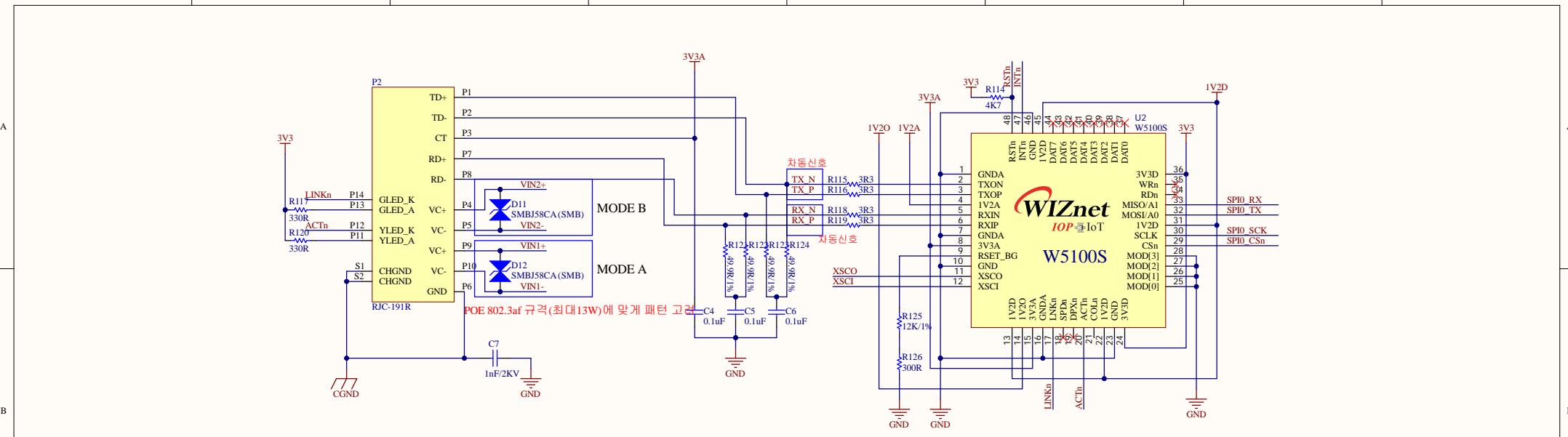


VBUS->5V 1.5A 패턴 고려



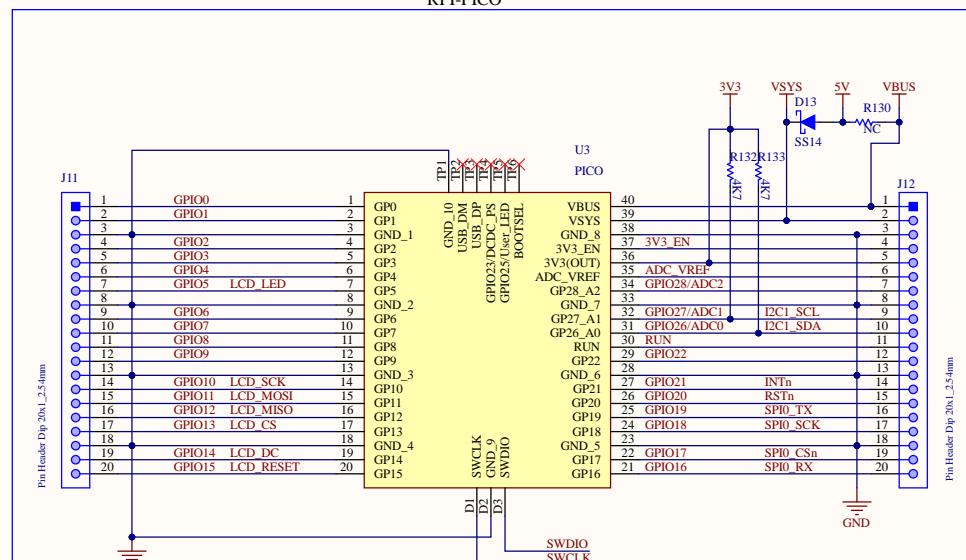
Title		
Size	Number	Revision
A3		
Date: 9-30-2022	Sheet of	
File: E:\WORK_\PICO,LCD.SchDoc		Drawn By:



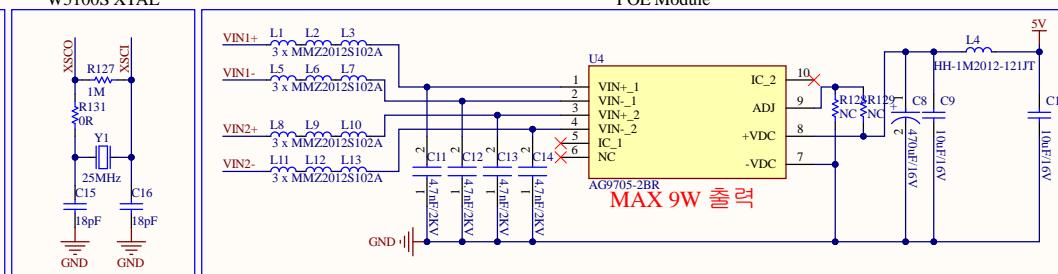
B

B

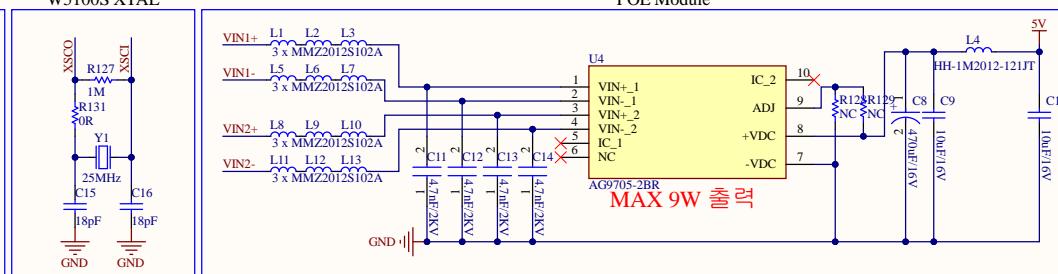
VBUS->5V 1.5A 패턴 고려 RPI-PICO



W5100S XTAL



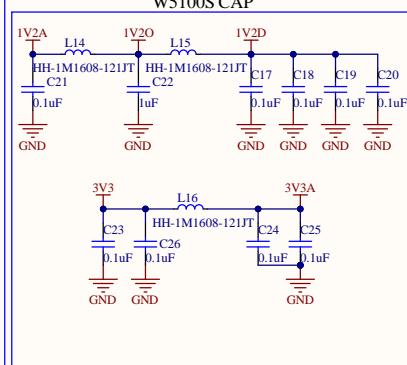
POE Module



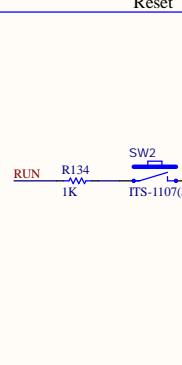
C

C

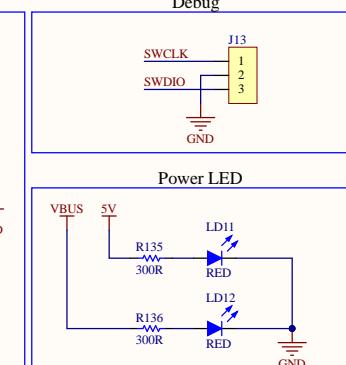
W5100S CAP



Reset



5V 1.5A 패턴 두께 고려



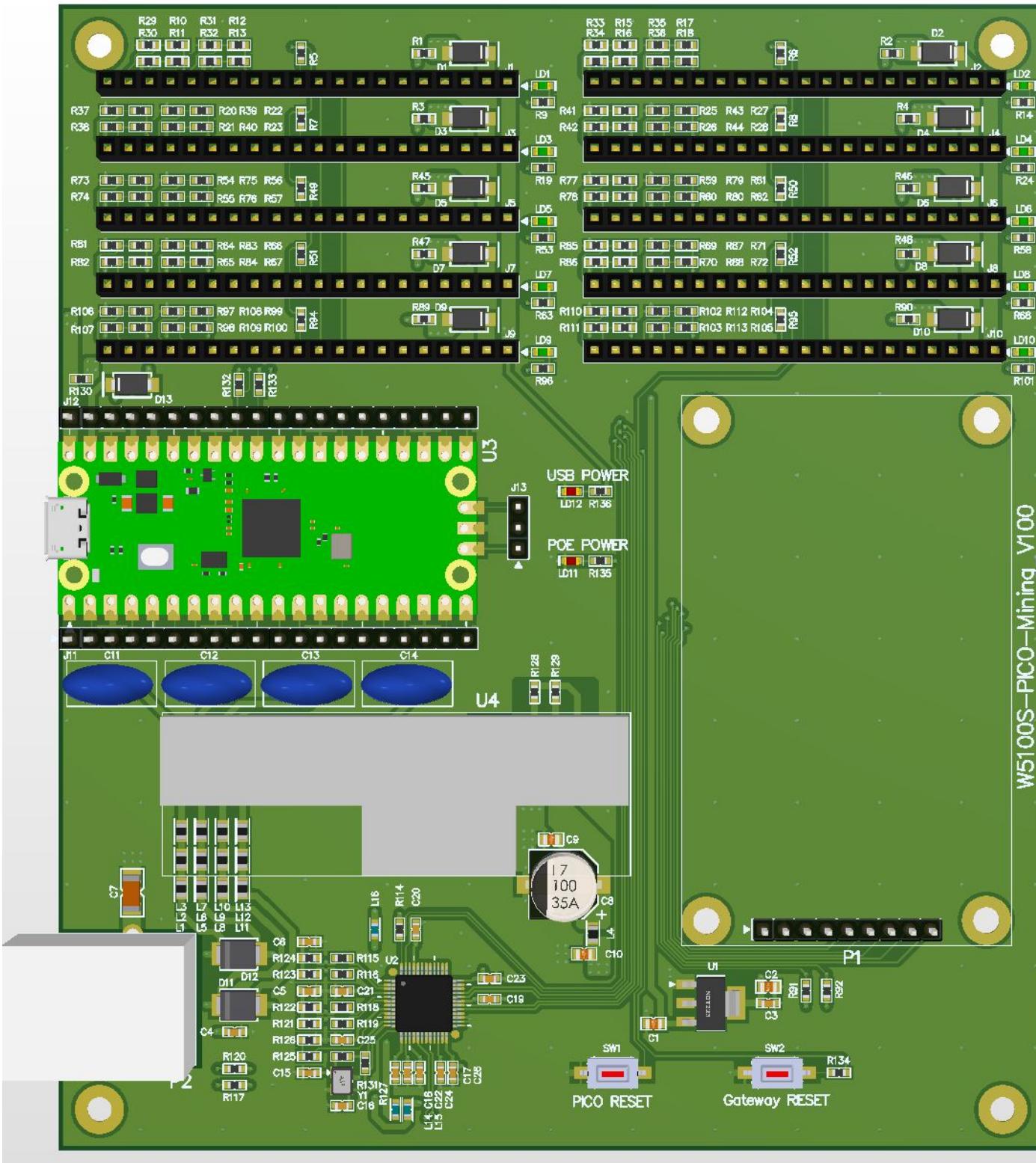
Title

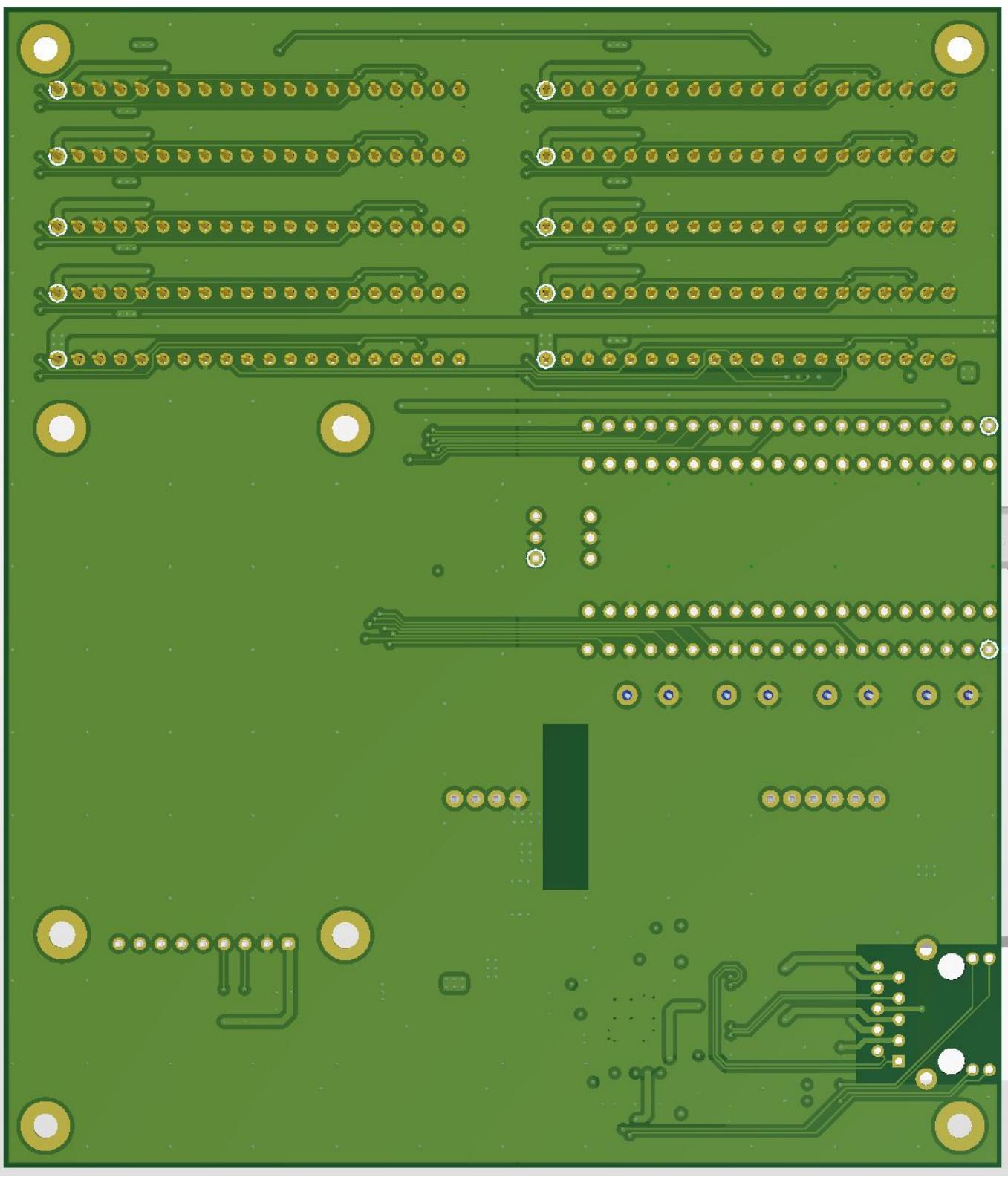
Size
A3Date:
9-30-2022Number
Sheet of
1File:
E:\WORK...\W5100S+POE.SchDocRevision
Drawn By:

D

D

W5100S-PICO-Mining_V100





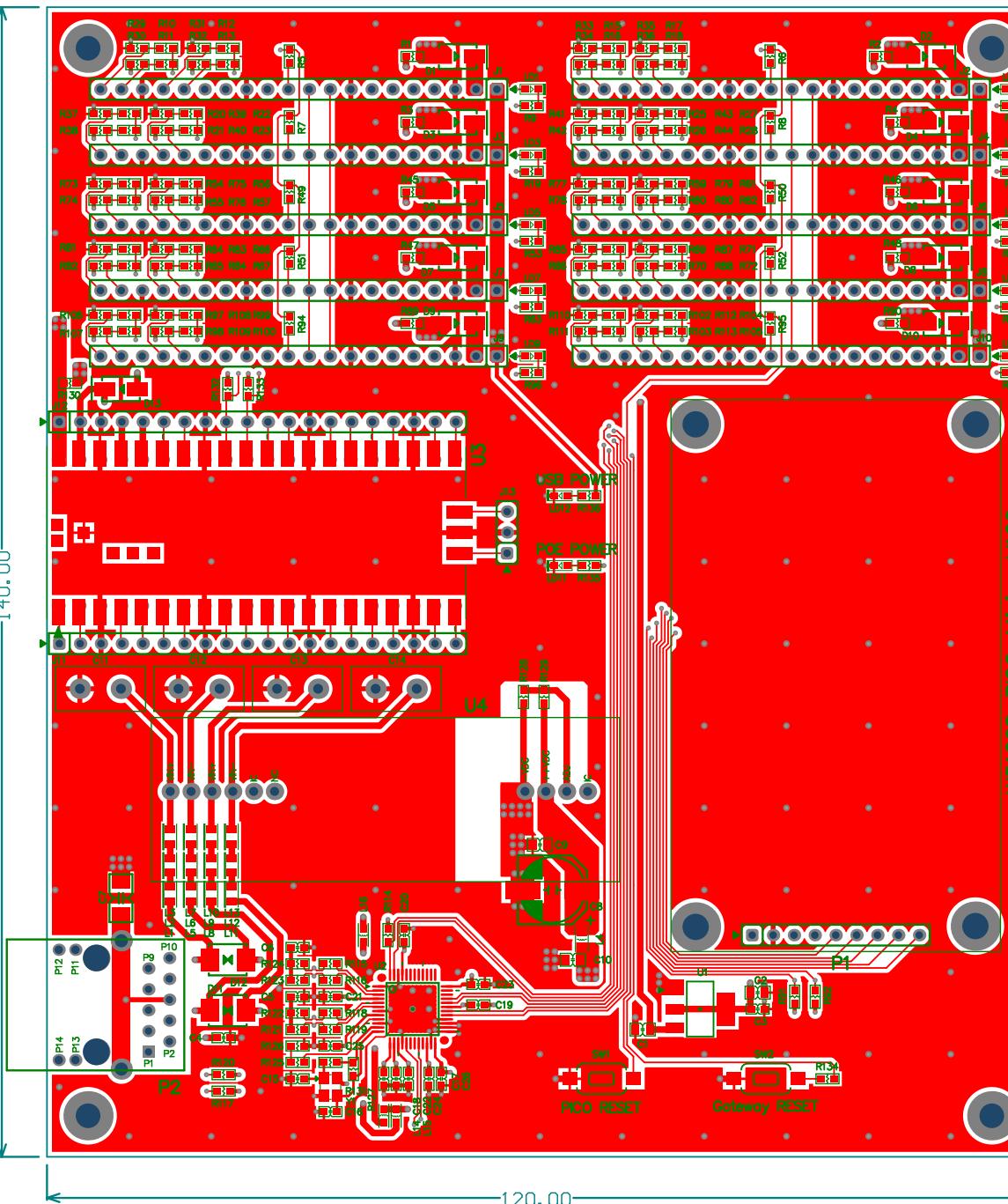
BOARD NAME : W5100S-PICO-Mining_V100

VERSION : V100

SIZE : 120mmX140mmX1.6T

GERBER DATE : 2022. 09. 30.

LAYER : Top Overlay



Layer Stack Up Detail for:
W5100S-PICO-Mining_V100.PcbDoc

Layer Name : 4L_1.6T

Top Layer
Internal Plane 1 (GND)
Internal Plane 2 (POWER)
Bottom Layer

KS Technology artwork

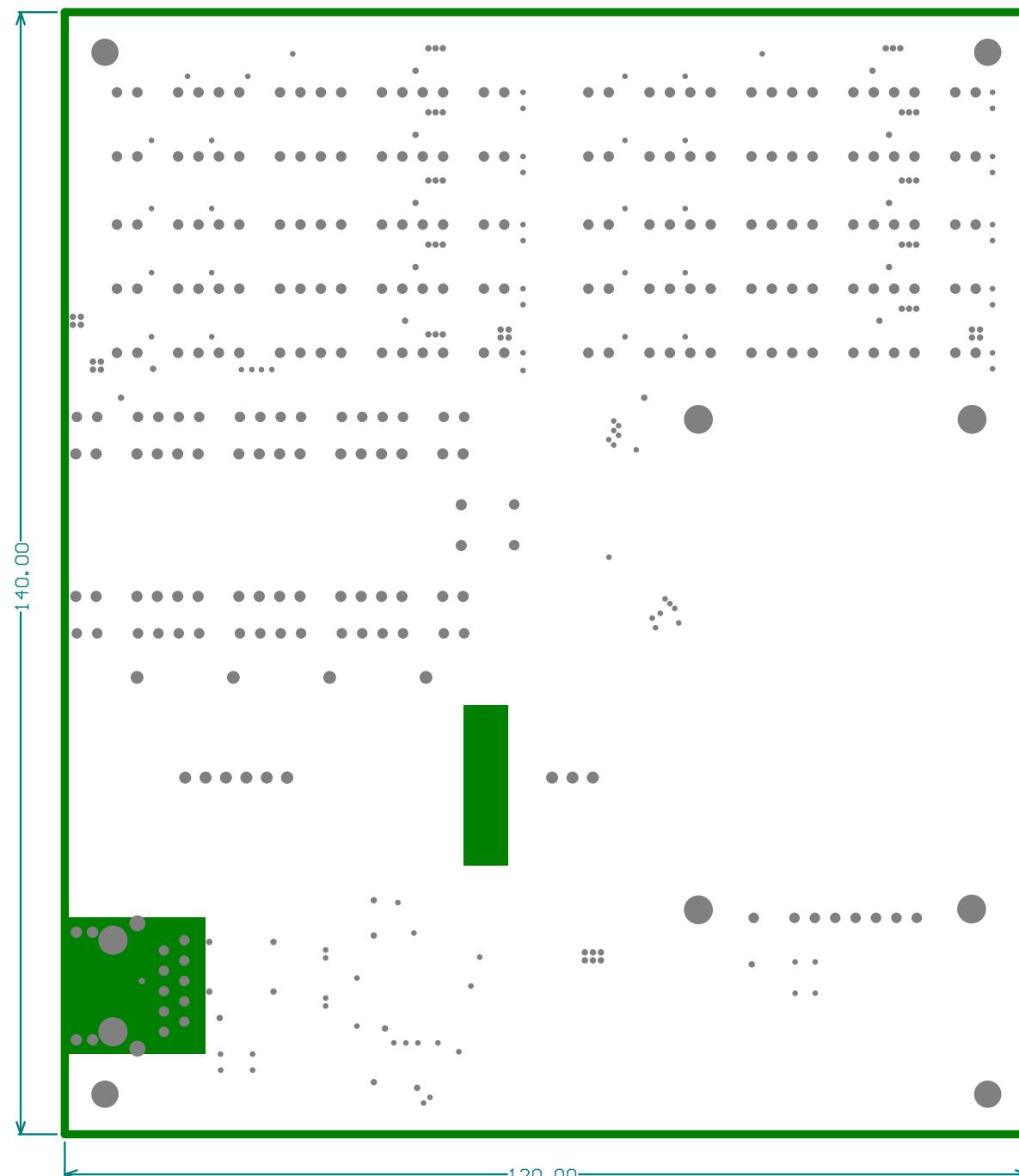
BOARD NAME : W5100S-PICO-Mining_V100

VERSION : V100

SIZE : 120mmX140mmX1.6T

GERBER DATE : 2022. 09. 30.

LAYER : Internal Plane 1



Layer Stack Up Detail for:

W5100S-PICO-Mining_V100.PcbDoc

Layer Name : 4L_1.6T

Top Layer

Internal Plane 1 (GND)

Internal Plane 2 (POWER)

Bottom Layer

KS Technology artwork

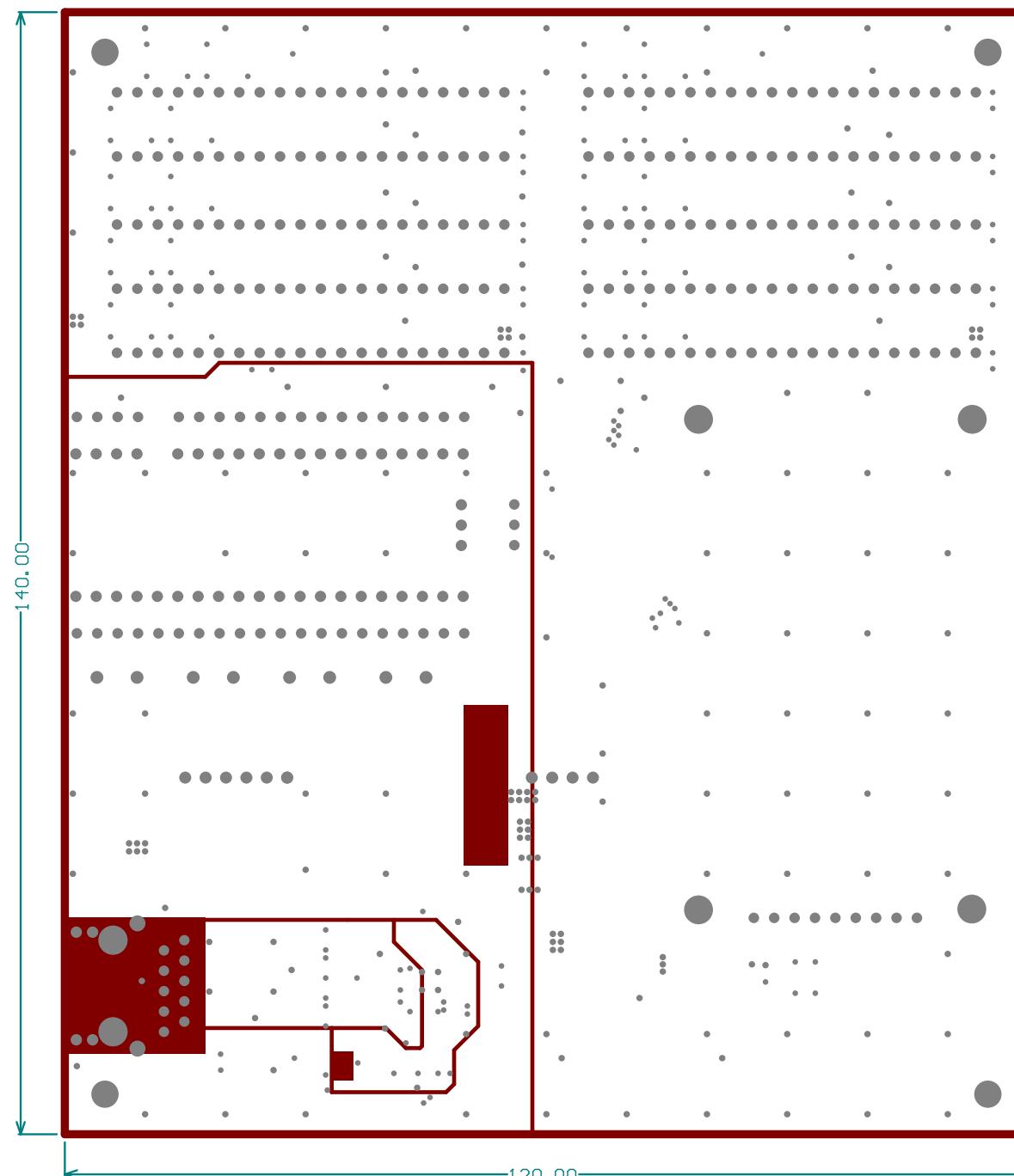
BOARD NAME : W5100S-PICO-Mining_V100

VERSION : V100

SIZE : 120mmX140mmX1.6T

GERBER DATE : 2022. 09. 30.

LAYER : Internal Plane 2



Layer Stack Up Detail for:
W5100S-PICO-Mining_V100.PcbDoc

Layer Name : 4L_1.6T

Top Layer
Internal Plane 1 (GND)
Internal Plane 2 (POWER)
Bottom Layer

KS Technology artwork

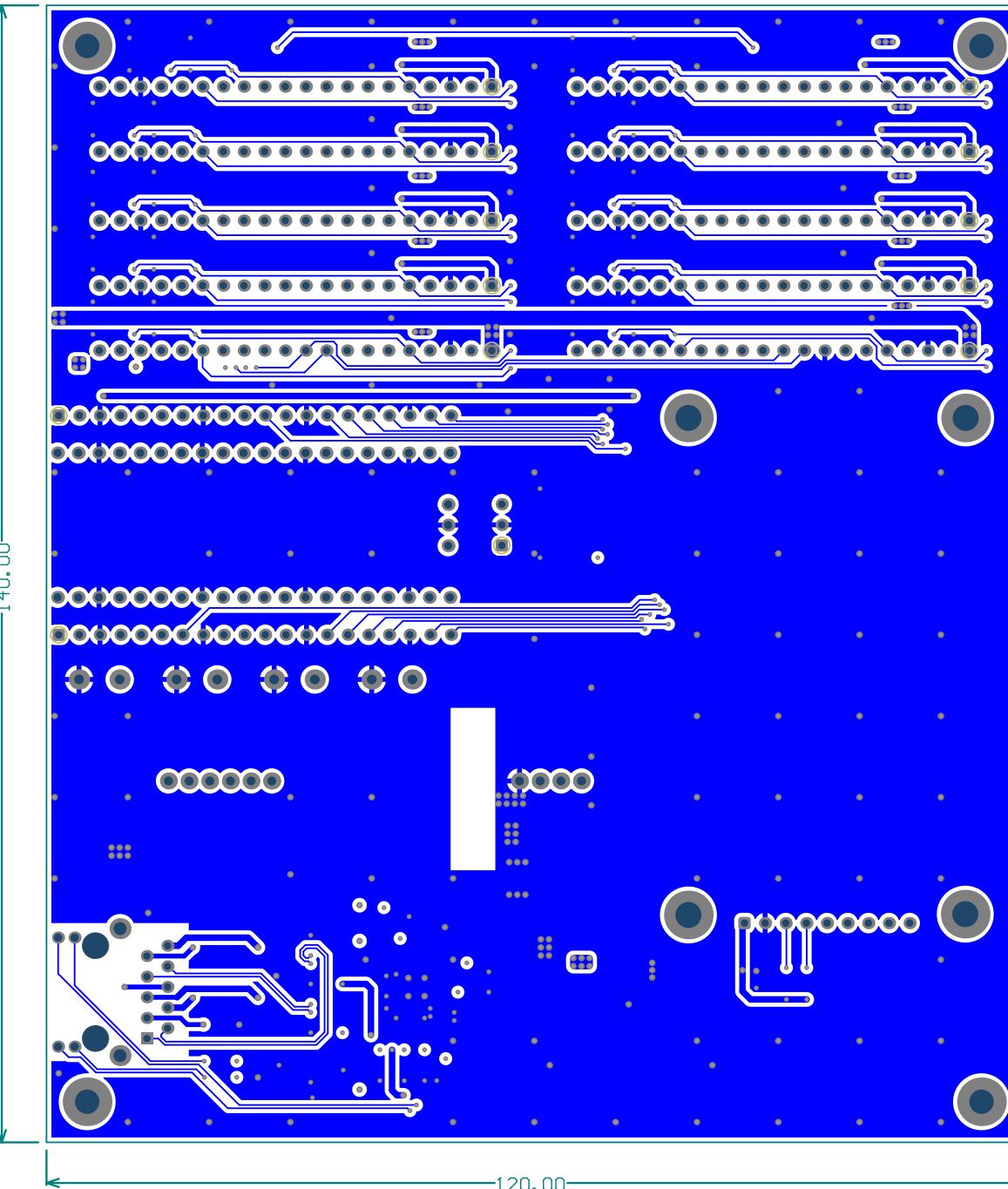
BOARD NAME : W5100S-PICO-Mining_V100

VERSION : V100

SIZE : 120mmX140mmX1.6T

GERBER DATE : 2022. 09. 30.

LAYER : Bottom Overlay



Layer Stack Up Detail for:
W5100S-PICO-Mining_V100.PcbDoc

Layer Name : 4L_1.6T

Top Layer
Internal Plane 1 (GND)
Internal Plane 2 (POWER)
Bottom Layer

KS Technology artwork

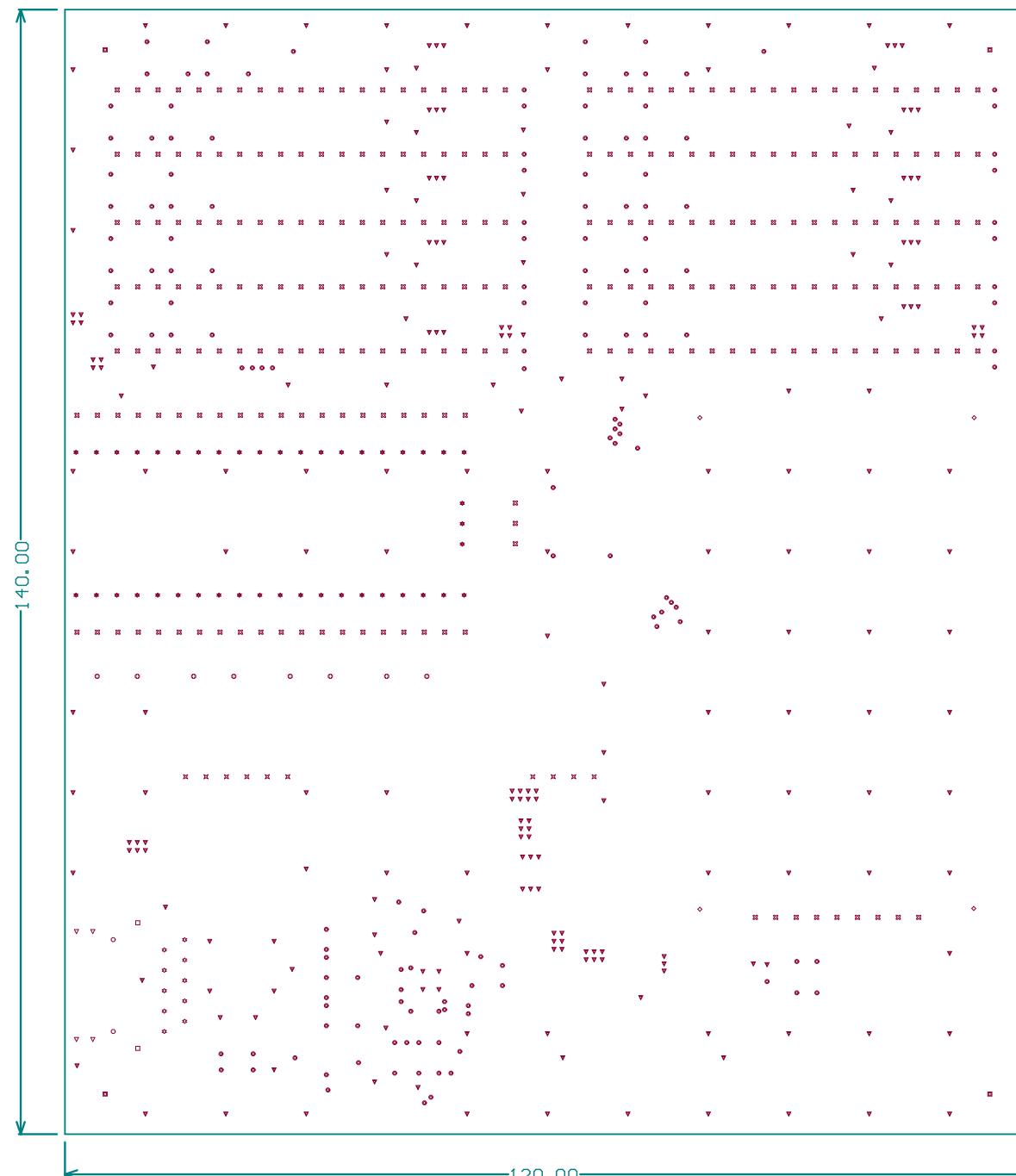
BOARD NAME : W5100S-PICO-Mining_V100

VERSION : V100

SIZE : 120mmX140mmX1.6T

GERBER DATE : 2022. 09. 30.

LAYER : Drill Drawing



Symbol	Count	Hole Size	Plated	Hole Type
□	2	1.600mm (62.99mil)	PTH	Round
○	2	3.250mm (127.95mil)	NPTH	Round
▽	4	1.020mm (40.16mil)	PTH	Round
■	4	3.000mm (118.11mil)	PTH	Round
◇	4	3.200mm (125.98mil)	PTH	Round
⊕	8	1.200mm (47.24mil)	PTH	Round
⊗	10	0.900mm (35.43mil)	PTH	Round
⊛	10	1.100mm (43.31mil)	PTH	Round
✳	43	1.000mm (39.37mil)	PTH	Round
◎	153	0.300mm (11.81mil)	PTH	Round
▽	244	0.400mm (15.75mil)	PTH	Round
⊗	252	0.914mm (36.00mil)	PTH	Round
736 Total				

Layer Stack Up Detail for:
W5100S-PICO-Mining_V100PcbDoc

Layer Name : 4L_1.6T

Top Layer
Internal Plane 1 (GND)
Internal Plane 2 (POWER)
Bottom Layer

KS Technology artwork