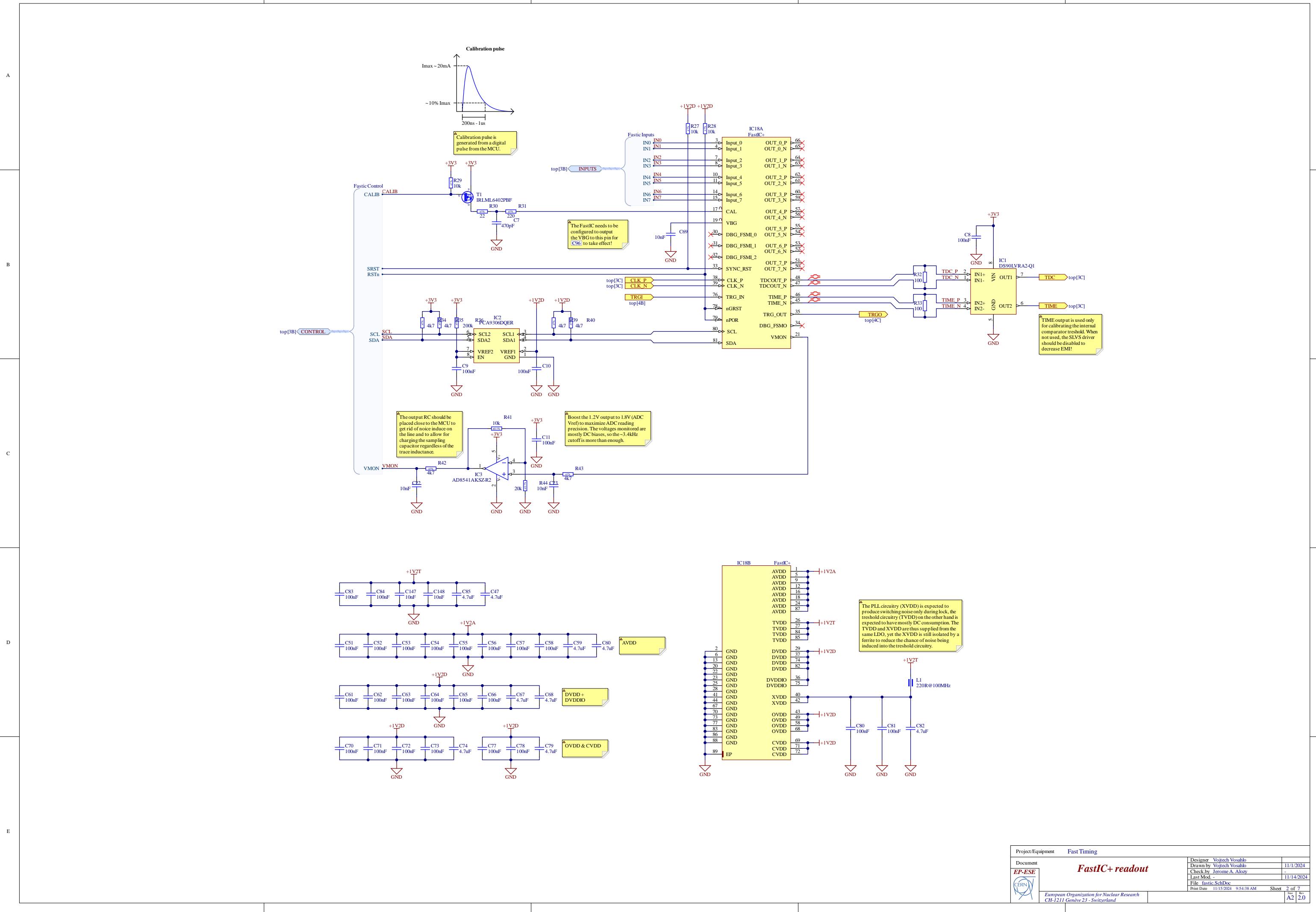
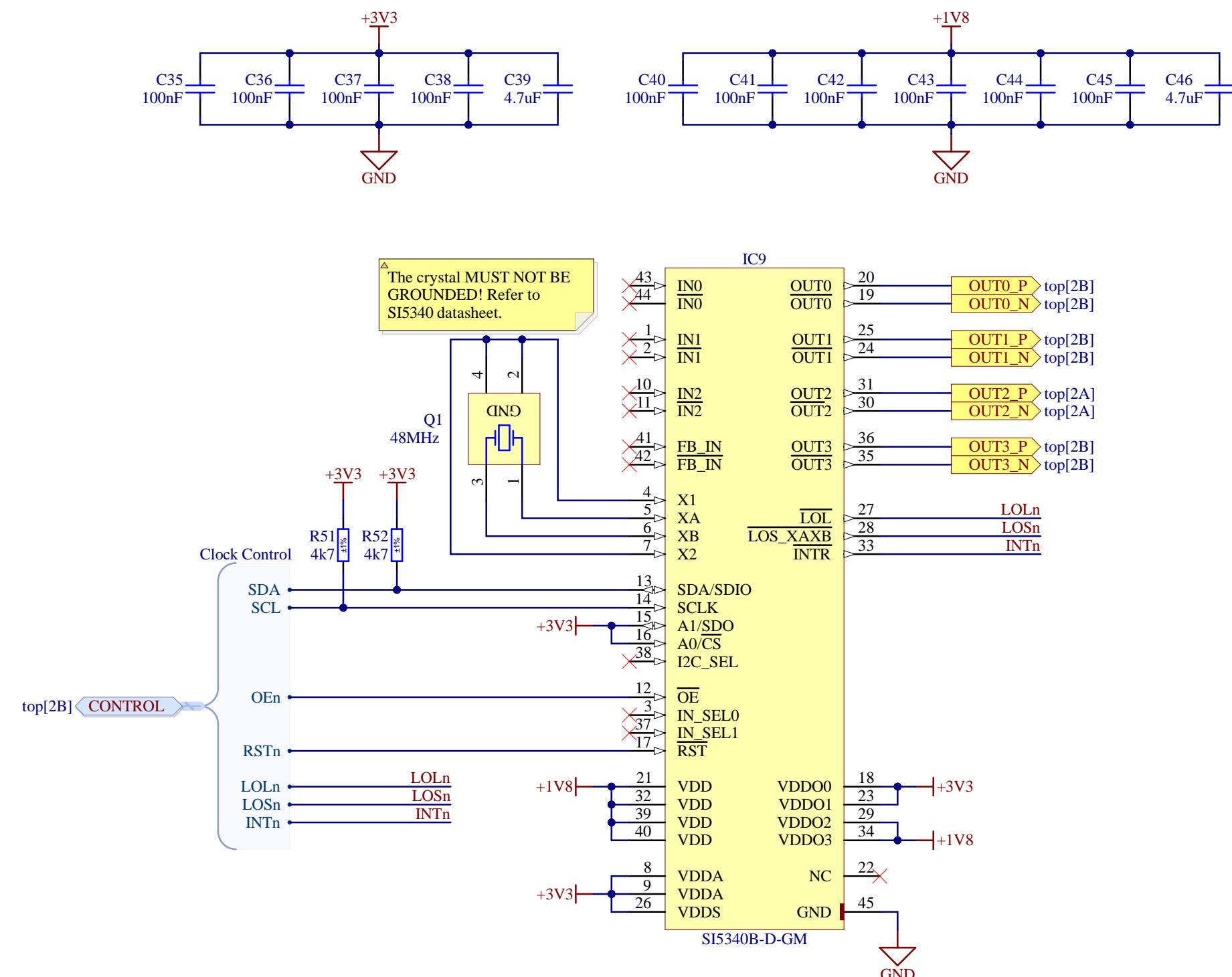


| Project/Equipment | Fast Timing | Designer | Vojtech Vosahl |
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| Document | | Drawn by | Vojtech Vosahl |
| P-ESE | FastIC+ readout | Check by | Jerome A. Alozy |
| | | Last Mod. | 11/13/2024 |
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| Print Date | | 11/15/2024 9:54:38 AM | Sheet 1 of 7 |





| Project/Equipment | Fast Timing | Designer | Vojtech Vosahlo |
|-------------------|------------------------|------------|-----------------------|
| Document | | Drawn by | Vojtech Vosahlo |
| EP- ESE | <i>FastIC+ readout</i> | Check by | Jerome A. Alozy |
| | | Last Mod. | 11/15/2024 |
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A

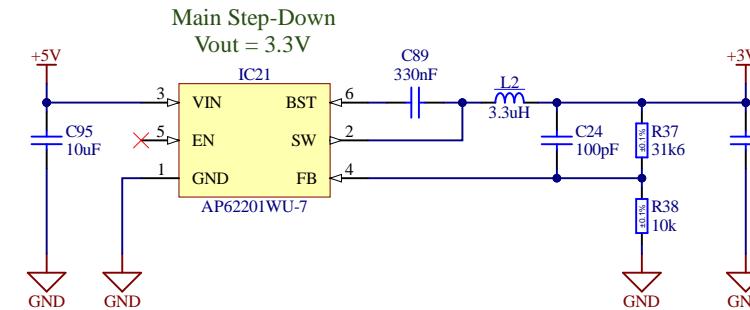
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Power Consumption (Absolute Maximum)
 Pmax(FastICs)=230mW
 Pmax(Clock)=860mW
 Pmax(USB)=100mW
 Pmax(STM32)=1000mW
 Pmax(HV)=160mW

Pmax ~ 2.4W which should fit into the USB 2.0 current specification (2.5W).

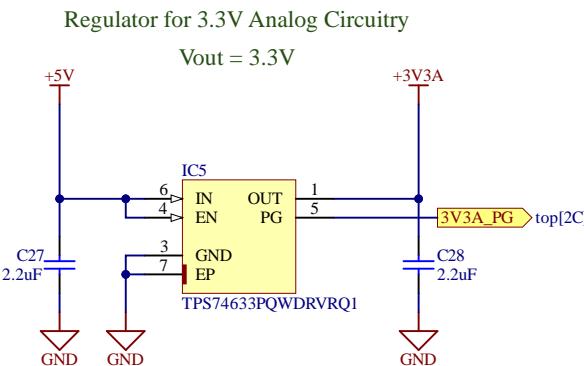
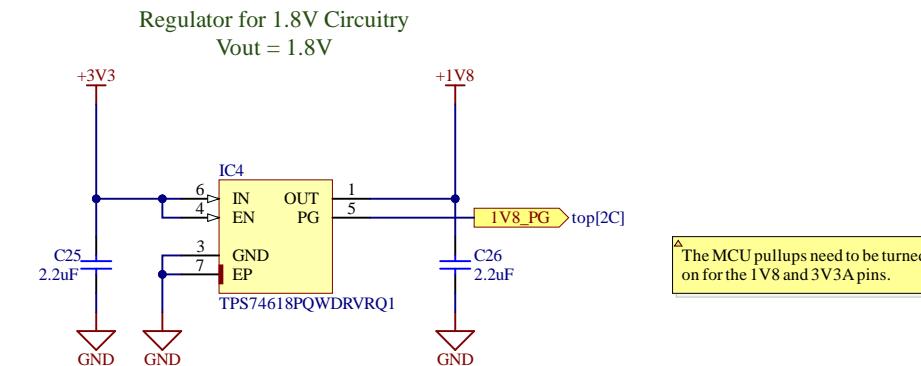
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B



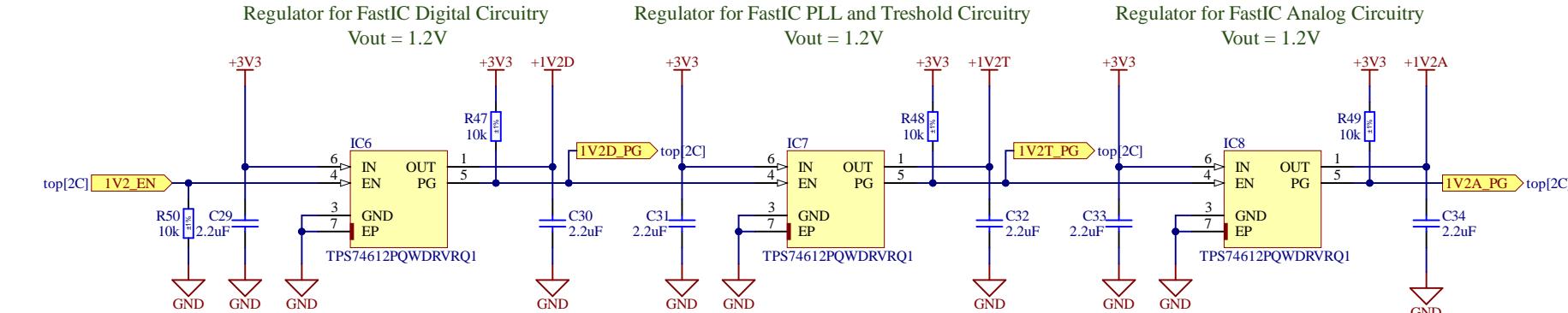
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C



D

D



E

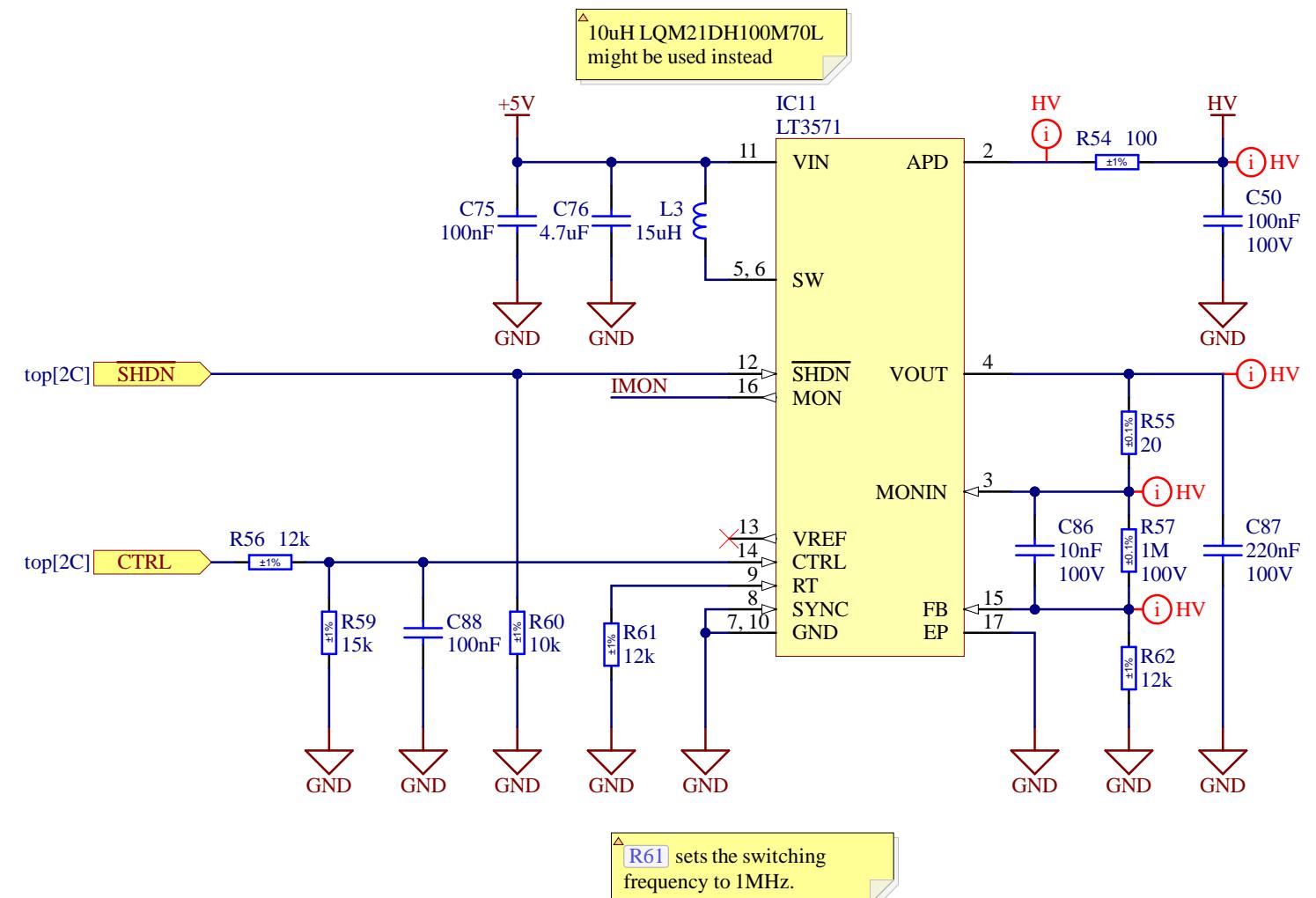
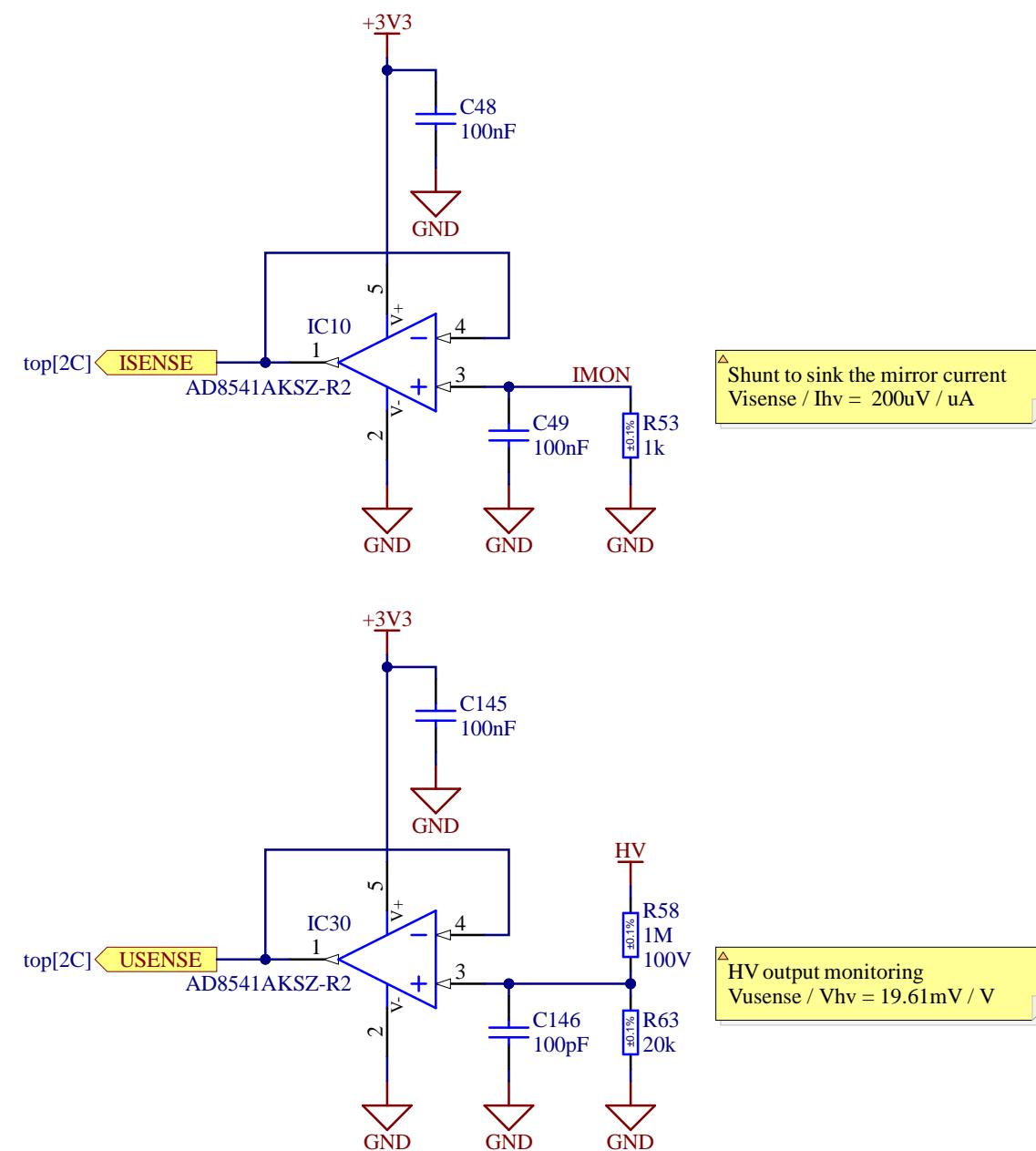
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| Project/Equipment | | Fast Timing | |
|-------------------|--------|-------------|-----------------------|
| Document | EP-ESE | Designer | Vojtech Vosahlo |
| | | Drawn by | Vojtech Vosahlo |
| | | Check by | Jerome A. Alozy |
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| | | Sheet | 4 of 7 |
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| | | Rev | 2.0 |

FastIC+ readout

European Organization for Nuclear Research
CH-1211 Genève 23 - Switzerland

A



Project/Equipment

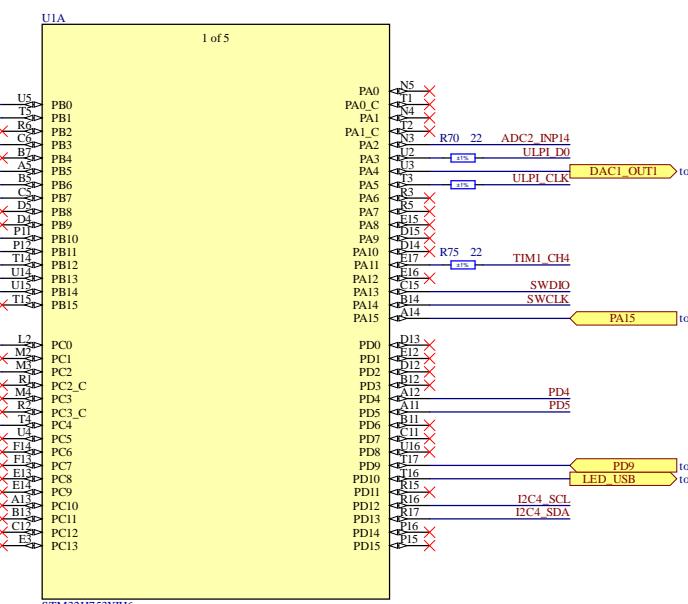
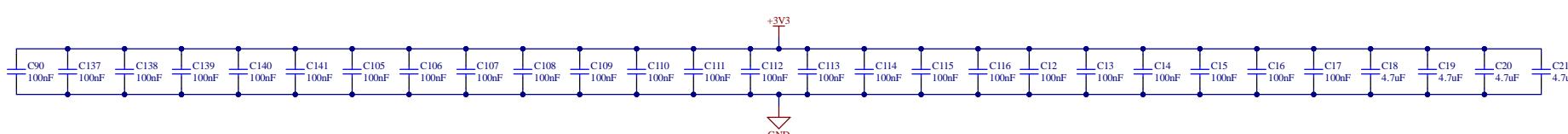
Fast Timing

Document

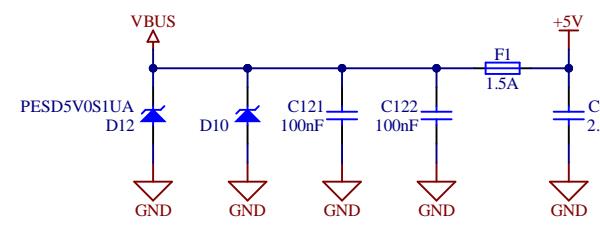
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**FastIC+ readout**

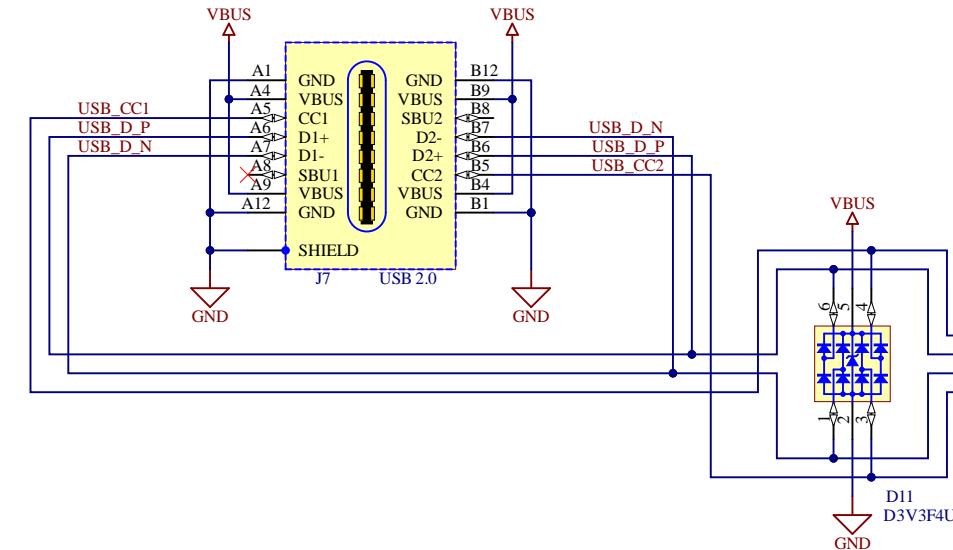
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| Designer | Vojtech Vosahlo | |
| Drawn by | Vojtech Vosahlo | 11/1/2024 |
| Check by | Jerome A. Alozy | |
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| Size | A4 | Rev 2.0 |



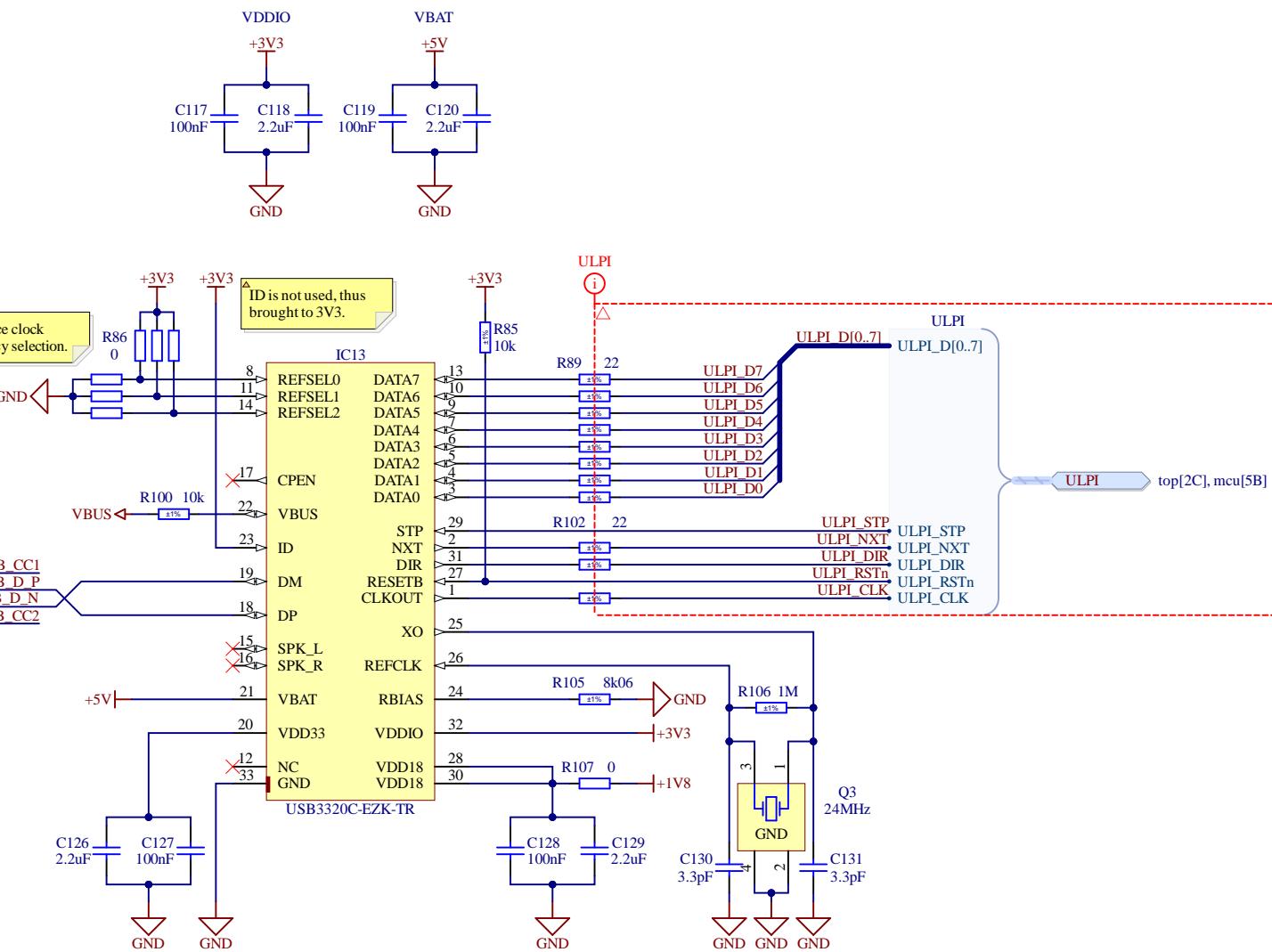
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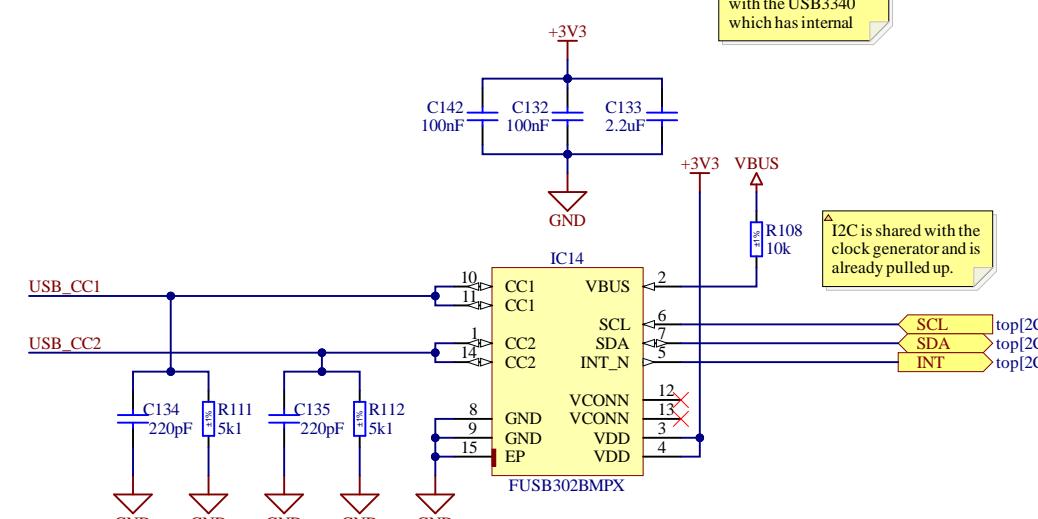
B



C



D



E

| Project/Equipment | | FastIC+ readout | |
|-------------------|--|-----------------|-----------------------|
| Document | | Designer | Vojtech Vosahlo |
| EP-ESE | | Drawn by | Vojtech Vosahlo |
| | | Check by | Jerome A. Alozy |
| | | Last Mod. | 11/15/2024 |
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| | | Sheet | 7 of 7 |
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| | | Rev | 2.0 |