# Homework #5

[ECE30021/ITP30002] Operating Systems

#### Mission



- Implement address translation of paging on the server <u>using</u> <u>vi and gcc</u>.
- Don't use other editor or other compiler

#### Submission

- Submit hw5.c
  - □ Compiled by "gcc hw5.c" command on the server.
- Use filezilla ftp client to download the file to your PC.
  - https://filezilla-project.org/download.php
- Due date: PM 11:00, June. 4<sup>th</sup>

### Honor Code Guidelines

#### "Assignment"

- Assignments are an educational activity necessary to fully understand the lecture, and to apply the materials to practical problems. Students should complete all assignments with honesty and sincerity to develop the knowledge and skills intended in the assignment.
- Submitted assignments are reflected in a grade evaluation. Therefore, student should never commit any kinds of dishonest behavior including acquisition, utilization, request, and providing of unauthorized information or assistance that can disrupt the fairness of evaluation.
- It is allowed to get help from peers in order to understand the lecture materials, announcements, or the directions for homework. However, each student should complete the entire assignments on their own.
- Submitting assignments or program codes written by others or acquired from the internet without explicit approval of the professor is regarded as cheating.
- Showing or lending one's own homework to other student is also considered cheating that disturbs fair evaluation and hinders the academic achievement of the other student.
- It is regarded as cheating if two or more students conduct their homework together and submit it individually when the homework is not a group assignment.
- One may receive help from other students if a problem in the assignment could not be solved even with the best effort; however, the assistance should never go beyond the correction of basic grammatical errors. It is cheating to get help about design, logic, and algorithm required in assignments from other person than the professor, TA, and tutor.
- It can be suspected or regarded as cheating if the similarity between assignments submitted by different students is beyond an acceptable degree that can be considered as a coincidence or when the student is not able to explain in detail about their homework.

## **Honor Code Guidelines**

#### ■ "과제"

- 과제는 교과과정의 내용을 소화하여 실질적인 활용 능력을 갖추기 위한 교육활동이다. 학생은 모든 과제를 정직하고 성실하게 수행함으로써 과제에 의도된 지식과 기술을 얻기 위해 최선을 다해야 한다.
- 제출된 과제물은 성적 평가에 반영되므로 공식적으로 허용되지 않은 자료나 도움을 획득, 활용, 요구, 제공하는 것을 포함하여 평가의 공정성에 영향을 미치는 모든 형태의 부정행위 는 단호히 거부해야 한다.
- 수업 내용, 공지된 지식 및 정보, 또는 과제의 요구를 이해하기 위하여 동료의 도움을 받는 것은 부정행위에 포함되지 않는다. 그러나, 과제를 해결하기 위한 모든 과정은 반드시 스스 로의 힘으로 수행해야 한다.
- 담당교수가 명시적으로 허락한 경우를 제외하고 다른 사람이 작성하였거나 인터넷 등에서 획득한 과제물, 또는 프로그램 코드의 일부, 또는 전체를 이용하는 것은 부정행위에 해당한 다.
- 자신의 과제물을 타인에게 보여주거나 빌려주는 것은 공정한 평가를 방해하고, 해당 학생의 학업 성취를 저해하는 부정행위에 해당한다.
- 팀 과제가 아닌 경우 두 명 이상이 함께 과제를 수행하여 이를 개별적으로 제출하는 것은 부 정행위에 해당한다.
- 스스로 많은 노력을 한 후에도 버그나 문제점을 파악하지 못하여 동료의 도움을 받는 경우도 단순한 문법적 오류에 그쳐야 한다. 과제가 요구하는 design, logic, algorithm의 작성에 있어서 담당교수, TA, tutor 이외에 다른 사람의 도움을 받는 것은 부정행위에 해당한다.
- 서로 다른 학생이 제출한 제출물간 유사도가 통상적으로 발생할 수 있는 정도를 크게 넘어서는 경우, 또는 자신이 제출한 과제물에 대하여 구체적인 설명을 하지 못하는 경우에는 부정행위로 의심받거나 판정될 수 있다.

### Problem 0

#### Search and study C/C++ bit-wise operators

- Bitwise and/ or operators: & and |
- Bitwise shift operators: >> and <<</p>
- Bitwise composite operators: &=, |=, >>=, <<=

#### Don't miss followings

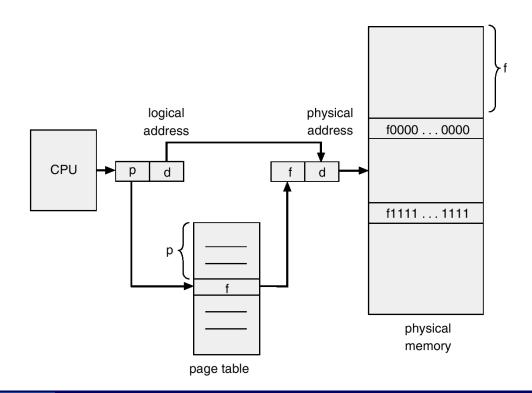
- 'x << k' is equivalent to 'x \* 2k'</p>
- 'x << 1' is equivalent to 'x \* 2'
- 2<sup>k</sup> is computed by '1 << k'
- It is recommended to parenthesize bit-wise operations, because they have low precedence.

## Simulate Paging System

- You have a 16-bit computer. The logical address is composed of a page number (6 bit) and a page offset (10 bits)
- Write the following questions and their answers in the source file (hw5.c) as comments. Write their answers and how they were computed.
  - What is the maximum number of page table entries?
  - What is the size of a page (in bytes)?
  - How many pages are necessary to store a process whose size is 24800 bytes?

## Paging System

Implement address translation of paging system.



#### Global Declarations

Declare the followings globally.

```
#define BitsForPageNo 6
#define BitsForPageOffset 10
#define ProcessSize 24800
void init_page_table(int table[], int no_page);
char* to_binary(unsigned short addr);
unsigned short logical2physical(
                 unsigned short logical_addr, int page_table[]);
```

### **Functions**

- void init\_page\_table(int table[], int no\_page);
  - □ Initialize page table entries 60-i\*2
  - □ For simple experiment, set table[i] by (i \* 2 + 3) for all i.
- char\* to\_binary(unsigned short addr);
  - Return a string containing binary representation of addr.
  - Provided
- unsigned short logical2physical(

unsigned short logical\_addr, int page\_table[]);

- Given a page table, translates a logical address into the corresponding physical address.
- Returns the physical address.

## Page Table (table[i] = 60 - i \* 2)

```
page_table[0, 0x0] = 60
page_table[1, 0x1] = 58
page_table[2, 0x2] = 56
page_table[3, 0x3] = 54
page_table[4, 0x4] = 52
page_table[5, 0x51 = 50
page_table[6, 0x6] = 48
page_table[7, 0x7] = 46
page_table[8, 0x8] = 44
page_table[9, 0x9] = 42
page_table[10, 0xa] = 40
page_table[11, 0xb] = 38
page_table[12, 0xc] = 36
```

```
page_table[13, 0xd] = 34
page_table[14, 0xe] = 32
page_table[15, 0xf] = 30
page_table[16, 0x10] = 28
page_table[17, 0x11] = 26
page_table[18, 0x12] = 24
page_table[19, 0x13] = 22
page_table[20, 0x14] = 20
page_table[21, 0x15] = 18
page_table[22, 0x16] = 16
page_table[23, 0x171 = 14
page_table[24, 0x18] = 12
```

## to\_binary() function

```
char* to_binary(unsigned short addr)
   static char bits[17]; // why 'static' is necessary?
    int i = 0;
    for(i = 0; i < 16; i++){
            bits[15 - i] = '0' + (addr & 1);
            addr >>= 1;
    bits[16] = 0;
    return bits;
```

## logical2physical() function

# Guideline

- unsigned short logical2physical(unsigned short logical\_addr, int page\_table[])
  - Retrieve page number from logical\_addr
    - □ Use >> operator
  - □ Retrieve page offset from logical\_addr
    - Use & operator
  - Convert page number into frame number using page\_table
  - Combine frame number and page offset to make physical address
    - □ Use << operator and | operator</p>
  - Return physical address

## main() function

```
int main()
  int no page = 0;
  int *page_table = NULL;
  // logical addresses to test case logical2physical() - can be changed
  unsigned short logical_addr[] = { 0x21dd, 0x0829, 0x08f0, 0x0ad1, 0x071e, 0x0431, 0x0fb8, 0x0b18, 0x38ab, 0x07a0 };
  int i = 0:
  // print maximum # of pages and page size
  no_page = ·····; // compute # of pages to store ProcessSize (=24800) bytes
  page_table = (int*)malloc(no_page * sizeof(int));
  if(page_table == NULL){
     printf("Failed to allocate memory!₩n");
     exit(-1);
  init_page_table(page_table, no_page);
  for(i = 0; i < 10; i++)
       unsigned short physical_addr = logical2physical(logical_addr[i], page_table);
       printf("0x%04x (%s) ", logical_addr[i], to_binary(logical_addr[i]));
       printf("--> 0x%04x (%s)\mathcal{w}n", physical_addr, to_binary(physical_addr));
  free(page_table);
  return 0;
```

## **Expected Result**

```
0x21dd (0010000111011101) --> 0xb1dd (1011000111011101)
0 \times 0829 \quad (0000100000101001) \quad --> 0 \times e029 \quad (1110000000101001)
0x08f0 (0000100011110000) --> 0xe0f0 (1110000011110000)
0x0ad1 (0000101011010001) --> 0xe2d1 (1110001011010001)
0x071e (00000111100011110) --> 0xeb1e (1110101100011110)
0x0431 (0000010000110001) --> 0xe831 (1110100000110001)
0x0fb8 (00001111110111000) --> 0xdbb8 (1101101110111000)
0x0b18 (0000101100011000) --> 0xe318 (1110001100011000)
0x38ab (0011100010101011) --> 0x80ab (1000000010101011)
0x07a0 (00000111110100000) --> 0xeba0 (1110101110100000)
```