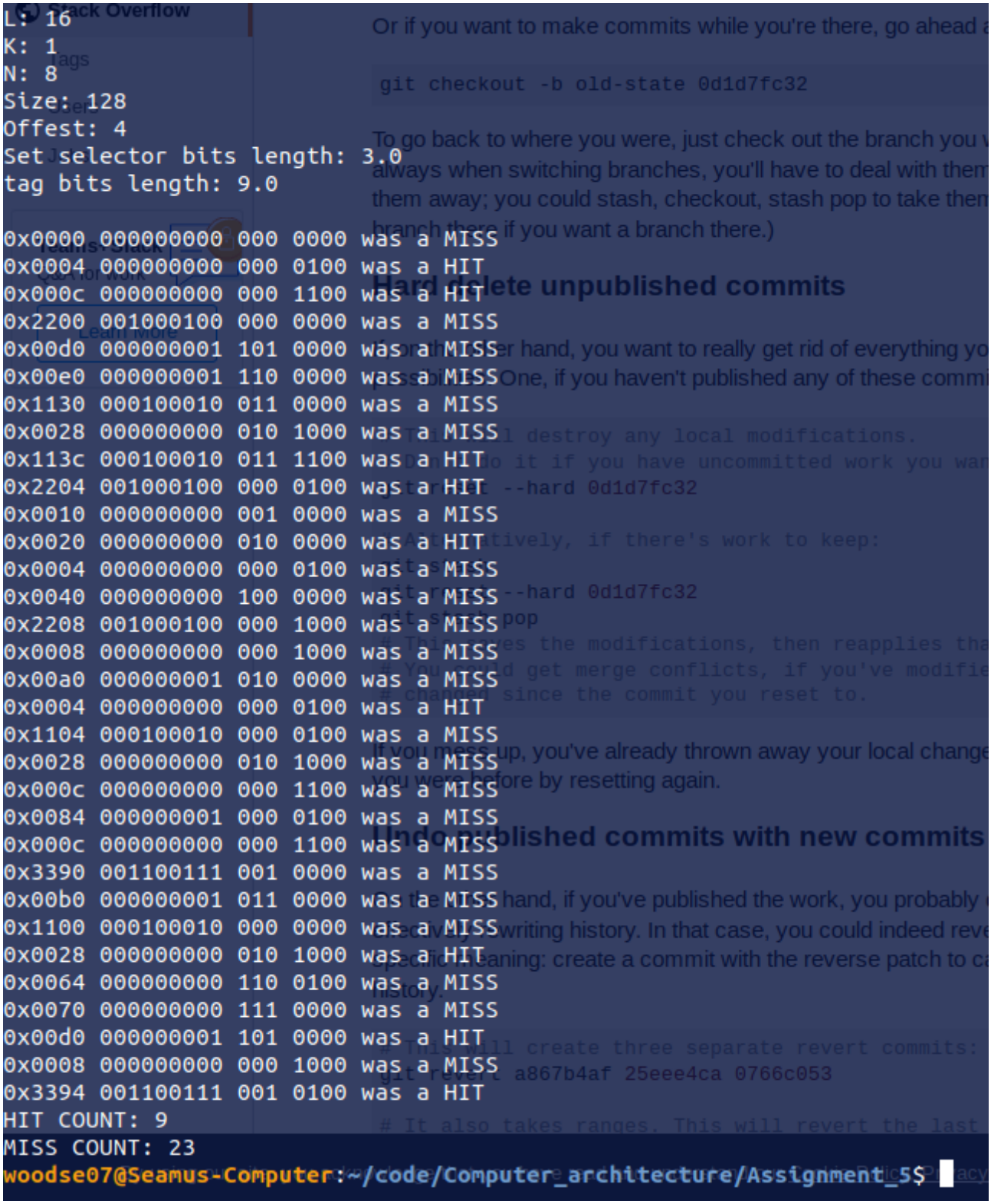
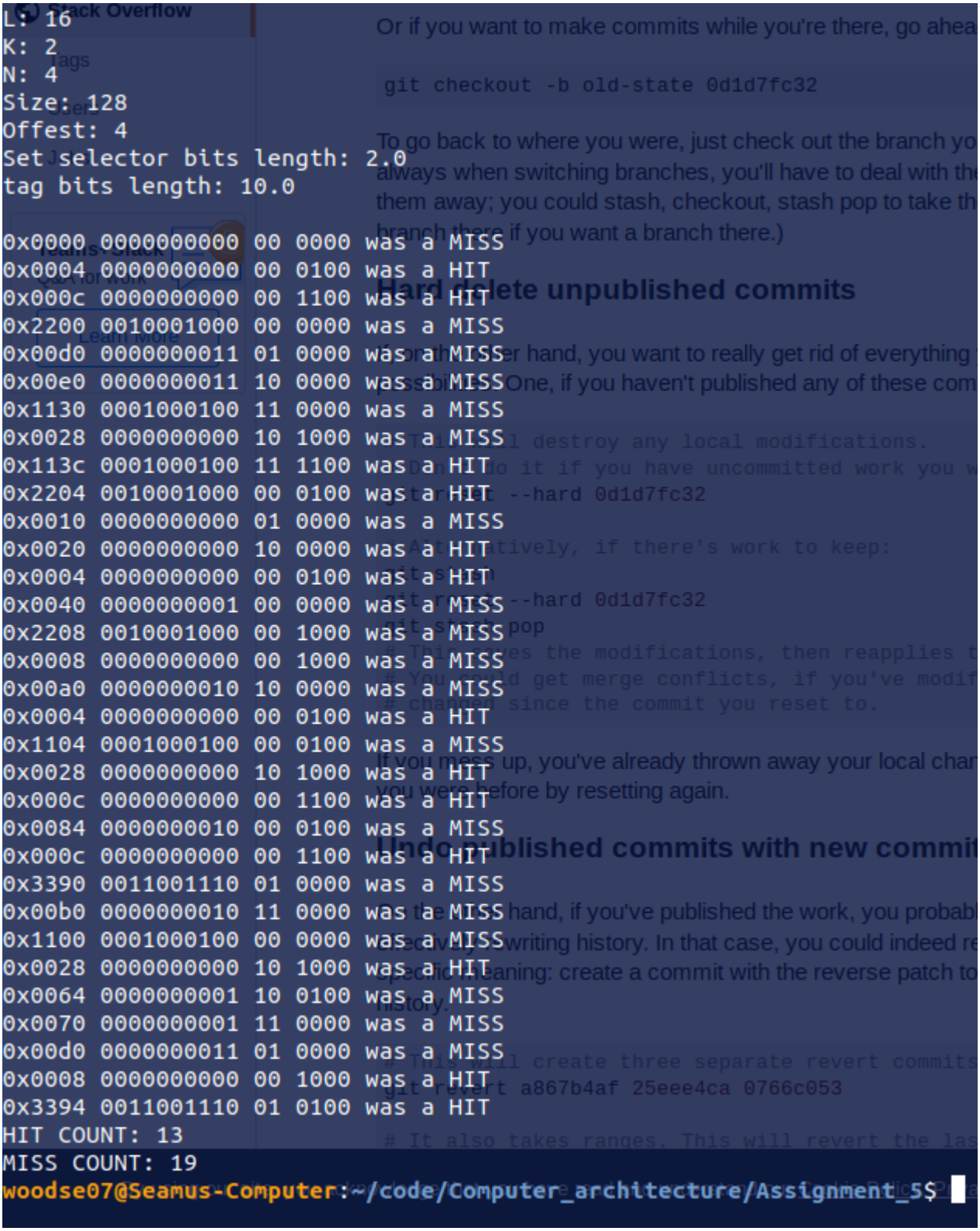
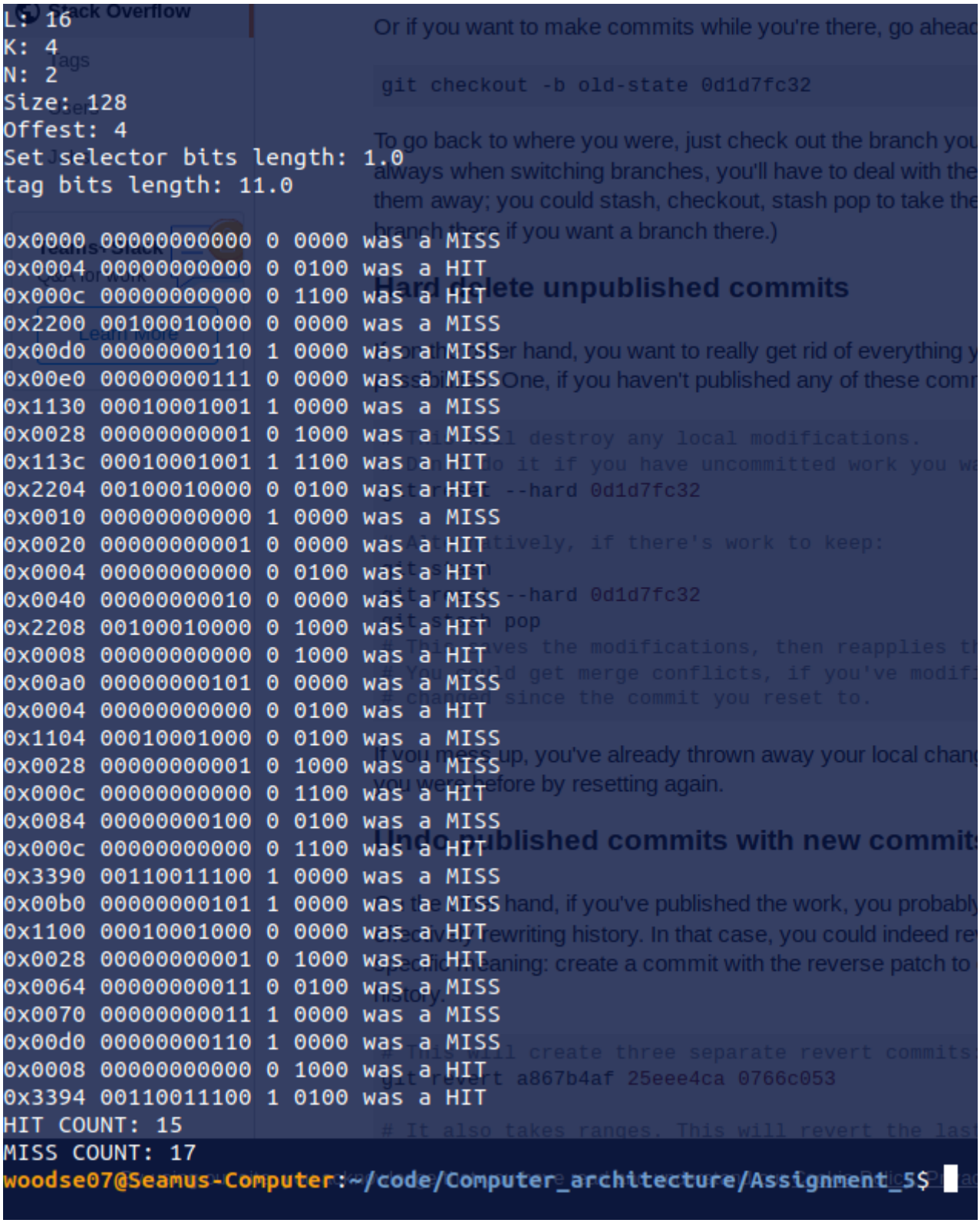
Part (i)


L: 16
K: 1
N: 8
Size: 128
Offest: 4
Set selector bits length: 3.0
tag bits length: 9.0
0x0000 000000000 000 0000 was a MISS
0x0004 000000000 000 0100 was a HIT
0x000c 000000000 000 1100 was a HIT
0x2200 001000100 000 0000 was a MISS
0x00d0 000000001 101 0000 was a MISS
0x00e0 000000001 110 0000 was a MISS
0x1130 000100010 011 0000 was a MISS
0x0028 000000000 010 1000 was a MISS
0x113c 000100010 011 1100 was a HIT
0x2204 001000100 000 0100 was a HIT
0x0010 000000000 001 0000 was a MISS
0x0020 000000000 010 0000 was a HIT
0x0004 000000000 000 0100 was a MISS
0x0040 000000000 100 0000 was a MISS
0x2208 001000100 000 1000 was a MISS
0x0008 000000000 000 1000 was a MISS
0x00a0 000000001 010 0000 was a MISS
0x0004 000000000 000 0100 was a HIT
0x1104 000100010 000 0100 was a MISS
0x0028 000000000 010 1000 was a MISS
0x000c 000000000 000 1100 was a MISS
0x0084 000000001 000 0100 was a MISS
0x000c 000000000 000 1100 was a MISS
0x3390 001100111 001 0000 was a MISS
0x00b0 000000001 011 0000 was a MISS
0x1100 000100010 000 0000 was a MISS
0x0028 000000000 010 1000 was a HIT
0x0064 000000000 110 0100 was a MISS
0x0070 000000000 111 0000 was a MISS
0x00d0 000000001 101 0000 was a HIT
0x0008 000000000 000 1000 was a MISS
0x3394 001100111 001 0100 was a HIT
HIT COUNT: 9
MISS COUNT: 23
woodse07@Seamus-Computer:~/code/Computer_architecture/Assignment_5$

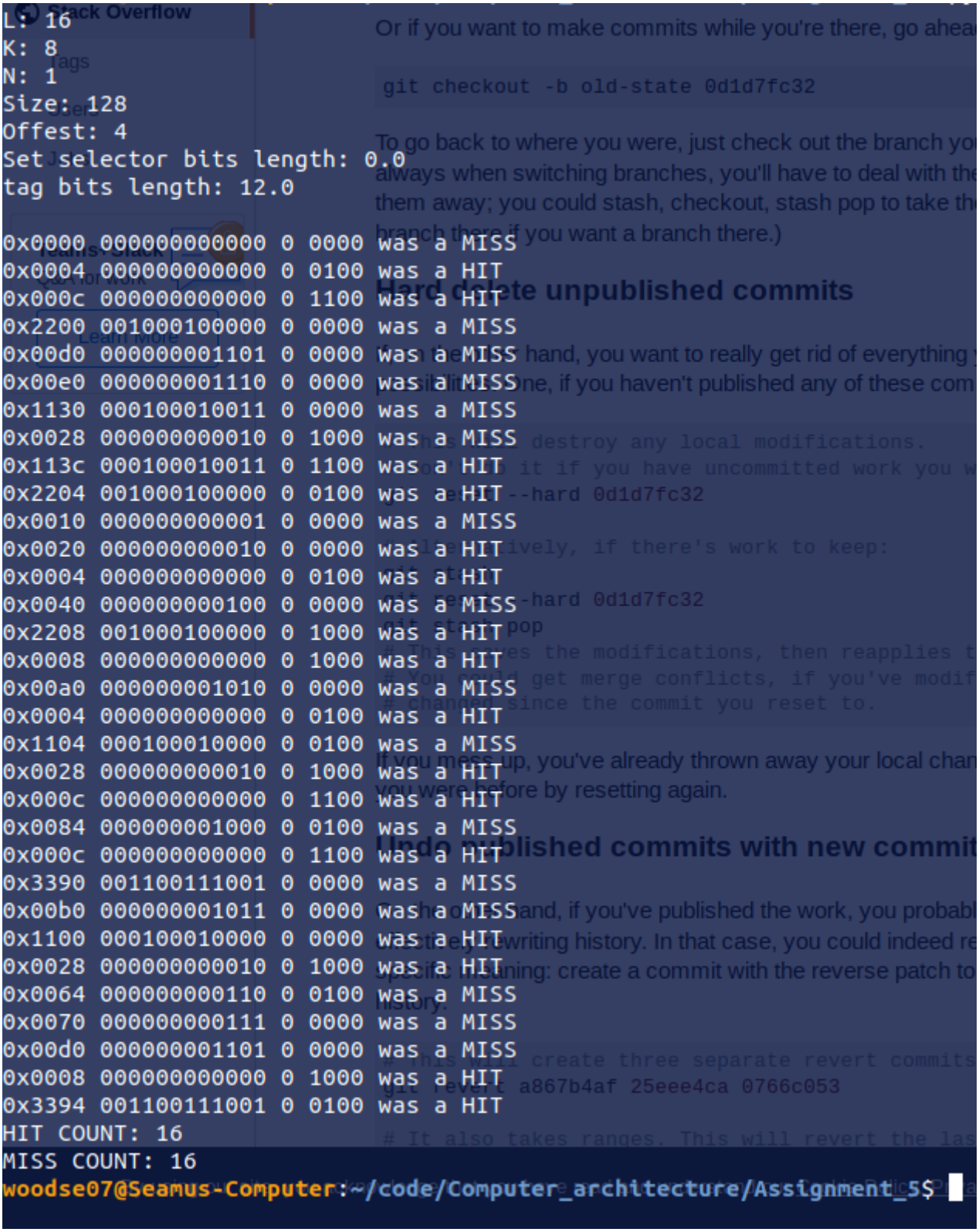
Part (ii)


L: 16
K: 2
N: 4
Size: 128
Offest: 4
Set selector bits length: 2.0
tag bits length: 10.0
0x0000 0000000000 00 0000 was a MISS
0x0004 0000000000 00 0100 was a HIT
0x000c 0000000000 00 1100 was a HIT
0x2200 0010001000 00 0000 was a MISS
0x00d0 0000000011 01 0000 was a MISS
0x00e0 0000000011 10 0000 was a MISS
0x1130 0001000100 11 0000 was a MISS
0x0028 0000000000 10 1000 was a MISS
0x113c 0001000100 11 1100 was a HIT
0x2204 0010001000 00 0100 was a HIT
0x0010 0000000000 01 0000 was a MISS
0x0020 0000000000 10 0000 was a HIT
0x0004 0000000000 00 0100 was a HIT
0x0040 0000000001 00 0000 was a MISS
0x2208 0010001000 00 1000 was a MISS
0x0008 0000000000 00 1000 was a MISS
0x00a0 0000000010 10 0000 was a MISS
0x0004 0000000000 00 0100 was a HIT
0x1104 0001000100 00 0100 was a MISS
0x0028 0000000000 10 1000 was a HIT
0x000c 0000000000 00 1100 was a HIT
0x0084 0000000010 00 0100 was a MISS
0x000c 0000000000 00 1100 was a HIT
0x3390 0011001110 01 0000 was a MISS
0x00b0 0000000010 11 0000 was a MISS
0x1100 0001000100 00 0000 was a MISS
0x0028 0000000000 10 1000 was a HIT
0x0064 0000000001 10 0100 was a MISS
0x0070 0000000001 11 0000 was a MISS
0x00d0 0000000011 01 0000 was a MISS
0x0008 0000000000 00 1000 was a HIT
0x3394 0011001110 01 0100 was a HIT
HIT COUNT: 13
MISS COUNT: 19
woodse07@Seamus-Computer:~/code/Computer_architecture/Assignment_5$

Part (iii)


L: 16
K: 4
N: 2
Size: 128
Offest: 4
Set selector bits length: 1.0
tag bits length: 11.0
0x0000 00000000000 0 0000 was a MISS
0x0004 00000000000 0 0100 was a HIT
0x000c 00000000000 0 1100 was a HIT
0x2200 00100010000 0 0000 was a MISS
0x00d0 00000000110 1 0000 was a MISS
0x00e0 00000000111 0 0000 was a MISS
0x1130 00010001001 1 0000 was a MISS
0x0028 00000000001 0 1000 was a MISS
0x113c 00010001001 1 1100 was a HIT
0x2204 00100010000 0 0100 was a HIT
0x0010 00000000000 1 0000 was a MISS
0x0020 00000000001 0 0000 was a HIT
0x0004 00000000000 0 0100 was a HIT
0x0040 00000000010 0 0000 was a MISS
0x2208 00100010000 0 1000 was a HIT
0x0008 00000000000 0 1000 was a HIT
0x00a0 00000000101 0 0000 was a MISS
0x0004 00000000000 0 0100 was a HIT
0x1104 00010001000 0 0100 was a MISS
0x0028 00000000001 0 1000 was a MISS
0x000c 00000000000 0 1100 was a HIT
0x0084 00000000100 0 0100 was a MISS
0x000c 00000000000 0 1100 was a HIT
0x3390 00110011100 1 0000 was a MISS
0x00b0 00000000101 1 0000 was a MISS
0x1100 00010001000 0 0000 was a HIT
0x0028 00000000001 0 1000 was a HIT
0x0064 00000000011 0 0100 was a MISS
0x0070 00000000011 1 0000 was a MISS
0x00d0 00000000110 1 0000 was a MISS
0x0008 00000000000 0 1000 was a HIT
0x3394 00110011100 1 0100 was a HIT
HIT COUNT: 15
MISS COUNT: 17
woodse07@Seamus-Computer:~/code/Computer_architecture/Assignment_5$

Part (iv)


L: 16
K: 8
N: 1
Size: 128
Offest: 4
Set selector bits length: 0.0
tag bits length: 12.0
0x0000 000000000000 0 0000 was a MISS
0x0004 000000000000 0 0100 was a HIT
0x000c 000000000000 0 1100 was a HIT
0x2200 001000100000 0 0000 was a MISS
0x00d0 000000001101 0 0000 was a MISS
0x00e0 000000001110 0 0000 was a MISS
0x1130 000100010011 0 0000 was a MISS
0x0028 000000000010 0 1000 was a MISS
0x113c 000100010011 0 1100 was a HIT
0x2204 001000100000 0 0100 was a HIT
0x0010 000000000001 0 0000 was a MISS
0x0020 000000000010 0 0000 was a HIT
0x0004 000000000000 0 0100 was a HIT
0x0040 000000000100 0 0000 was a MISS
0x2208 001000100000 0 1000 was a HIT
0x0008 000000000000 0 1000 was a HIT
0x00a0 000000001010 0 0000 was a MISS
0x0004 000000000000 0 0100 was a HIT
0x1104 000100010000 0 0100 was a MISS
0x0028 000000000010 0 1000 was a HIT
0x000c 000000000000 0 1100 was a HIT
0x0084 000000001000 0 0100 was a MISS
0x000c 000000000000 0 1100 was a HIT
0x3390 001100111001 0 0000 was a MISS
0x00b0 000000001011 0 0000 was a MISS
0x1100 000100010000 0 0000 was a HIT
0x0028 000000000010 0 1000 was a HIT
0x0064 000000000110 0 0100 was a MISS
0x0070 000000000111 0 0000 was a MISS
0x00d0 000000001101 0 0000 was a MISS
0x0008 000000000000 0 1000 was a HIT
0x3394 001100111001 0 0100 was a HIT
HIT COUNT: 16
MISS COUNT: 16
woodse07@Seamus-Computer:~/code/Computer_architecture/Assignment_5$

Question 3 output: