DATE BY Be sure this is the latest version! 2/26/14 PR Initial release T1 C19: Fix reference text. S1-S7: C19: Fix reference text. 51–57: Fix pin map. U4: Move pin 1 to 3V rall. C18: Place next to U4. D5: fix 4 & 26 reversed. 04/ D5: fix interference. R16: Chg. 120 was 10X. ADD: Logo, radius corners. U4: Fix footprint. ADD: Batt. light [D52], & layer stack up drawing. MISC: Fix drill hole sizes. 4.425 NOTE 5c $A \leftarrow$ SEC A-A .85 (not to scale) r=0.625 ⊤typ. 6 pl. DISPLAY UNIT DISPLAY 0.710 NOTE 5a .540 NOTE 4 .375 o 0.565 0.69 0.625 typ 1.860 LAYER STACK UP DETAIL 2.61 NOTES: 3.535 SMD COMPONENT Side 1: DESCRIPTION: TOP SILK [thotcon_0x5-F_SilkS.gto] .062" (nominal) Etched Circuit board, 2 side 1 oz (finished) Cu, for hybrid SMT/through lead. Minimum 8/8 SMT design rules. Not for lead free assembly. TOP SOLDER MASK [thotcon_0x5-F_Mask.gts] TOP COPPER [thotcon_0x5-F_copper.gtl] + CORE/DIELECTRIC Refer to 00-PCB-README file for solder mask & silkscreen colors and quantities. BOTTOM COPPER [thotcon_0x5-B_copper.gbl] BOTTOM SOLDER MASK [thotcon_0x5-B_mask.gbs] 3. Dimensions: BOTTOM SILK [thotcon_0x5-B_SilkS.gbo] Finish dimensions per drawing. +/-.005" (.000 decimal), +/-.015" (.00 decimal), +/-.003" on holes, 1/64" on fractions, unless noted. Undimensioned holes to be positioned per artwork. THROUGH-HOLE COMPONENT Side 4. Co-ordinate System: SEE ALSO thotcon_0x5-F_Paste.gtp (top paste), thotcon_0x5-B_Paste.gbp (bottom paste) Drill file and SMT centroid 0, 0 origin (reference point) located per drawing. LICENSE: CC-BY-SA-3.0 5. ASSEMBLY (refer also to 00-ASM-README file). Project Mgr: R. Ristich BOARD, PC, THOTCON 0x5 DS2 mounts "lens down" on SMD side. Verify presence of hole, and fit of DS2 prior to assembly. Place RED pushbuttons at S1 and S2 ONLY. Place BLACK pushbuttons at S3—S7. Fixture display to maintain display — PCB clearance per drawing. Designed: Paul Reich Drawn: Paul Reich Workshop 88 Orient battery holders per legend File: thotcon_0x5.brd

Sheet:

Size: A

KiCad E.D.A.

Title: BOARD, PC, THOTKON 0X5

Date: 11 apr 2014

REVISIONS

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