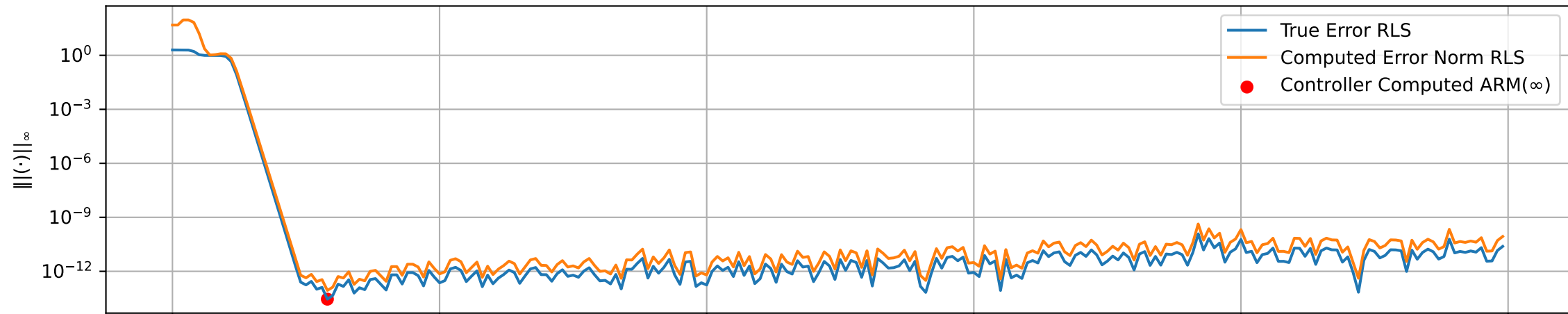


### RLS: True Error and Computed Error



### ARM( $\infty$ ): True Error and Computed Error

