

08/November/2023

To Whom It May Concern,

I am Mahesh Maloo, Technical Manager at Tech Mahindra Cerium Systems Pvt Ltd. I am writing to provide a professional reference for Mr. Bhuvanesh S Kayarabettu, who is a skilled and dedicated engineer, has been an integral part of our team at TechM Cerium Systems. It has come to my attention that Mr. Bhuvanesh S Kayarabettu intends to pursue his master's degree at your esteemed university and thereby I recommend him for the same.

Bhuvanesh joined Cerium Systems as a full-time Engineer in December 2020. In his first project assigned to him by TechM Cerium Systems at Intel, Bangalore. Bhuvanesh contributed to the hardware verification of Intel's proprietary IP, which is a high-speed DAC and ADC IPs used on Intel/Altera FPGAs.

Next Bhuvanesh continued in Intel, Bangalore. During his second Intel project, He worked on two of Intel's proprietary communication IPs, lasting a year. In this role, his responsibilities were on RTL Design of a subIP within the larger communication IPs, RTL Lint & CDC analysis, Code-Coverage analysis, and Timing analysis.

Currently, Bhuvanesh is working as a Client RTL Design Engineer at Qualcomm, Bangalore. He is working on the Neural Signal Processing IP used in Qualcomm SoCs. His responsibilities at Qualcomm encompass various levels, including RTL Design, Lint, CDC, UNR, and timing analysis.

Throughout his tenure here, Bhuvanesh has displayed a strong commitment to meeting project goals and deadlines. He excels as a team player, collaborating effectively with colleagues and stakeholders to drive project success.

Sincerely,



Mahesh Maloo
Technical Manager
TechM Cerium Systems
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