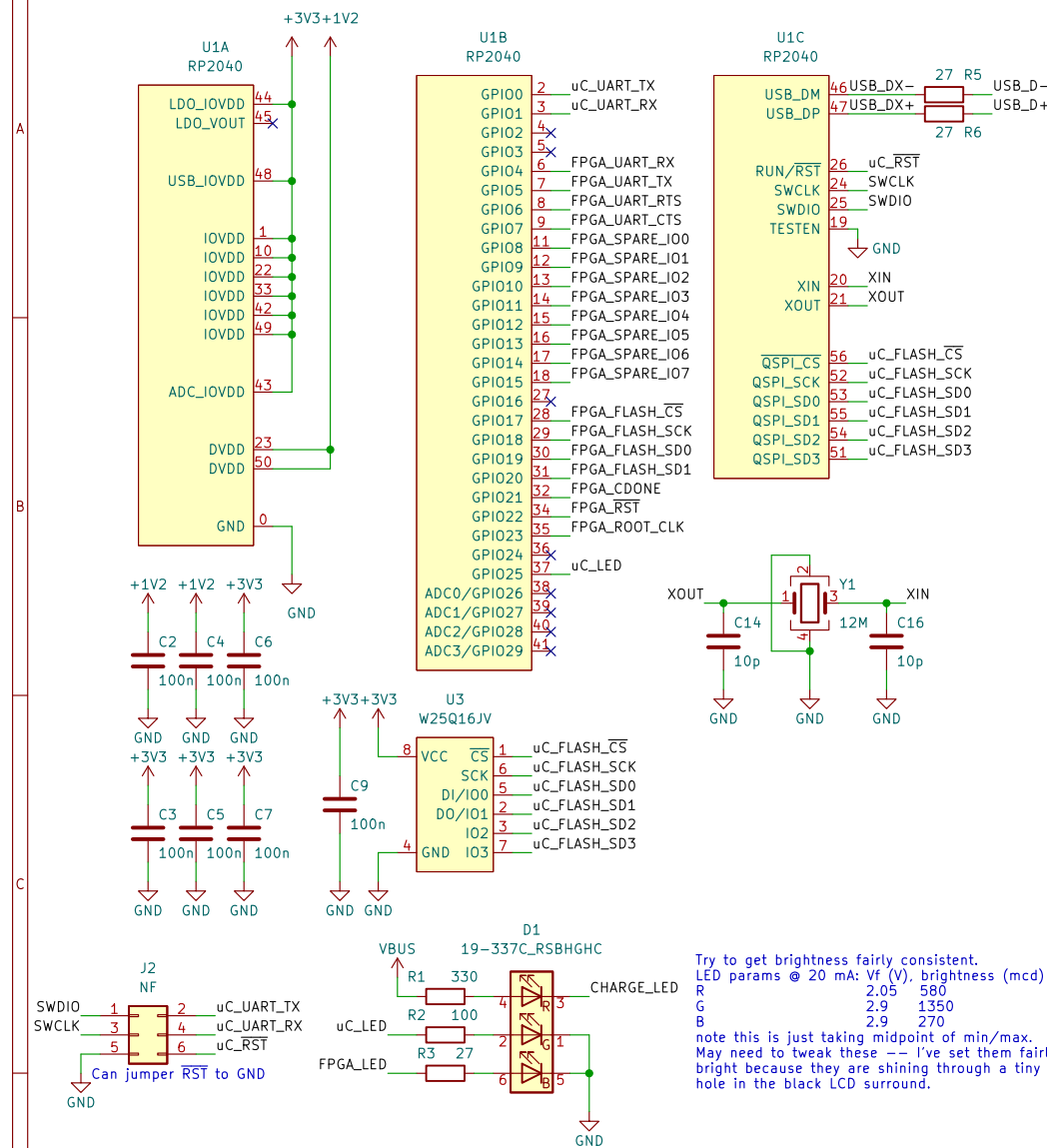
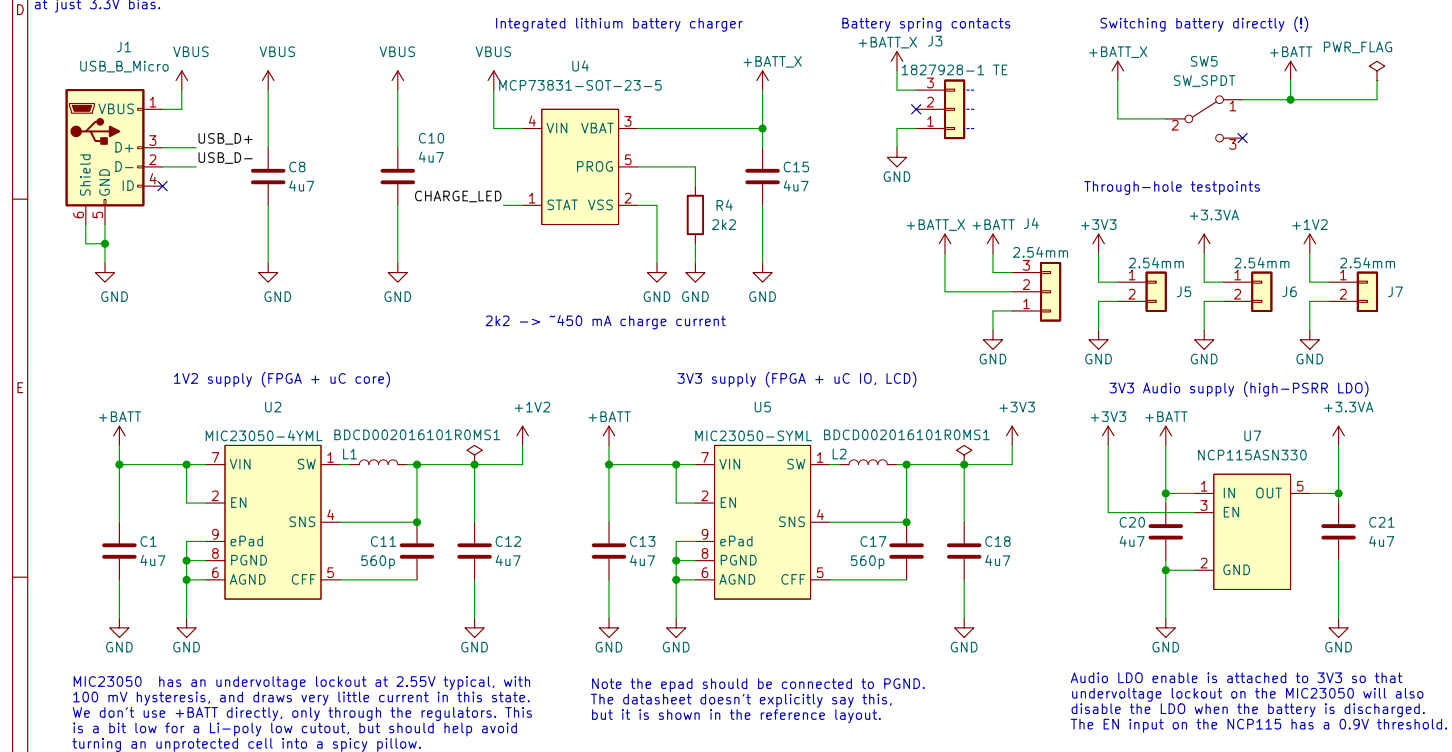


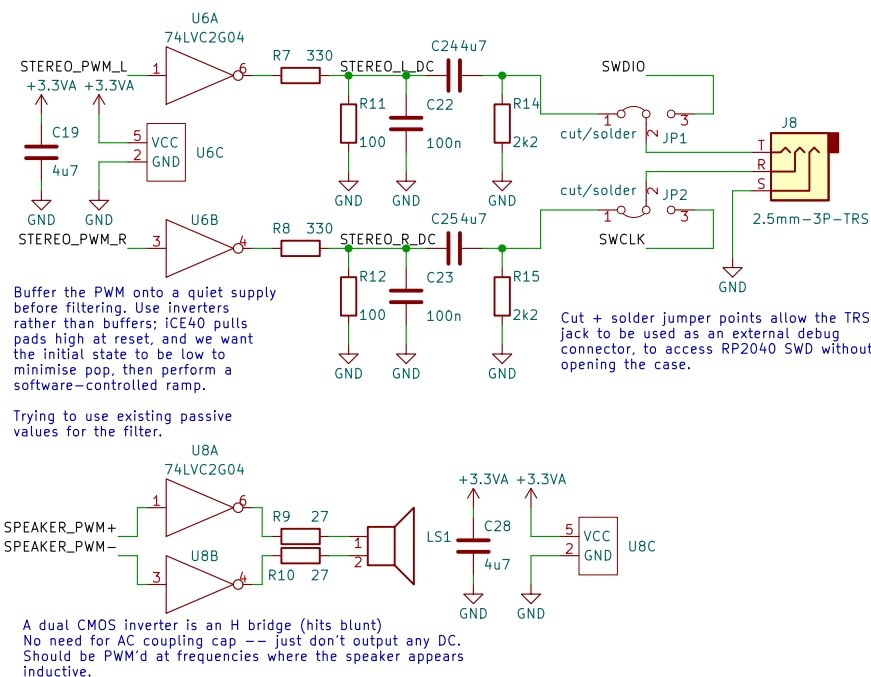
Microcontroller



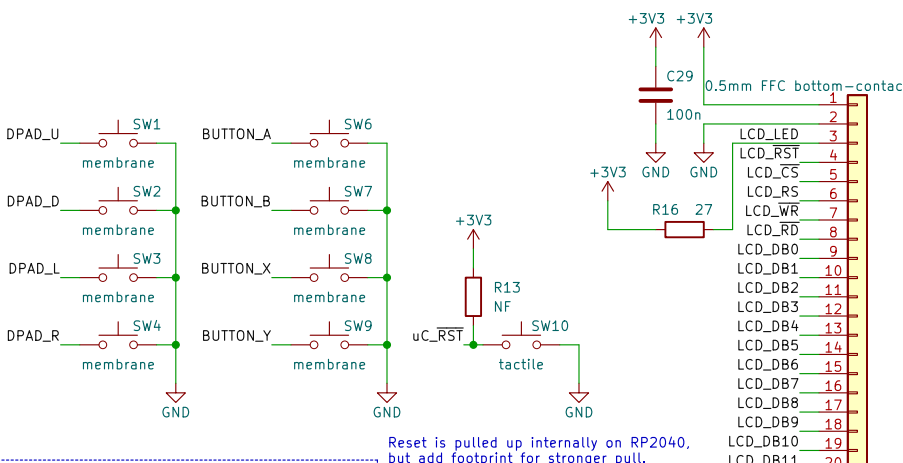
Power Supply and Battery Management



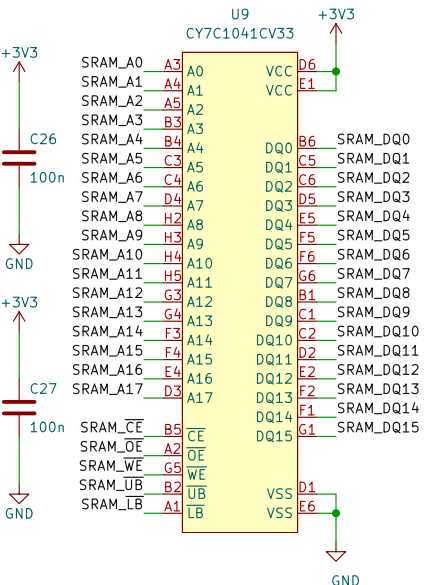
Audio



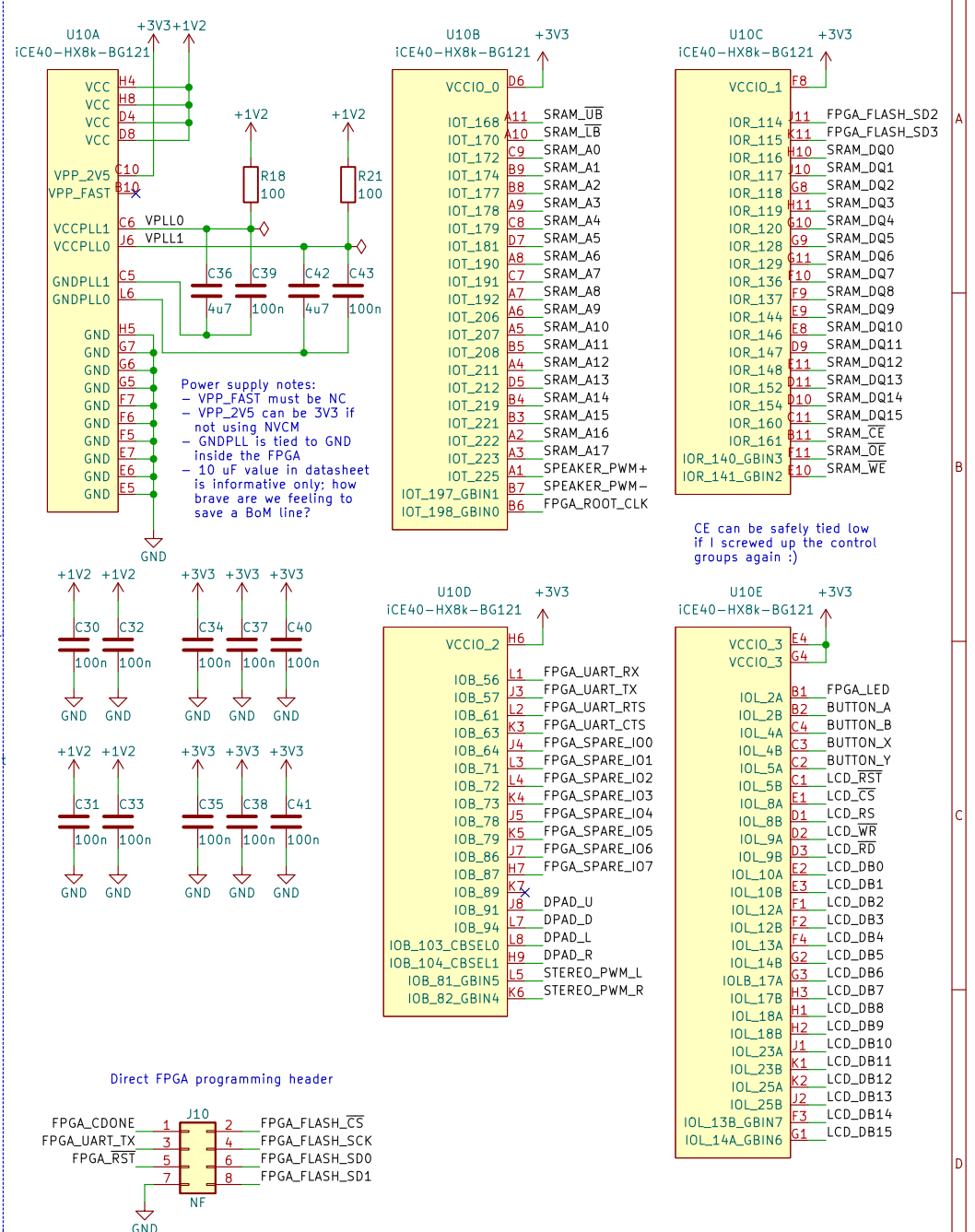
LCD and Face Buttons



SRAM for FPGA (4Mbit)



FPGA



I'm not convinced that all of these pullups are necessary, but they are all recommended by Lattice. Some of them can probably be marked no-fit for the next board revision, especially SCK which will drain a lot of current.

No need for external pullups on SD2/SD3 (WPn/HOLDn) because the FPGA asserts pad pullups when reset.

Luke Wren

Sheet: /
File: riscboy.kicad_sch

Title: RISCBoy

Size: A3	Date: 2021-02-21
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Rev: B0