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流表配置

结合测试的文档我已经总结了个ovs-dpdk的常见命令行文档:

http://10.10.3.15:8090/pages/viewpage.action?pageId=7910136

```
1 #正常转发(最简转发)
    ovs-ofctl add-flow br0 "table=0,in_port=dpdk0,actions=output:dpdk1"
   ovs-ofctl add-flow br0 "table=0,in_port=dpdk1,actions=output:dpdk0"
 5
   #2级流表转发
   ovs-ofctl add-flow br0 "table=0,actions=goto_table=1"
 7
    ovs-ofctl add-flow br0 "table=1,in_port=dpdk0,actions=output:dpdk1"
8
   ovs-ofctl add-flow br0 "table=1,in_port=dpdk1,actions=output:dpdk0"
9
10 #3级流表转发
11
   ovs-ofctl add-flow br0 "table=0,actions=goto_table=1"
12 ovs-ofctl add-flow br0 "table=1,actions=goto_table=2"
13 ovs-ofctl add-flow br0 "table=2,in_port=dpdk0,actions=output:dpdk1"
14
    ovs-ofctl add-flow br0 "table=2,in_port=dpdk1,actions=output:dpdk0"
15
    #匹配SMAC流转发
16
    ovs-ofctl add-flow br0
17
    "table=0,in_port=dpdk0,ip,dl_src=B8:CE:01:01:00:33,actions=output:dpdk1"
18
19
    #匹配DMAC流转发
20
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,ip,dl_dst=B8:EE:01:01:00:99,actions=output:dpdk1"
21
22
    #匹配S+DMAC流转发
    ovs-ofctl add-flow br0
23
    "table=0,in_port=dpdk0,ip,ip,dl_src=B8:CE:01:01:00:33,dl_dst=B8:EE:01:01:00
    :99,actions=output:dpdk1"
24
25
    #匹配VLAN流转发
    ovs-ofctl add-flow br0
26
    "table=0,in_port=dpdk0,dl_vlan=100,actions=output:dpdk1"
27
```

```
28 #匹配SMAC+SIP转发
29
    vs-ofctl add-flow br0
    "table=0,in_port=dpdk0,ip,nw_src=192.168.0.0/32,dl_src=B8:CE:01:01:00:33,ac
    tions=output:dpdk1"
30
    #匹配DMAC+DIP转发
31
32
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,ip,nw_dst=3.3.3.3,dl_dst=B8:EE:01:01:00:99,actions=0
    utput:dpdk1"
33
34
    #匹配SMAC+DMAC+SIP+DIP转发
35
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,ip,nw_src=192.168.0.0/32,dl_src=B8:CE:01:01:00:33,nw
    _dst=3.3.3.3,d1_dst=B8:EE:01:01:00:99,actions=output:dpdk1"
36
37
    #匹配SMAC+DMAC+SIP+DIP+S_TCP+D_TCP转发
3.8
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,tcp,nw_src=192.168.0.0/32,dl_src=B8:CE:01:01:00:33,n
    w_dst=3.3.3.3,dl_dst=B8:EE:01:01:00:99,tp_src=1024,tp_dst=1024,actions=outp
    ut:dpdk1"
39
40
    #匹配SMAC+DMAC+SIP+DIP+S_UCP+D_UCP转发
    ovs-ofctl add-flow br0
41
    "table=0,in_port=dpdk0,udp,nw_src=192.168.0.0/32,dl_src=B8:CE:01:01:00:33,n
    w_dst=3.3.3.3,d1_dst=B8:EE:01:01:00:99,tp_src=1024,tp_dst=1024,actions=outp
    ut:dpdk1"
42
    #加一层vlan
43
44
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_vlan_vid:10,output:dpdk1"
45
    #剥一层vlan
46
47
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,dl_vlan=100,actions=strip_vlan,output:dpdk1"
48
49
    #修改单层vlan
50
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,dl_vlan=100,actions=mod_vlan_vid:10,output:dpdk1"
51
52
    #修改源mac
    ovs-ofctl add-flow br0
53
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,output:dpdk1"
54
    #修改目的mac
55
56
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_dl_dst:B8:EE:01:01:00:88,output:dpdk1"
57
58
    #修改源+目的mac
59
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:B8:E
    E:01:01:00:88, output:dpdk1"
60
    #修改源mac+vlan
61
62
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_vlan_vid:10
    ,output:dpdk1"
63
   #修改源+目的mac+vlan
```

```
ovs-ofctl add-flow br0
65
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:B8:E
    E:01:01:00:88,mod_vlan_vid:10,output:dpdk1"
66
67
    #修改源ip
68
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_nw_src:192.169.0.2,output:dpdk1"
69
70
    #修改目的ip
71
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_nw_dst:5.5.5.5,output:dpdk1"
72
73
    #修改源+目的ip
    ovs-ofctl add-flow br0
74
    "table=0,in_port=dpdk0,actions=mod_nw_src:192.169.0.2,mod_nw_dst:5.5.5.5,ou
    tput:dpdk1"
75
76
    #修改源mac+目的mac+源IP+目的IP
    ovs-ofctl add-flow br0
77
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:B8:E
    E:01:01:00:88,mod_nw_src:192.169.0.2,mod_nw_dst:5.5.5.5,output:dpdk1"
78
79
    #修改源mac+目的mac+源IP+目的IP+vlan
    ovs-ofctl add-flow br0
    "table=0,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:B8:E
    E:01:01:00:88,mod_nw_src:192.169.0.2,mod_nw_dst:5.5.5.5,mod_vlan_vid:10,out
    put:dpdk1"
81
82
    #修改源mac+目的mac+源IP+目的IP+vlan+tcp源端口+tcp目的端口
    ovs-ofctl add-flow br0
    "table=0,tcp,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:
    B8:EE:01:01:00:88,mod_nw_src:192.169.0.2,mod_nw_dst:5.5.5.5,mod_vlan_vid:10
    ,mod_tp_src=1000,mod_tp_dst=1000,output:dpdk1"
84
    #修改源mac+目的mac+目的IP+vlan+tcp源端口+tcp目的端口
    ovs-ofctl add-flow br0
    "table=0,tcp,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:
    B8:EE:01:01:00:88,mod_nw_dst:5.5.5.5,mod_vlan_vid:10,mod_tp_src=1000,mod_tp
    _dst=1000,output:dpdk1"
87
    #修改源mac+目的mac+目的IP+vlan+tcp目的端口
88
    ovs-ofctl add-flow br0
    "table=0,tcp,in_port=dpdk0,actions=mod_dl_src:B8:CE:01:01:00:35,mod_dl_dst:
    B8:EE:01:01:00:88,mod_nw_dst:5.5.5.5,mod_vlan_vid:10,mod_tp_dst=1000,output
    :dpdk1"
```

rte_flow

模式条目

模式条目分成两类:

```
匹配协议头部及报文数据(ANY, RAW, ETH, VLAN, IPV4, IPV6, ICMP, UDP, TCP, SCTP,, VXLAN, MPLS, GRE等等)。
匹配元数据或影响模式处理(END, VOID, INVERT, PF, VF, PORT等等)。
```

常规流表举例:

index	Description
0	Eth
1	IPv4
2	TCP
3	VOID
4	END

常见的几个令人费解的匹配项

PORT:Matches packets coming from the specified physical port of the underlying device.匹配来自指定底层设备物理端口的数据包。

VF:Matches packets addressed to a virtual function ID of the device.

匹配寻址到设备虚拟功能ID的数据包。

PF:Matches packets addressed to the physical function of the device.

匹配寻址到设备物理功能的数据包。

RAW: Matches a byte string of a given length at a given offset.

在给定的偏移量处匹配给定长度的字节字符串。

其他好理解匹配项的举例如下,像eth、ipv4等不赘述:

Matches a VXLAN header (RFC 7348).

- flags: normally 0x08 (I flag).
- rsvd0: reserved, normally 0x000000.
- vni: VXLAN network identifier.
- rsvd1: reserved, normally 0x00.
- Default mask matches VNI only.

action项

主要包含以下三种:

- 终止类操作,如DROP、QUEUE、RSS、PF、VF,防止被后续流表处理。
- 非终止类操作,如PASSTHRU、DUPDUP,以供后续流表继续处理。
- 其他不影响数据包的非终止类操作,如END、VOID、MARK、FLAG、COUNT。

当多个动作组合在一起时,他们必须是不同的类型,这里还有个注意点: PASSTHRU动作是可以覆盖终止类动作的。

这里单独介绍下几个action:

mark:将整数值附加到数据包并设置PKT_RX_FDIR和PKT_RX_FDIR_ID的mbuf标志。

flag: 和mark很像,但是flag只设置PKT_RX_FDIR的mbuf标志,不设置值。

DUP: 复制包到给定的queue中。

流表模块卸载流程

```
dpif_netdev_flow_put
                                                        try_send_to_netdev
 8
                                                               1
 9
                                                          parse_flow_put
10
                           queue_netdev_flow_put
11
                                   (回调)
                                                         netdev_flow_put
12
                           netdev_offload_dpdk_flow_put
13
                                                         netdev_tc_flow_put
14
                           netdev_offload_dpdk_flow_create
15
16
            netdev_offload_dpdk_actions/netdev_offload_dpdk_mark_rss
                           //组织pattern和action
17
18
19
                           netdev_offload_dpdk_flow_create
20
21
                           netdev_dpdk_rte_flow_create
22
23
                           rte_flow_create
24
                                   |各厂商驱动回调
25
                           sfc_flow_create
26
27
    内核态:
28
    当报文不匹配的时候,会将报文上报,会调用udpif_upcall_handler
29
    udpif_upcall_handler-->recv_upcalls-->handle_upcalls-->dpif_operate--
    >dpif_netlink_operate-->try_send_to_netdev-->parse_flow_put--
    >netdev_flow_put-->netdev_tc_flow_put
30
31
   用户态:
32
   fast_path_processing->handle_packet_upcall->dp_netdev_flow_add-
    >queue_netdev_flow_put->dp_netdev_flow_offload_main
33
34
    内核态卸载是由handler线程处理,而用户态OVS+DPDK是由dp_netdev_flow_offload线程处理,
    此时的handler线程处于阻塞状态。
```

重点函数分析: action信息组织

```
1
    static int
 2
    parse_flow_actions(struct netdev *netdev,
 3
                       struct flow_actions *actions,
 4
                        struct nlattr *nl_actions,
 5
                       size_t nl_actions_len)
 6
 7
        struct nlattr *nla;
 8
        size_t left;
 9
10
        add_count_action(actions);
        NL_ATTR_FOR_EACH_UNSAFE (nla, left, nl_actions, nl_actions_len) {
11
            if (nl_attr_type(nla) == OVS_ACTION_ATTR_OUTPUT) {
12
13
                if (add_output_action(netdev, actions, nla)) {
14
                    return -1;
15
            } else if (nl_attr_type(nla) == OVS_ACTION_ATTR_DROP) {
16
17
                add_flow_action(actions, RTE_FLOW_ACTION_TYPE_DROP, NULL);
18
            } else if (nl_attr_type(nla) == OVS_ACTION_ATTR_SET ||
19
                       nl_attr_type(nla) == OVS_ACTION_ATTR_SET_MASKED) {
20
                const struct nlattr *set_actions = nl_attr_get(nla);
                 const size_t set_actions_len = nl_attr_get_size(nla);
```

```
22
                bool masked = nl_attr_type(nla) == OVS_ACTION_ATTR_SET_MASKED;
23
24
               if (parse_set_actions(actions, set_actions, set_actions_len,
25
                                     masked)) {
26
                   return -1;
27
               }
28
            } else if (nl_attr_type(nla) == OVS_ACTION_ATTR_PUSH_VLAN) {
29
               const struct ovs_action_push_vlan *vlan = nl_attr_get(nla);
30
31
               if (parse_vlan_push_action(actions, vlan)) {
32
                   return -1;
33
               }
34
            }
35
36
        if (nl_actions_len == 0) {
37
38
            VLOG_DBG_RL(&rl, "No actions provided");
39
            return -1;
40
        }
41
42
        add_flow_action(actions, RTE_FLOW_ACTION_TYPE_END, NULL);
43
        return 0;
44
    }
45
```

pattern信息组织:

```
1
    static int
 2
    parse_flow_match(struct netdev *netdev,
 3
                    odp_port_t orig_in_port OVS_UNUSED,
 4
                    struct flow_patterns *patterns,
 5
                    struct match *match)
 6
 7
        struct flow *consumed_masks;
 8
        uint8_t proto = 0;
 9
        consumed_masks = &match->wc.masks;
10
11
12
        if (!flow_tnl_dst_is_set(&match->flow.tunnel)) {
            memset(&consumed_masks->tunnel, 0, sizeof consumed_masks->tunnel);
13
14
        }
15
16
        patterns->physdev = netdev;
17
        memset(&consumed_masks->in_port, 0, sizeof consumed_masks->in_port);
18
        /* recirc id must be zero. */
19
        if (match->wc.masks.recirc_id & match->flow.recirc_id) {
20
            return -1;
21
        }
22
        consumed_masks->recirc_id = 0;
23
        consumed_masks->packet_type = 0;
24
        /* Eth */
25
26
        if (match->wc.masks.dl_type ||
27
            28
29
            add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_ETH, spec, mask);
30
        }
```

```
31
32
       /* VLAN */
       if (match->wc.masks.vlans[0].tci && match->flow.vlans[0].tci) {
33
34
          .....
35
36
          add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_VLAN, spec, mask);
37
       }
38
       /* For untagged matching match->wc.masks.vlans[0].tci is 0xFFFF and
       * match->flow.vlans[0].tci is 0. Consuming is needed outside of the if
39
40
       * scope to handle that.
       */
41
42
       memset(&consumed_masks->vlans[0], 0, sizeof consumed_masks->vlans[0]);
43
       /* IP v4 */
44
45
       if (match->flow.dl_type == htons(ETH_TYPE_IP)) {
46
          47
       /* If fragmented, then don't HW accelerate - for now. */
48
       if (match->wc.masks.nw_frag & match->flow.nw_frag) {
49
          return -1;
51
       }
52
       consumed_masks->nw_frag = 0;
53
       /* IP v6 */
54
55
       if (match->flow.dl_type == htons(ETH_TYPE_IPV6)) {
56
          .....
57
          add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_IPV6, spec, mask);
58
          /* Save proto for L4 protocol setup. */
59
60
          proto = spec->hdr.proto & mask->hdr.proto;
61
       }
62
       if (proto == IPPROTO_TCP) {
63
64
          65
          add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_TCP, spec, mask);
66
       } else if (proto == IPPROTO_UDP) {
67
          68
          add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_UDP, spec, mask);
69
       } else if (proto == IPPROTO_SCTP) {
70
          71
          add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_SCTP, spec, mask);
72
       } else if (proto == IPPROTO_ICMP) {
73
              74
       }
75
76
       add_flow_pattern(patterns, RTE_FLOW_ITEM_TYPE_END, NULL, NULL);
77
78
       if (!is_all_zeros(consumed_masks, sizeof *consumed_masks)) {
79
          return -1;
80
       }
81
       return 0;
   }
82
```

在下发到pmd流程结束后,调用函数 ufid_to_rte_flow_associate,建立ufid到rte_flow的映射关系,cmap_insert(&ufid_to_rte_flow,

rte_flow在pmd中的大致流程

```
1
                      函数入口
2
                         Т
3
                   解析pattern组织成流表key A
4
5
                    解析action组织成流表act B
6
7
          根据解析的pattern确定使用那种key-template, 如template c
8
9
          存储流表key A到template c的 c-hash-list
10
11
          存储流表act B到action专门的 act-hash-list
12
13
     如果带隧道,下发隧道的hash-list和其action为metadata的ac-hash-list
14
15
          组织key与act为逻辑要求的格式下发软硬件通道
16
```

EAL初始化

核心函数分析:

```
/* Launch threads, called at application init(). */
2
   int
3
   rte_eal_init(int argc, char **argv)
4
 5
 6
            /* rte_eal_cpu_init() ->
 7
                         eal_cpu_core_id()
8
                          eal_cpu_socket_id()
9
                    * 读取/sys/devices/system/[cpu|node]
10
                    * 设置lcore_config->[core_role|core_id|socket_id] */
            if (rte_eal_cpu_init() < 0) {</pre>
11
                    rte_eal_init_alert("Cannot detect lcores.");
12
13
                    rte_errno = ENOTSUP;
14
                    return -1;
            }
15
16
17
            /* eal_parse_args() ->
18
                          eal_parse_common_option() ->
19
                               eal_parse_coremask()
20
                               eal_parse_master_lcore()
21
                               eal_parse_lcores()
22
                          eal_adjust_config()
23
                    * 解析-c、--master_lcore、--lcores参数
24
                    * 在eal_parse_lcores()中确认可用的logical CPU
                    * 在eal_adjust_config()中设置rte_config.master_lcore为0 (设
25
    置第一个1core为MASTER 1core) */
            fctret = eal_parse_args(argc, argv);
26
27
            if (fctret < 0) {</pre>
28
                    rte_eal_init_alert("Invalid 'command line' arguments.");
29
                    rte_errno = EINVAL;
30
                    rte_atomic32_clear(&run_once);
31
                    return -1;
```

```
32
            }
33
            /* 初始化大页信息 */
34
35
            if (rte_eal_memory_init() < 0) {</pre>
36
                    rte_eal_init_alert("Cannot init memory\n");
37
                    rte_errno = ENOMEM;
38
                    return -1;
39
            }
40
41
            /* eal_thread_init_master() ->
42
                        eal_thread_set_affinity()
43
                    * 设置当前线程为MASTER lcore
44
                    * 在eal_thread_set_affinity()中绑定MASTER lcore到logical CPU
45
            eal_thread_init_master(rte_config.master_lcore);
46
            /* rte_bus_scan() ->
47
48
                         rte_pci_scan() ->
49
                              pci_scan_one() ->
50
                                  pci_parse_sysfs_resource()
51
                                  rte_pci_add_device()
52
                    * 遍历rte_bus_list链表,调用每个bus的scan函数,pci为
    rte_pci_scan()
53
                    * 遍历/sys/bus/pci/devices目录,为每个DBSF分配struct
    rte_pci_device
                    * 逐行读取并解析每个DBSF的resource, 保存到dev->mem_resource[i]
54
55
                    * 将dev插入rte_pci_bus.device_list链表 */
56
            if (rte_bus_scan()) {
                    rte_eal_init_alert("Cannot scan the buses for devices\n");
57
                    rte_errno = ENODEV;
58
59
                    return -1;
            }
60
61
            /* pthread_create() ->
62
63
                          eal_thread_loop() ->
64
                              eal_thread_set_affinity()
65
                    * 为每个SLAVE lcore创建线程,线程函数为eal_thread_loop()
                    * 在eal_thread_set_affinity()中绑定SLAVE lcore到logical CPU
66
    */
67
            RTE_LCORE_FOREACH_SLAVE(i) {
68
                    /*
69
70
                            * create communication pipes between master thread
71
                            * and children
72
                    /* MASTER lcore创建pipes用于MASTER和SLAVE lcore间通信(父子线程
73
    间通信) */
74
                    if (pipe(lcore_config[i].pipe_master2slave) < 0)</pre>
75
                            rte_panic("Cannot create pipe\n");
76
                    if (pipe(lcore_config[i].pipe_slave2master) < 0)</pre>
77
                            rte_panic("Cannot create pipe\n");
78
                    lcore_config[i].state = WAIT; /* 设置SLAVE lcore的状态为WAIT
79
    */
80
                    /* create a thread for each lcore */
81
82
                    ret = pthread_create(&lcore_config[i].thread_id, NULL,
83
                                            eal_thread_loop, NULL);
```

```
84
 85
            }
            /*函数用于载入业务函数。函数流程如下:
 86
 87
 88
            检查所有的副线程,是否都在WAIT状态。
89
           1.1 如果不是所有的副线程都在WAIT状态,则返回-EBUSY,跳出程序。
 90
            1.2 如果所有的副线程都在WAIT状态,进行后续的步骤。
 91
            遍历所有的副线程。
 92
           2.1. 调用rte_eal_remote_launch(),为各个副线程载入业务函数f,并通知副线程
    执行。
93
           主线程按需执行业务函数f。
 94
            3.1. 如果参数call_master设置为CALL_MASTER,则主线程需要执行业务函数f。
 95
            3.2. 如果参数call_master设置为SKIP_MASTER,则主线程不用执行业务函数f。*/
 96
            rte_eal_mp_remote_launch(sync_func, NULL, SKIP_MASTER);
 97
            /* 用于等待所有副线程返回 */
98
99
            rte_eal_mp_wait_lcore();
100
            /* Probe all the buses and devices/drivers on them */
101
102
            /* rte_bus_probe() ->
103
             rte_pci_probe() ->
104
                 pci_probe_all_drivers() ->
105
                     rte_pci_probe_one_driver() ->
106
                         rte_pci_match()
107
                         rte_pci_map_device() ->
108
                             pci_uio_map_resource()
109
                         eth_ixgbe_pci_probe()
         * 遍历rte_bus_list链表,调用每个bus的probe函数,pci为rte_pci_probe()
110
111
         * rte_pci_probe()/pci_probe_all_drivers()分别遍历
    rte_pci_bus.device_list/driver_list链表,匹配设备和驱动
112
         * 映射BAR,调用驱动的probe函数,ixgbe为eth_ixgbe_pci_probe() */
113
           if (rte_bus_probe()) {
                   rte_eal_init_alert("Cannot probe devices\n");
114
                   rte_errno = ENOTSUP;
115
116
                   return -1;
117
            }
118
119
    }
120
```

rte以太网设备配置流程分析

```
struct rte_eth_dev *dev = &rte_eth_devices[port_id];
驱动probe阶段识别到网卡后,层层深入调用到rte_eth_dev_allocate,从rte_eth_devices数组中分配
一个未使用的结构
后续API中的port_id其实访问的就是rte_eth_devices[port_id]
```

核心对象

```
port
端口对象,例如一个pcie网卡
rx_queue/tx_queue
端口收发队列对象
多核环境下,端口收到包后可指定响应的cpu来处理这个包。
通过增加收发队列,根据五元组哈希分配处理的core,实现计算资源的初步负载均衡
```

每个端口进来的包通过rss模块计算hash后,发送到对应cpu的queue上等待处理

tx_desc/rx_desc

网卡驱动中收发dma的队列数量。

收发desc中描述了dma收发需要的信息,如源/目的地址、长度等

核心接口

函数	功能
rte_eth_dev_count()	网卡数
rte_eth_dev_configure()	配置网卡
rte_eth_rx_queue_setup()	为网卡分配接收队列
rte_eth_tx_queue_setup()	为网卡分配发送队列
rte_eth_rx_burst()	网卡收包函数
rte_eth_tx_burst()	网卡发包函数
rte_eth_dev_start()	启动网卡

核心流程

```
1
                 rte_pktmbuf_pool_create
2
                    创建mbuf_pool
3
4
                    port_init
5
                   端口设置队列数
6
7
                 rte_eth_dev_configure
8
                        |---rte_eth_dev_rx_queue_config
9
          如果是第一次配置,那么就为每个发包队列分配一个指针。
          如果是重新配置,而且新的队列数量不为0,那么就释放老的队列
10
11
          如果是重新配置,而且新的队列数量为0,那么就释放所有的队列。
12
                        |---rte_eth_dev_tx_queue_config
13
                        |---nbl_repr_dev_configure
14
          配置队列的个数以及接口的配置信息(分配队列指针空间)
15
16
                 rte_eth_rx_queue_setup
17
                        |---nbl_rx_queue_setup
          使用mbuf_pool数据初始化接收队列,分配desc空间
18
19
20
                 rte_eth_tx_queue_setup
21
                 设置以太网设备 的发包队列
22
                        |---nbl_tx_queue_setup
                           网口的发包队列的初始化
23
24
25
                 rte_eth_dev_start
26
                        |---nbl_dev_start
27
                        |---nb1_tx_start
28
                        |---nbl_rx_start
29
   1、启动设备,为每个队列设置dma寄存器,标识每个队列的描述符ring的地址和长度
   2、启动设备的收发单元(设置寄存器, mbuf设置为网卡收包的dma地址, 记录在desc上 启动引擎)
30
   3、混杂、组播、链路状态更新
31
```

32	
33	1core_main
34	rx_burst/tx_burst收发包
35	nbl_repr_rx_burst
36	nbl_repr_tx_burst
37	