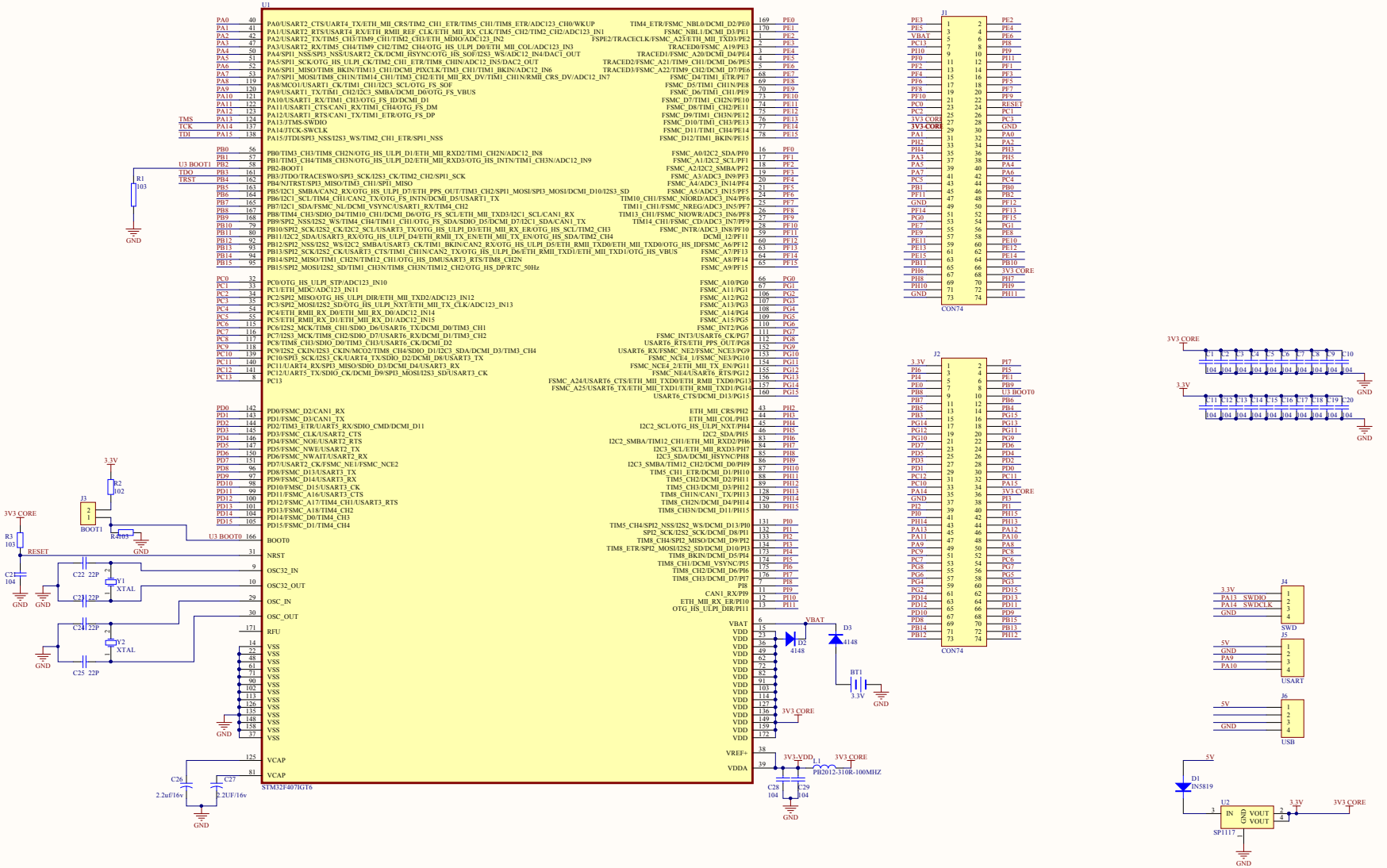
U1
STM32F407IGT16
PA0 40 PA0/USART2_CTS/UART4_TX/ETH_MII_CRS/TIM2_CH1_ETR/TIM5_CH1/TIM8_ETR/ADC123_CH0/WKUP
PA1 41 PA1/USART2_RTS/UART4_RX/ETH_RMII_REF_CLK/ETH_MII_RX_CLK/TIM5_CH2/TIM2_CH2/ADC123_IN1
PA2 42 PA2/USART2_TX/TIM5_CH3/TIM9_CH1/TIM2_CH3/ETH_MII_MDIO/ADC123_IN2
PA3 47 PA3/USART2_RX/TIM5_CH4/TIM9_CH2/TIM2_CH4/OTG_HS_ULPI_D0/ETH_MII_COL/ADC123_IN3
PA4 50 PA4/SPI1_NSS/SPI3_NSS/USART2_CK/DCMI_HSYNC/OTG_HS_SOF/I2S3_WS/ADC12_IN4/DAC1_OUT
PA5 51 PA5/SPI1_SCK/OTG_HS_ULPI_CK/TIM2_CH1_ETR/TIM8_CHIN/ADC12_IN5/DAC2_OUT
PA6 52 PA6/SPI1_MISO/TIM8_BKIN/TIM13_CH1/DCMI_PIXCLK/TIM3_CH1/TIM1_BKIN/ADC12_IN6
PA7 53 PA7/SPI1_MOSI/TIM8_CHIN/TIM14_CH1/TIM3_CH2/ETH_MII_RX_DV/TIM1_CH1N/RMII_CRS_DV/ADC12_IN7
PA8 119 PA8/MCO1/USART1_CK/TIM1_CH1/I2C3_SCL/OTG_FS_SOF
PA9 120 PA9/USART1_TX/TIM1_CH2/I2C3_SMBA/DCMI_D0/OTG_FS_VBUS
PA10 121 PA10/USART1_RX/TIM1_CH3/OTG_FS_ID/DCMI_D1
PA11 122 PA11/USART1_CTS/CAN1_RX/TIM1_CH4/OTG_FS_DM
PA12 123 PA12/USART1_RTS/CAN1_TX/TIM1_ETR/OTG_FS_DP
TMS PA13 124 PA13/JTMS-SWDIO
TCK PA14 137 PA14/JTCK-SWCLK
TDI PA15 138 PA15/JTDI/SPI3_NSS/I2S3_WS/TIM2_CH1_ETR/SPI1_NSS
PB0 56 PB0/TIM3_CH3/TIM8_CH2N/OTG_HS_ULPI_D1/ETH_MII_RXD2/TIM1_CH2N/ADC12_IN8
PB1 57 PB1/TIM3_CH4/TIM8_CH3N/OTG_HS_ULPI_D2/ETH_MII_RXD3/OTG_HS_INTN/TIM1_CH3N/ADC12_IN9
U3 BOOT1 PB2 58 PB2-BOOT1
TDO PB3 161 PB3/JTDO/TRACESWO/SPI3_SCK/I2S3_CK/TIM2_CH2/SPI1_SCK
TRST PB4 162 PB4/NJTRST/SPI3_MISO/TIM3_CH1/SPI1_MISO
PB5 163 PB5/I2C1_SMBA/CAN2_RX/OTG_HS_ULPI_D7/ETH_PPS_OUT/TIM3_CH2/SPI1_MOSI/SPI3_MOSI/DCMI_D10/I2S3_SD
PB6 164 PB6/I2C1_SCL/TIM4_CH1/CAN2_TX/OTG_FS_INTN/DCMI_D5/USART1_TX
PB7 165 PB7/I2C1_SDA/FSMC_NL/DCMI_VSYNC/USART1_RX/TIM4_CH2
PB8 167 PB8/TIM4_CH3/SDIO_D4/TIM10_CH1/DCMI_D6/OTG_FS_SCL/ETH_MII_TXD3/I2C1_SCL/CAN1_RX
PB9 168 PB9/SPI2_NSS/I2S2_WS/TIM4_CH4/TIM11_CH1/OTG_FS_SDA/SDIO_D5/DCMI_D7/I2C1_SDA/CAN1_TX
PB10 79 PB10/SPI2_SCK/I2S2_CK/I2C2_SCL/USART3_TX/OTG_HS_ULPI_D3/ETH_MII_RX_ER/OTG_HS_SCL/TIM2_CH3
PB11 80 PB11/I2C2_SDA/USART3_RX/OTG_HS_ULPI_D4/ETH_RMII_TX_EN/ETH_MII_TX_EN/OTG_HS_SDA/TIM2_CH4
PB12 92 PB12/SPI2_NSS/I2S2_WS/I2C2_SMBA/USART3_CK/TIM1_BKIN/CAN2_RX/OTG_HS_ULPI_D5/ETH_RMII_TXD0/ETH_MII_TXD0/OTG_HS_ID/FSMC_A6/PF12
PB13 93 PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N/CAN2_TX/OTG_HS_ULPI_D6/ETH_RMII_TXD1/ETH_MII_TXD1/OTG_HS_VBUS FSMC_A7/PF13
PB14 94 PB14/SPI2_MISO/TIM1_CH2N/TIM12_CH1/OTG_HS_DMUSART3_RTS/TIM8_CH2N FSMC_A8/PF14
PB15 95 PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N/TIM8_CH3N/TIM12_CH2/OTG_HS_DP/RTC_50Hz FSMC_A9/PF15
PC0 32 PC0/OTG_HS_ULPI_STP/ADC123_IN10 FSMC_A10/PG0
PC1 33 PC1/ETH_MDC/ADC123_IN11 FSMC_A11/PG1
PC2 34 PC2/SPI2_MISO/OTG_HS_ULPI_DIR/ETH_MII_TXD2/ADC123_IN12 FSMC_A12/PG2
PC3 35 PC3/SPI2_MOSI/I2S2_SD/OTG_HS_ULPI_NXT/ETH_MII_TX_CLK/ADC123_IN13 FSMC_A13/PG3
PC4 54 PC4/ETH_RMII_RX_D0/ETH_MII_RX_D0/ADC12_IN14 FSMC_A14/PG4
PC5 55 PC5/ETH_RMII_RX_D1/ETH_MII_RX_D1/ADC12_IN15 FSMC_A15/PG5
PC6 115 PC6/I2S2_MCK/TIM8_CH1/SDIO_D6/USART6_TX/DCMI_D0/TIM3_CH1 FSMC_INT2/PG6
PC7 116 PC7/I2S3_MCK/TIM8_CH2/SDIO_D7/USART6_RX/DCMI_D1/TIM3_CH2 FSMC_INT3/USART6_CK/PG7
PC8 117 PC8/TIM8_CH3/SDIO_D0/TIM3_CH3/USART6_CK/DCMI_D2 USART6_RTS/ETH_PPS_OUT/PG8
PC9 118 PC9/I2S2_CKIN/I2S3_CKIN/MCO2/TIM8_CH4/SDIO_D1/I2C3_SDA/DCMI_D3/TIM3_CH4 USART6_RX/FSMC_NE2/FSMC_NCE3/PG9
PC10 139 PC10/SPI3_SCK/I2S3_CK/UART4_TX/SDIO_D2/DCMI_D8/USART3_TX FSMC_NCE4_1/FSMC_NE3/PG10
PC11 140 PC11/UART4_RX/SPI3_MISO/SDIO_D3/DCMI_D4/USART3_RX FSMC_NCE4_2/ETH_MII_TX_EN/PG11
PC12 141 PC12/UART5_TX/SDIO_CK/DCMI_D9/SPI3_MOSI/I2S3_SD/USART3_CK FSMC_NE4/USART6_RTS/PG12
PC13 8 PC13 FSMC_A24/USART6_CTS/ETH_MII_TXD0/ETH_RMII_TXD0/PG13
FSMC_A25/USART6_TX/ETH_MII_TXD1/ETH_RMII_TXD1/PG14
USART6_CTS/DCMI_D13/PG15
PD0 142 PD0/FSMC_D2/CAN1_RX ETH_MII_CRS/PH2
PD1 143 PD1/FSMC_D3/CAN1_TX ETH_MII_COL/PH3
PD2 144 PD2/TIM3_ETR/UART5_RX/SDIO_CMD/DCMI_D11 I2C2_SCL/OTG_HS_ULPI_NXT/PH4
PD3 145 PD3/FSMC_CLK/USART2_CTS I2C2_SDA/PH5
PD4 146 PD4/FSMC_NOE/USART2_RTS I2C2_SMBA/TIM12_CH1/ETH_MII_RXD2/PH6
PD5 147 PD5/FSMC_NWE/USART2_TX I2C3_SCL/ETH_MII_RXD3/PH7
PD6 150 PD6/FSMC_NWAIT/USART2_RX I2C3_SDA/DCMI_HSYNC/PH8
PD7 151 PD7/USART2_CK/FSMC_NE1/FSMC_NCE2 I2C3_SMBA/TIM12_CH2/DCMI_D0/PH9
PD8 96 PD8/FSMC_D13/USART3_TX TIM5_CH1_ETR/DCMI_D1/PH10
PD9 97 PD9/FSMC_D14/USART3_RX TIM5_CH2/DCMI_D2/PH11
PD10 98 PD10/FMSC_D15/USART3_CK TIM5_CH3/DCMI_D3/PH12
PD11 99 PD11/FSMC_A16/USART3_CTS TIM8_CH1N/CAN1_TX/PH13
PD12 100 PD12/FSMC_A17/TIM4_CH1/USART3_RTS TIM8_CH2N/DCMI_D4/PH14
PD13 101 PD13/FSMC_A18/TIM4_CH2 TIM8_CH3N/DCMI_D11/PH15
PD14 104 PD14/FSMC_D0/TIM4_CH3
PD15 105 PD15/FSMC_D1/TIM4_CH4 TIM5_CH4/SPI2_NSS/I2S2_WS/DCMI_D13/PI0
SPI2_SCK/I2S2_SCK/DCMI_D8/PI1
BOOT0 166 BOOT0 TIM8_CH4/SPI2_MISO/DCMI_D9/PI2
NRST 31 NRST TIM8_ETR/SPI2_MOSI/I2S2_SD/DCMI_D10/PI3
OSC32_IN 9 OSC32_IN TIM8_BKIN/DCMI_D5/PI4
OSC32_OUT 10 OSC32_OUT TIM8_CH1/DCMI_VSYNC/PI5
TIM8_CH2/DCMI_D6/PI6
OSC_IN 29 OSC_IN TIM8_CH3/DCMI_D7/PI7
OSC_OUT 30 OSC_OUT PI8
CAN1_RX/PI9
RFU 171 RFU ETH_MII_RX_ER/PI10
OTG_HS_ULPI_DIR/PI11
VBAT 6
VDD 15
VDD 23
VDD 36
VDD 49
VDD 62
VDD 72
VDD 82
VDD 91
VDD 103
VDD 114
VDD 127
VDD 136
VDD 149
VDD 159
VDD 172
VSS 14
VSS 22
VSS 48
VSS 61
VSS 71
VSS 90
VSS 102
VSS 113
VSS 126
VSS 135
VSS 148
VSS 158
VSS 37
VREF+ 38
VDDA 39
VCAP 125
VCAP 81
J1
CON74
J2
CON74
J3
BOOT1
R4 103
R1 103
R2 102
R3 103
C2 104
C22 22P
C23 22P
C24 22P
C25 22P
Y1 XTAL
Y2 XTAL
C26 2.2uF/16v
C27 2.2UF/16v
D2 4148
D3 4148
BT1 3.3V
L1 PB2012-310R-100MHZ
C28 104
C29 104
C1 104 C2 104 C3 104 C4 104 C5 104 C6 104 C7 104 C8 104 C9 104 C10 104
C11 104 C12 104 C13 104 C14 104 C15 104 C16 104 C17 104 C18 104 C19 104 C20 104
J4 SWD
J5 USART
J6 USB
D1 IN5819
U2 SP1117
IN GND VOUT VOUT
3V3 CORE
3.3V
5V
VBAT
GND
RESET
U3 BOOT0
Title
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Number
Revision
Date: 2013/7/9
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