

# CS3210

Wang Xiyu

September 1, 2025

## 1 Computer architecture

- Concurrency:
  - Multiple tasks can start, run and complete in overlapping time period
  - may not be running or executing at the same instant
  - multiple execution flows make progress at the same time by interleaving their execution OR by the same time
- Parallelism:
  - Multiple tasks running simultaneously
  - Not only making progress, but also execute simultaneously
- Single processor:
  - Bit level parallelism:
    - \* parallelism by increasing the processor word size, e.g. parallel addition of 64 bit numbers on 32 bit machine
  - Instruction level parallelism:
    - \* pipelining: [time parallelism] number of pipeline stages = maximum achievable speedup
    - \* superscaling: [space parallelism] Duplicate the pipeline, multiple instructions can be on the same execution stage. Scheduling is challenging. Stronger structural hazard, less cycles per instruction