

Wyatt Wood

Kingston, Ontario

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EDUCATION:

Queen's University - *Computer Engineering*, 2014-2018 (Anticipated)

Fundamentals of Information and Data Structures

- Sorting algorithms, data structures, and graph theory in C.

Digital Systems

- CMOS, NMOS, PMOS circuits, hardware manipulation using VHDL and FPGAs, latches, and state machines.

Intro to Computing Science

- File I/O, error handling, GUI applications using JavaFX, and OOP using Java.

Computer Architecture

- Basic computer structure, instruction set architecture, and assembly language programming.

SOFTWARE PROFICIENCIES:

C, Java, Arduino, Assembly Language, HTML & CSS, Microsoft Word, Excel and PowerPoint, NESDAtrak, VHDL.

RELEVANT EXPERIENCE:

Seniors Outreach Services

Office Intern | Napanee, ON | June - Aug 2015

- Improved team efficiency by producing a Microsoft Excel overview, and revising the NESDAtrak user manual created by a previous employee.
- Updated 300+ client profiles solving errors in the NESDAtrak database.
- Consulted upgrading to current operating system and Microsoft Office versions for performance enhancements and fewer incompatibilities on company computers.

Visual Toolbox

Queen's University | Nov - Dec 2015

- Generated creative ideas for applications that would be beneficial to visually impaired people.
- Wrote concise reports investigating the financial and societal impacts of the application.
- Created the preliminary user interface and the touch gestures associated with it.
- Presented and demonstrated the final application in a team of 4 to an audience and a panel of judges.

Professional Website

Developer, Designer / Nov 2015 – Jan 2016

- Using my knowledge of HTML and CSS, I designed a professional, attractive website in 3 months in my spare time.

Digital Systems & Computer Architecture Laboratories

Queen's University / Sep 2015 – Apr 2016

- Collaborated with a team member to implement software that would accomplish various tasks (logic simplification, finite state machines, registers, decoders, and encoders) using VHDL and FPGA technology in Digital Systems labs, solidifying material taught during lectures.
- Implemented assembly language code that would perform various low level operations using a specific instruction set architecture in Computer Architecture labs with a team member.