TRANSLATORS: is a program that transferred is a program LATRODOCTION exprisher in one form (Rogram) > Translators > (Program) TYPES OF TRANSLATORS Interprets: executes program on the fly is as statement. are tetaled results are gother, transforms a program varitien in a high-level language into an executable formsy Setching statements from the source program and executing them "on the fly"; statement by statement -> authut > Translater 7 000-7 (interpreter) A new execution of a sche programe Seitles the source code og BASIC, Parl 2) languages that are complied (1e. complier) Source code/ > Translative (Complier) program fatche E IR exatrin-

The system of a language is defined by the granter
The syntax of a language is defined by the granter of the program.
G=(S,N,T,R), All statements are transformed into IR before
a grammar execution begins eg (16# c++, (obol , fased)
i definally Ada, Visual Basic NET
a skorting IR = Intermediate Representation of the source called
Symbol S, a Intermedicate code.
non-terminals fly
Terminals and 2) Languages generated by byte-code.
production rules byte-cale
P. Source Translator > TR > Franklator
production rules P. Source Source program Pr
Leve - Letters A new execution of the same
A new execution of the same
program fetcho the IR, not he source program
eg Java
Example java
77 javac Frample java & complier
- C (CLS)
>> java Example < Interpretor >> Example exe
Other transfectors, or consins of compliens.
i) Assembles: use the mnemonics of could, signed matting and a second
signed on Lie Sal
signed multi and unsigned mult

igt X = 5; > delinder Host keywood identifier operator ii) Loaden! reflects class code to check to malicions code. [ii] Laken! link a program file doing execution. iv) Preprocessis; make soles all me this necessary for execution have be added to the pragur. phase / stages of the compilation process
generated a local error of the new temper / syntatic process
and illegal Florer token Senadic Analyzi tokens | Symbolic Senantic Angysis IP Somantic legal Suntax tokens stored Symbolicale. Senatic optimizer valid tolers are street in a Optimization IR Ambuel pase trees error Deephole Code output /optimization Generalin r output L'Egypord Lidentifier Exponetar Stateral Edelineter

Semantia Analysis eg lint x [s. 0] = 1,2,3,3; // Semantic error int x [3] = {1,2,5}, // (orrect eg for (inti=5, i<=5; 4+3 double k = 10/i', 10/0 Dring semantic analysis type checking is done and affig is I released when here are incompatible types - Annonated passe fee adds more information to a purse free LEXICAL AMARYSIS scanning Hokening micro syntax (Regular Expression) Automata) PROPERTIES OF REGISTAR EXPRESSIONS

The symbol "x" is a regular expression (RE)

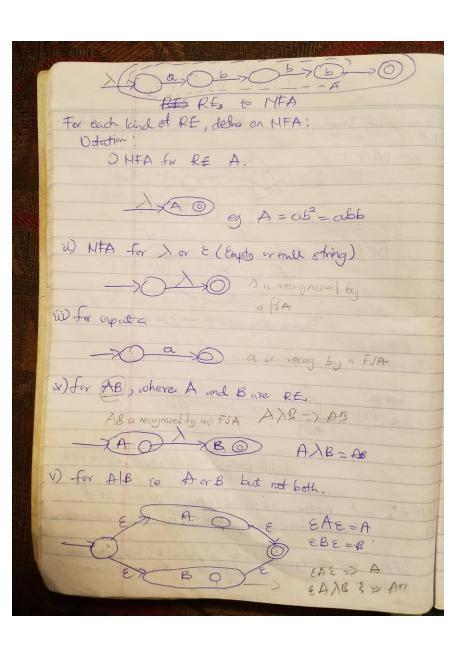
2) Myz is a string which is x followed by y 3) & or () or the fingly or mill is a RE 4) E or) BRE, E or) is on empty string DEar DRE 5) (oncatenation (.) and = of is a RE MB & ab & ba 7) Klene star (*)! 6) UHIOH eg at means zerdo) or aub= la, b) is a RE more o's Example ~ (1) (w) i) a. (boc) a*= (a, a, a, a, a) in at (500) v) (x) * v
in a (600) vi) (E*) * v a*={},a,aa,aaa, 3 vi) (E*) * V 8) LE Cleene Plus (+) ix) a. (buc) es at meas are () 1 A set contains well-defined a={a,a,a,a,...} => a+= { d, aa, aaa, 3 diects. 0 *. (b) () = {a,a,a,a,...3. (b) (b) = (), a, aa, aaa3

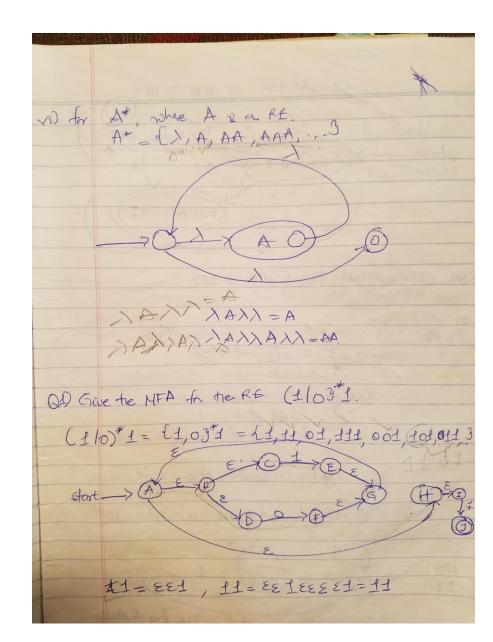
A LEXER is implemented as an automation. Automater in a a drite state machine (FSM) with inputs, but no adjust eg. Enlandes 0010 Deco = 8 watered State = < = { 50,51,52,53 } Find state F= (so, 5, 3, 00 10 or recogniting i) 0011 = 2 12 is accepted An automaton con se determinate at state s. Go non determination nd oolol not accepted Since sold end at FINITE-STATE AUTOMATA (FSA) state 5, which is not a Definition A finite - state cultareton. Sinal state. M=(5,7,f, 80, F) consists & a finite set & of states, a finite input alphaset I, a tensitive furtice I that anys a next stage to every pair of state and inputs on initial states So, and subset 7 of & consisting of Sind states.

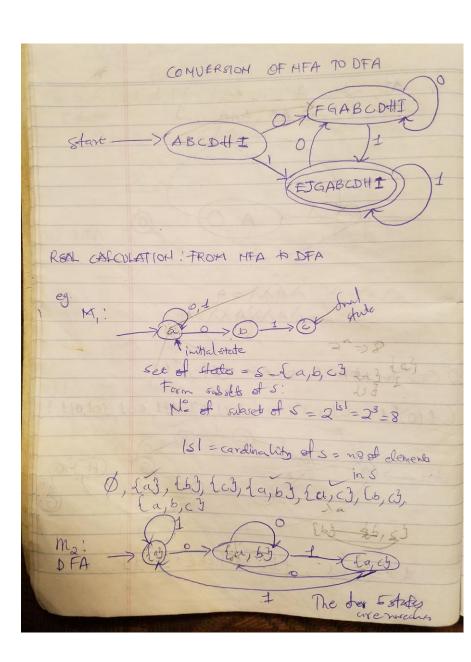
Assignment Find L(M3) The language of M3. Jula 2) TYPES AUTOMATA (FSA): 1) Deterministic Tinite-state Actomata (DFA) Given a (state, input) me an easily determine the next state. 18: For multiple puthsfedges inputs should be unique in These solare no 1- (or E) transition(s) in Von deterministic Finite- State Automate i) Given a pair of (state, input) there is no unique next State. eg +) T=10,13

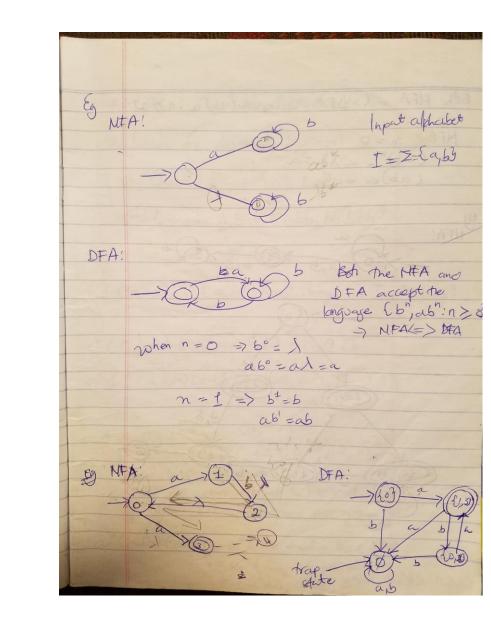
ff-fegular Expression w NFA may contain 1-(or E) to anothers 9 NFA and DFA one equivalent. NFA and DFA machine can be designed to recognize cos allept the same languages Language of a machine M, denoted by L(M) is all words / strings that M can accept the cognie. RE -> MFA -> DFA thigh-level sketch RES Lexical Table-driven implementation of specification. DFA.

For a given language the HFA can be simpler than the es MFA: DFA This implies NFA < 7 DFA DFA can be exponentially larger ten HFA. MFA: put: 101 lolis reagnised (accepted by the HFA DNFAIs and DFAS recognize laccept tresame set of languago. (regular languago generated by segular expression ii) DFAs are easity easier to implement. - There are no choices to consider le no multiple paths with same inputo



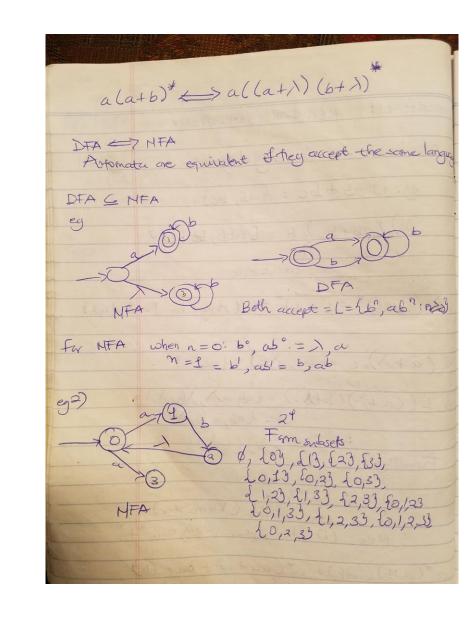




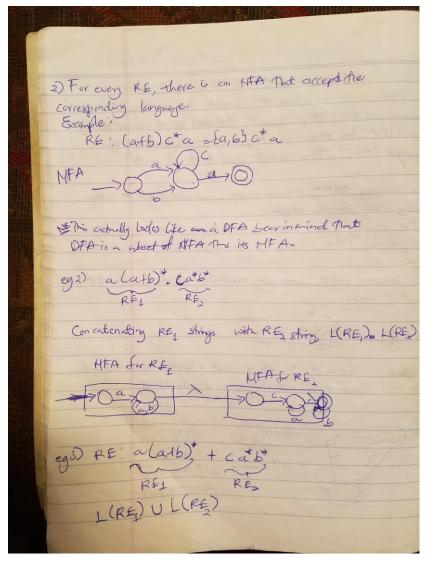


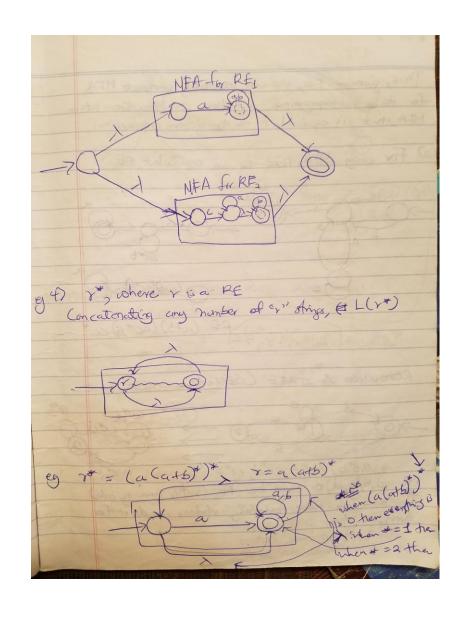
Both MFA and DFA accept h (ab) a: 1703 NFA: n=0 $(ab)^{\circ}a = \lambda a = a$ (ab)a = abaeg HFA! a

MORE OH REGILAR EXPRESSIONLE mechanism for describing languages! es. 1) as fbc = fab, bas i) lab+bc/bb= {abb, bbbbbbb Las, 63 concatenate. in (a+5) = { a, b = all strings on I = = = { a, b } iv) (a+1), c = {ac, 16} = {ac, c3 v) L((a+N(b+b)) = (ab, a), Nb, 1/1) = hab, a, b, Ny Formally: (a, A) (b, 1) 0 + 6 Union Las, 9/1,15, 1) in a is concatenate Note: (.) has higher primy than "+" a (afb) (afb) = a (aatab + bat bb) = a { aa, ab, ba, bb3 } = { a aa, a ab, a ba, a ba, a bb: n>03



EQUIVALENCE OF RES and NFA A language can be represented by a regular expression (RE) if and only if (iff) it is acceptedly Some HFA. In other words, RELicorresponds to regular languages (RLD). The proof consists of 3 people. Ufor exery NFA, there is an equivalent NFA with exactly one final state. 2) For every RE, thre is an equivalent MFA, 3) For every MFA, there is an equivalent RE. D Given con HFA, we can construct an equivalent MFA with one final state make a new final state and add a 1-transisting each old final state to the new final state. aboa > = ab + c ab* > = ab* aca) = aca





Al ways remove the intermediate made. Thus, given a RE, we can reconsilely construct MFA For its a subexpressions, and then combine them into an MFA that is equivalent to the given expressions 3) For every NFA, there is an equivalent PE (a+6)(ac*b) c R=! (a+5) (ac*b) REMOVING A STATE (GENERAL CASE) aetb, aetd, ced, cetb ab, c,d, e may be REs (rather tran grass) here are nove remaining states, we do it for

CC* b 9, EXPRESSION FOR A TWO-STATE AUTOMATOR (General Case) RE! a+5(d+ca+5)* (u+) = a+ 0: ab (d+cas) 1: a'b (d+ca'b) a+b(ca+b)+ This given any HIT, we may remove all its states except the initial and the final states and then convert it to a RE.

1) Lot 4, = {a, ba, abc3 na 4 12 = 412 = ha, ba, abey Ebcb, 63 =7 { abcb, ab, babcb, bab, ab cbcb, abcb} 2) Let L = {a, aas ter 13=1.1.1 = {a, aa3- {a, aa3. {a, aa> > (uaa, uhaa, aagaa, aaaaga) L* = L° UL'UL UL3 U ... = [] Li AUTOMATA An arromation is an abstract model of a computer which adepts some input (astring), produces output (yes/no or astring), and nay have intered strage (wonally a stack or tape). Input st

An automator sperates in a discrete time frame
(like a real computer). A particular state of the automator,
including the state of the control unit, input and storage
is alkal a constituration. The transaction from a configurate
to the next one is a male.

If the automator is called an

accepter. If the stay artiful is a string, it is called a

SYNTATIC ANALYSIS

transduces

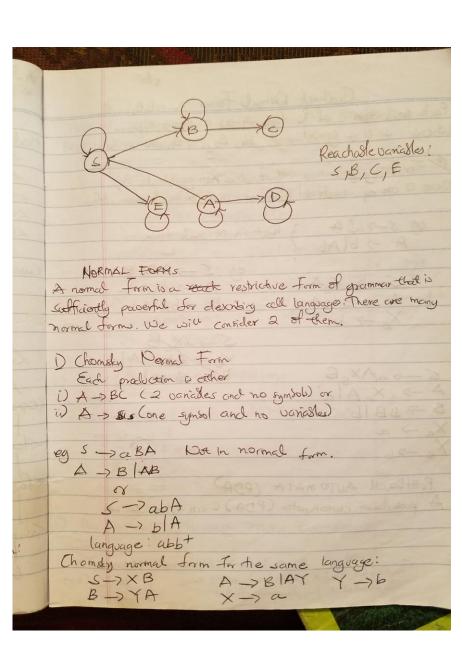
This to the 2rd phase of the compilation process. The phase this to draw or generate a parse or derivation free. If the parser is not able to generate the parse tree, it displays syntax error message. Gramass are used to define the syntax of a language.

A phrase structure granmar G=(V,T,S,P) consists of a Nocabilary (alphaset), a subset T of V consists of a start symbol 5 fm V, and a set of production F. The set of productions P. Theset V-T is denoted by M. Elenets of H are called nonterminal symbols were my the 1 H : 1

Top down approach starts from the starting syrand and dorive the terminals. Starting symbol is a non-ferninal Botton - up parsing! The passe-tree is generaled I'm é terminals intil tre starting sendoles encounterd or reacted. Botton Up 27 8/ /cligit? /digit? Top down (interger) Linfeger> (digit7

If more than one passe tree can be generated for a sigle statement. AMBIGUOUS GRAMMAR Production rule: Lexpression > = Lexpression > [+ 1-3 + Herm > Lexpr (+1 + top) Herm? ! = Lfactor> (*1 /3 Lfactor> / Lfactor) Hactor := < variable > / < liferal > Loorade? = ablc ... = ABlc ... Z Literal 7 !! String or numeric (ase 2: at(btc) Case 1: (a+b) +C L'expression> Lexpression + Lexpression L'term> Lexpressin> + Literany Lexperiment Like Lexpression) + Liferim> Luariables Luaniable b

Production rule S-> Alab A-Jaa B->a Case 2! (ase 1: las a Simplify' 5-) asalaa, B->b Language = (aa) + Saala(aa)a Variables that cannot be reached eg 5 7 BS/B/E B > CB/C D > BD/b
A -> DA/D/S C >aC/a E >cE/c To find unreachable variables alraw a dependency graph!



Greibach Dormal Form A-35B.B. .. Ba Each production is of the form A >s or A>s, Fifer. where s is a symbol and B1, B2,... Bn are varily For every grammar that does not generate), there is an equivalent grand in Grei back normalform eg stabA 3 Not in normal form A -> 6/A6 eg S-aAbB A -> aaA la 5-raXnA B 768 6 A -> 516A X -7 b 5->WXB Xc - AbaaAla \$ -> \$ 68 1b 5->aAXB A-) aXaAla B-7 bB 16 Xa -> a XR -75

PostDaoH AUTOMATA (PDA) is an automata und

IXPRESSION TYPES OF EXPRESSION. PREFIX EXPRESSIONS: Syntex! pretix - expressin ?! = Laperator 7 Loperand 1 Hoperand 27 Loperato>!!=+1-1*11-1-1. < operand 17: '= < operand > Loperand27 := Koperand> Loperand7:1= Luanisle7 | Knumber7 10) +24 = 2+4=6

10) +24 = 2+4=6

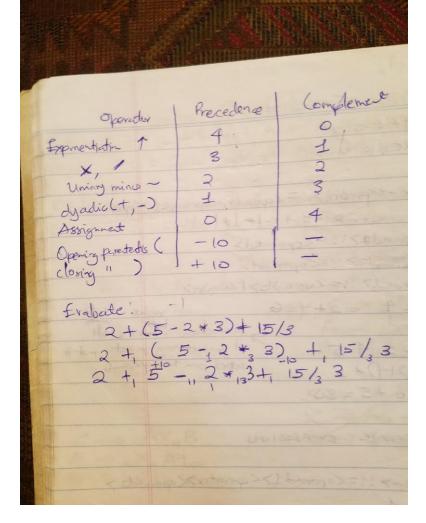
Scandon byte to left

10) + +245

Scandon byte to left

10) ++245

Nobelian to perads 90 +24 =2+4=6 =) 6 +5 = 30 2) INFIX / ALGEBRAIC EXPRESSION Syntax: <infix-expressin->!!=Loperand 1> Coperator > apparand > eg) 2+4 i) 2+4 +5 2+20 = 22 3) POSTFIX/SUFFIX/REVERSE POLICH EXPRESSION Egst) 2 4 + -5 Mar from pott tolet when you seed on period put it indiction.



An Afterfinding the sur of e compliments and 1. (+) PARENTHESPIRE ALGORITHM eg fully parenthence the expression d = a + b + a - e 1 m Step 1: Assign spend or precedence: d = a+ b * c-e 14 m Step 2: Find the complement of the operators d = 4 at 3 b + 6-3 e tom Step 3: Add back - to-back pare without egnal in minder to the complement of the operators d)))) = ((((a)))+(((b)*(c)))-(((e + m Step 4: Find the sum of the complements of + 1 => (A+3+1+3+0)+1=12 Step 5! Surround the whole expression with 12 opening ad closing parentieres: No maintain what is in step's and ack! Ebrackets ((((e)m))))) = ((((a)))+(((b)*(c)))-(((e)m))))))) 1) For hunan readulating remove redundant bradeto pureation (d = (a + (b * c)-(e 1 m)) Questr q = -b1 c-r 1/m +d 9 =0 = 1 b fy - 1 /3 m +, d 9 = 4 = 2 b 10 - 3 /, m + 3 d q = 1)) - ((k b 1))) - ((()/(m))) + ((1 d q)))) = (((k)) - ((b 1 c

q=~b1c-r/m+d q = -2 b 1/4 c -1/3 m +3 d q)))) = ((())~((b r c)))-(() /(m)))+(((d Sur of complexels = 4+2+0+3+1+3 => 10 14 (((d)))))))))))))))) For honan rediasing CODE GENERATION operation in source code operation in generated instruction (monerous) operate operated and + ABO SOR MOL DIX EXP RECA 'Acc [Recipional of contons of Alla REVA ALLE - AU. STOR

c - content

load into Accomplator mov, LDA The target code generated can be in essently language (w machine code). b) k=~b161-m/q*++5 es) a+b*c a+b*c LDA b MOL C; ALLE (ALL) + (L) STOR X; LDA m ADD a ACCELLACITICA) DIV a STOR & ; X, + (ALL) mul Y Protix expression STOR do LDA XI =k+ab REVA. = k (a+b) SUB 0 ADD S LOAD a STOR K ALLA 6 STOR K Assignment! Ia) Convert the ff algebraic/ ox infix expressions to reverse polish expression/strings. i) k=-t1s-d/m 1c+s * w in wo-stc*m+q17/s+d
Generate the assembly language code for atali and (ii)