

4.4.3.7 ARM Linux FIFO Driver

The Linux FIFO Device Driver is needed to access the FIFO for the MCAPI communication. It is available for the Nios II processor architecture. For the project it was necessary to port it to the ARM architecture. Direct access to the physical address space is not possible, because the ARM processor has a MMU. Therefore the driver has to map the physical address space into memory. After that it is possible to access the FIFO via pointer. For a test documentation look in tests.