

PHYS3500 Extra credit assignment

Student name: Andrew Hruschka

Design and demonstrate operation of the NAND based 2-bit full adder (with carry-over) using 7400 integrated circuit. The design should include testing circuits (DC input signals, output detection using LEDs).

- Theory: data-sheet with the circuit design (including testing circuit), and nicely organized calculations (if any) due April 18th, 3PM.
- Experiment (April 20, 25): assemble the circuit, demonstrate its operation to the TA; record the truth table (2 inputs, 3 outputs). Submit the (nicely-organized) experimental results for grading (due April 25, 5:20PM).