Section 2:

Truth Table:

A	B	C	Q	R	S
0	0	0	1	0	0
0	0	1	0	1	1
0	1	0	1	0	0
0	1	1	0	0	0
1	0	0	0	1	0
1	0	1	0	1	1
1	1	0	0	0	0
1	1	1	0	0	0

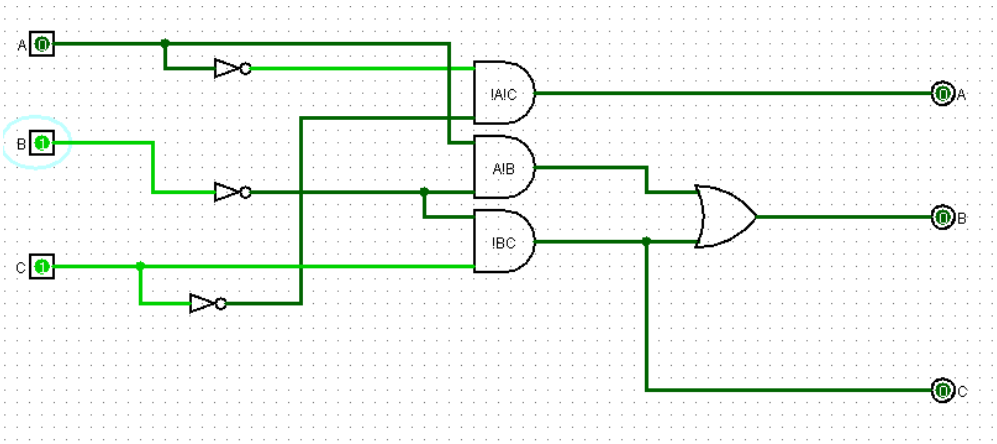
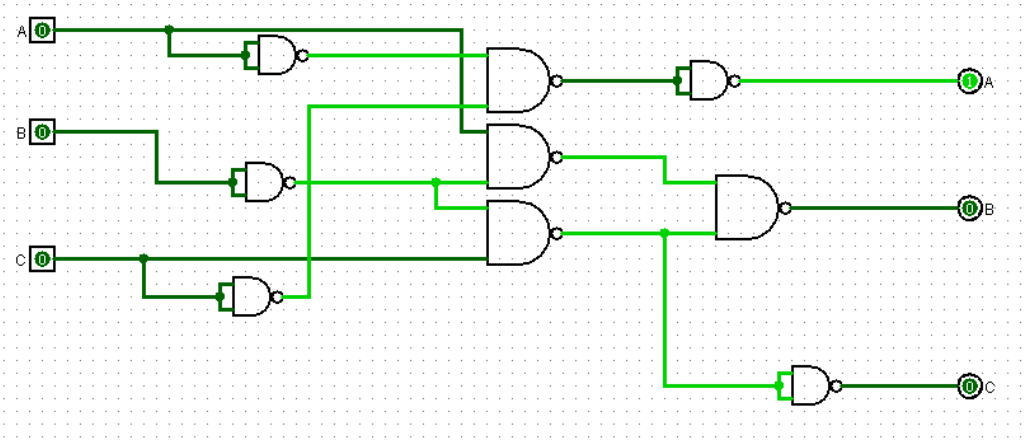
Karnaugh Maps / Expressions:

Q				
A\BC	00	01	11	10
0	1	0	0	1
1	0	0	0	0
Expression: !A!C				

R				
A\BC	00	01	11	10
0	0	1	0	0
1	1	1	0	0
Expression: A!B + !BC				

S				
A\BC	00	01	11	10
0	0	1	0	0
1	0	1	0	0
Expression: !BC				

Circuit:



NAND-Only Circuit: