Voltage Regulators – **Positive**

1.0 A

MC7800, MC7800A, MC7800AE, NCV7800

These voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators for a wide variety of applications including local, on-card regulation. These regulators employ internal current limiting, thermal shutdown, and safe-area compensation. With adequate heatsinking they can deliver output currents in excess of 1.0 A. Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

Features

- Output Current in Excess of 1.0 A
- No External Components Required
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe-Area Compensation
- Output Voltage Offered in 1.5%, 2% and 4% Tolerance
- Available in Surface Mount D²PAK-3, DPAK-3 and Standard 3-Lead Transistor Packages
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These are Pb-Free Devices

MAXIMUM RATINGS (T_A = 25°C, unless otherwise noted)

			Value		Unit
Rating	Symbol	369C	221A	936	
Input Voltage (5.0 – 18 V) (24 V)	VI		35 40		Vdc
Power Dissipation	P _D	Inte	W		
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	92	°C/W		
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	5.0 5.0 5.0			°C/W
Storage Junction Temperature Range	T _{stg}	_	°C		
Operating Junction Temperature	TJ		+150		°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



TO-220 T SUFFIX CASE 221AB

Heatsink surface connected to Pin 2.



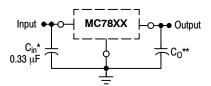
Pin 1. Input 2. Ground 3. Output D²PAK-3 D2T SUFFIX CASE 936

Heatsink surface (shown as terminal 4 in case outline drawing) is connected to Pin 2.



DPAK-3 DT SUFFIX CASE 369C

STANDARD APPLICATION



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

- XX, These two digits of the type number indicate nominal voltage.
 - * C_{in} is required if regulator is located an appreciable distance from power supply filter.
- ** C_O is not needed for stability; however, it does improve transient response. Values of less than 0.1 μF could cause instability.

ORDERING INFORMATION

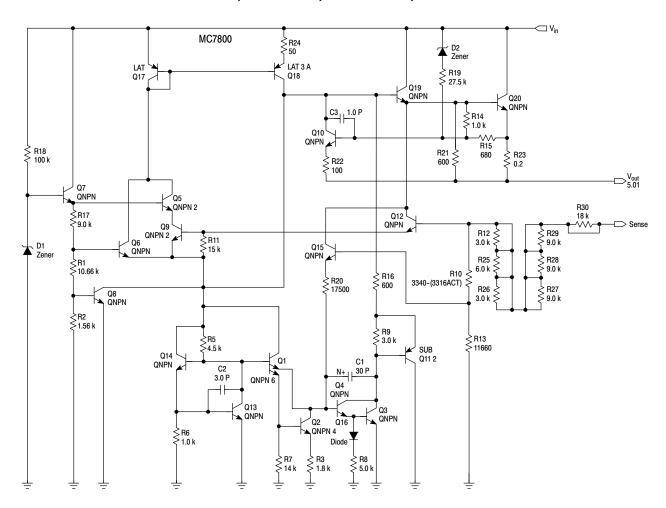
See detailed ordering and shipping information in the package dimensions section on page 23 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 27 of this data sheet.

1

^{*}This device series contains ESD protection and exceeds the following tests: Human Body Model 2000 V per MIL_STD_883, Method 3015. Machine Model Method 200 V.



This device contains 22 active transistors.

Figure 1. Representative Schematic Diagram

ELECTRICAL CHARACTERISTICS (V_{in} = 10 V, I_{O} = 500 mA, T_{J} = T_{low} to 125°C (Note 1), unless otherwise noted)

		MC7805B, NCV7805B				MC7805C		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	4.8	5.0	5.2	4.8	5.0	5.2	Vdc
	V _O	- 4.75	_ 5.0	- 5.25	4.75 -	5.0 -	5.25 -	Vdc
Line Regulation (Note 4) 7.5 Vdc \leq V $_{in}$ \leq 20 Vdc, 1.0 A 8.0 Vdc \leq V $_{in}$ \leq 12 Vdc	Reg _{line}	- -	5.0 1.3	100 50	- -	0.5 0.8	20 10	mV
Load Regulation (Note 4) 5.0 mA \leq I _O \leq 1.0 A 5.0 mA \leq I _O \leq 1.5 A (T _A = 25°C)	Reg _{load}	- -	1.3 0.15	100 50	- -	1.3 1.3	25 25	mV
Quiescent Current	I _B	-	3.2	8.0	-	3.2	6.5	mA
Quiescent Current Change 7.0 Vdc \leq V _{in} \leq 25 Vdc 5.0 mA \leq I _O \leq 1.0 A (T _A = 25°C)	Δl _B	- -	_ _	_ 0.5	- -	0.3 0.08	1.0 0.8	mA
Ripple Rejection 8.0 Vdc \leq V _{in} \leq 18 Vdc, f = 120 Hz	RR	-	68	-	62	83	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	0.9	-	-	0.9	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	-	0.6	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.3	-	-	-0.3	_	mV/°C

Tlow = 0°C for MC78XXC, MC78XXAC,
 = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB
 Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 10 V, I_{O} = 1.0 A, T_{J} = T_{low} to 125°C (Note 3), unless otherwise noted)

		MC7805AE	B/MC7805AC/N	ICV7805AB	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	4.9	5.0	5.1	Vdc
Output Voltage (5.0 mA \leq I $_{O}$ \leq 1.0 A, P $_{D}$ \leq 15 W) 7.5 Vdc \leq V $_{in}$ \leq 20 Vdc	Vo	4.8	5.0	5.2	Vdc
	Reg _{line}	- - - -	0.5 0.8 1.3 4.5	10 12 4.0 10	mV
Load Regulation (Note 4) 5.0 mA \leq I $_{O}$ \leq 1.5 A, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A 250 mA \leq I $_{O}$ \leq 750 mA	Reg _{load}	- - -	1.3 0.8 0.53	25 25 15	mV
Quiescent Current	I _B	-	3.2	6.0	mA
	Δl _B	- - -	0.3 - 0.08	0.8 0.8 0.5	mA
Ripple Rejection 8.0 Vdc \leq V _{in} \leq 18 Vdc, f = 120 Hz, I _O = 500 mA	RR	68	83	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	μV/V _O
Output Resistance (f = 1.0 kHz)	r _O	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	Α
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	ı	-0.3	-	mV/°C

^{3.} T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXAB, MC78XXAB, and MC78XXAEB

4. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 11 V, I_{O} = 500 mA, T_{J} = T_{low} to 125°C (Note 5), unless otherwise noted)

		MC7806B/NCV7806B						
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	5.75	6.0	6.25	5.75	6.0	6.25	Vdc
	V _O	_ 5.7	_ 6.0	- 6.3	5.7 -	6.0 -	6.3 -	Vdc
	Reg _{line}	- -	5.5 1.4	120 60	- -	0.5 0.8	24 12	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 6) 5.0 mA $\leq I_O \leq 1.5$ A	Reg _{load}	_	1.3	120	_	1.3	30	mV
Quiescent Current (T _J = 25°C)	I _B	_	3.3	8.0	-	3.3	8.0	mA
Quiescent Current Change 8.0 Vdc \leq V _{in} \leq 25 Vdc 5.0 mA \leq I _O \leq 1.0 A	Δl _B	_ _	- -	_ 0.5	- -	0.3 0.08	1.3 0.5	mA
Ripple Rejection 9.0 Vdc \leq V _{in} \leq 19 Vdc, f = 120 Hz	RR	-	65	-	58	65	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	_	10	_	_	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	0.9	-	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	_	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCV _O	_	-0.3	_	_	-0.3	-	mV/°C

^{5.} T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 6. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 11 V, I_{O} = 1.0 A, T_{J} = T_{low} to 125°C (Note 7), unless otherwise noted)

			MC7806AC		
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	5.88	6.0	6.12	Vdc
Output Voltage (5.0 mA \leq I $_{O}$ \leq 1.0 A, P $_{D}$ \leq 15 W) 8.6 Vdc \leq V $_{in}$ \leq 21 Vdc	Vo	5.76	6.0	6.24	Vdc
Line Regulation (Note 8) 8.6 Vdc \leq V _{in} \leq 25 Vdc, I _O = 500 mA 9.0 Vdc \leq V _{in} \leq 13 Vdc, I _O = 1.0 A	Reg _{line}		5.0 1.4	12 15	mV
Load Regulation (Note 8) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A 250 mA \leq I _O \leq 750 mA	Reg _{load}	- - -	1.3 0.9 0.2	25 25 15	mV
Quiescent Current	Ι _Β	-	3.3	6.0	mA
Quiescent Current Change 9.0 Vdc \leq V $_{in}$ \leq 25 Vdc, I $_{O}$ = 500 mA 9.0 Vdc \leq V $_{in}$ \leq 21 Vdc, I $_{O}$ = 1.0 A, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	Δl_{B}	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 9.0 Vdc \leq V _{in} \leq 19 Vdc, f = 120 Hz, I _O = 500 mA	RR	58	65	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	μV/V _O
Output Resistance (f = 1.0 kHz)	r _O	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O		-0.3	_	mV/°C

^{7.} T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 8. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 14 V, I_{O} = 500 mA, T_{J} = T_{low} to 125°C (Note 9), unless otherwise noted)

		MC7808B/NCV7808B				MC7808C		
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	7.7	8.0	8.3	7.7	8.0	8.3	Vdc
Output Voltage (5.0 mA \leq I $_O \leq$ 1.0 A, P $_D \leq$ 15 W) 10.5 Vdc \leq V $_{in} \leq$ 23 Vdc 11.5 Vdc \leq V $_{in} \leq$ 23 Vdc	Vo	- 7.6	- 8.0	- 8.4	7.6 -	8.0	8.4 _	Vdc
Line Regulation, T_J = 25°C, (Note 10) 10.5 Vdc \leq V _{in} \leq 25 Vdc 11 Vdc \leq V _{in} \leq 17 Vdc	Reg _{line}	- -	6.0 1.7	160 80	- -	6.0 1.7	32 16	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 10) 5.0 mA $\leq I_O \leq 1.5$ A	Reg _{load}	-	1.4	160	-	1.4	35	mV
Quiescent Current	Ι _Β	-	3.3	8.0	-	3.3	8.0	mA
Quiescent Current Change 10.5 $Vdc \le V_{in} \le 25 Vdc$ 5.0 $mA \le I_O \le 1.0 A$	Δl_{B}	- -	- -	_ 0.5	_ _	- -	1.0 0.5	mA
Ripple Rejection 11.5 Vdc ≤ V _{in} ≤ 18 Vdc, f = 120 Hz	RR	-	62	-	56	62	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	-	10	_	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	0.9	-	-	0.9	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	_	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	-	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	_	-0.4	_	_	-0.4	_	mV/°C

^{9.} T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 10. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

 $\textbf{ELECTRICAL CHARACTERISTICS} \; (V_{in} = 14 \; V, \; I_O = 1.0 \; A, \; T_J = T_{low} \; to \; 125^{\circ}C \; (Note \; 11), \; unless \; otherwise \; noted)$

		MC7808	AB/MC7808AC/N	CV7808AB	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	7.84	8.0	8.16	Vdc
Output Voltage (5.0 mA \leq I $_{O}$ \leq 1.0 A, P $_{D}$ \leq 15 W) 10.6 Vdc \leq V $_{in}$ \leq 23 Vdc	Vo	7.7	8.0	8.3	Vdc
Line Regulation (Note 12) $10.6 \text{ Vdc} \le V_{in} \le 25 \text{ Vdc}, I_O = 500 \text{ mA}$ $11 \text{ Vdc} \le V_{in} \le 17 \text{ Vdc}, I_O = 1.0 \text{ A}$ $10.4 \text{ Vdc} \le V_{in} \le 23 \text{ Vdc}, T_J = 25^{\circ}\text{C}$	Reg _{line}	- - -	6.0 1.7 5.0	15 18 15	mV
Load Regulation (Note 12) 5.0 mA \leq I $_{O}$ \leq 1.5 A, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A 250 mA \leq I $_{O}$ \leq 750 mA	Reg _{load}	- - -	1.4 1.0 0.22	25 25 15	mV
Quiescent Current	I _B	-	3.3	6.0	mA
Quiescent Current Change 11 Vdc \leq V _{in} \leq 25 Vdc, I _O = 500 mA 10.6 Vdc \leq V _{in} \leq 23 Vdc, I _O = 1.0 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A	Δl _B	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 11.5 Vdc \leq V $_{in}$ \leq 21.5 Vdc, f = 120 Hz, I $_{O}$ = 500 mA	RR	56	62	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	_	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	0.9	_	mΩ
Short Circuit Current Limit ($T_A = 25^{\circ}C$) $V_{in} = 35 \text{ Vdc}$	I _{SC}	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	-	-0.4	_	mV/°C

^{11.} T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 12. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 15 V, I_O = 500 mA, T_J = T_{low} to 125°C (Note 13), unless otherwise noted)

		MC7809B/NCV7809B				MC7809C	;	
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	8.65	9.0	9.35	8.65	9.0	9.35	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 11.5 Vdc \leq V _{in} \leq 24 Vdc	Vo	8.55	9.0	9.45	8.55	9.0	9.45	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 14) 11 Vdc $\leq V_{in} \leq 26$ Vdc 11.5 Vdc $\leq V_{in} \leq 17$ Vdc	Reg _{line}	_ _	6.2 1.8	32 16	_ _	6.2 1.8	32 16	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 14) 5.0 mA $\leq I_O \leq$ 1.5 A	Reg _{load}	-	1.5	35	-	1.5	35	mV
Quiescent Current	I _B	_	3.4	8.0	_	3.4	8.0	mA
Quiescent Current Change 11.5 $Vdc \le V_{in} \le 26 Vdc$ 5.0 $mA \le I_O \le 1.0 A$	Δl_{B}	_ _	_ _	1.0 0.5	_ _	_ _	1.0 0.5	mA
Ripple Rejection 11.5 Vdc \leq V _{in} \leq 21.5 Vdc, f = 120 Hz	RR	56	61	-	56	61	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	$V_I - V_O$	_	2.0	_	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz $\leq f \leq$ 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	_	1.0	_	_	1.0	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	_	0.2	_	-	0.2	-	Α
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-0.5	-	-	-0.5	-	mV/°C

^{13.}T_{low} = 0°C for MC78XXC, MC78XXAC,
= -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB

14. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

 $\textbf{ELECTRICAL CHARACTERISTICS} \ (V_{in} = 15 \ V, \ I_O = 1.0 \ A, \ T_J = T_{low} \ to \ 125^{\circ}C \ (Note \ 15), \ unless \ otherwise \ noted)$

		MC7	809AB/MC78	09AC	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (TJ = 25°C)	Vo	8.82	9.0	9.18	Vdc
Output Voltage (5.0 mA ≤ Io ≤ 1.0 A, PD ≤ 15 W) 11.5 Vdc ≤ Vin ≤ 24 Vdc	Vo	8.65	9.0	9.35	Vdc
Line Regulation (Note 16) 11.5 Vdc ≤ Vin ≤ 26 Vdc, Io = 500 mA 12 Vdc ≤ Vin ≤ 17 Vdc, Io = 1.0 A 11.5 Vdc ≤ Vin ≤ 24 Vdc, TJ = 25°C	Regline	- - -	6.2 1.8 5.2	16 7.0 16	mV
Load Regulation (Note 16) $5.0 \text{ mA} \le \text{Io} \le 1.5 \text{ A}, \text{ TJ} = 25^{\circ}\text{C}$ $5.0 \text{ mA} \le \text{Io} \le 1.0 \text{ A}$ $250 \text{ mA} \le \text{Io} \le 750 \text{ mA}$	Regload	- - -	- - -	25 25 15	mV
Quiescent Current	lв	-	3.3	6.0	mA
Quiescent Current Change 11.5 Vdc \leq Vin \leq 26 Vdc, Io = 500 mA 11.5 Vdc \leq Vin \leq 24 Vdc, Io = 1.0 A, TJ = 25°C 5.0 mA \leq Io \leq 1.0 A	ΔΙΒ	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 11.5 Vdc \leq Vin \leq 21.5 Vdc, f = 120 Hz, IO = 500 mA	RR	56	61	-	dB
Dropout Voltage (Io = 1.0 A, TJ = 25°C)	Vı_Vo		2.0		Vdc
Output Noise Voltage (TA = 25° C) 10 Hz \leq f \leq 100 kHz	Vn	-	10	-	μV/VO
Output Resistance f = 1.0 kHz	rO	-	1.0	-	mΩ
Short Circuit Current Limit (TA = 25°C) Vin = 35 Vdc	Isc	-	0.2	-	А
Peak Output Current (TJ = 25°C)	Imax	-	2.2	-	А
Average Temperature Coefficient of Output Voltage	TCVo	_	-0.5	-	mV/°C

 $[\]begin{array}{ll} 15.T_{\text{low}} = 0^{\circ}\text{C for MC78XXC, MC78XXAC,} \\ = -40^{\circ}\text{C for NCV78XX, MC78XXB, MC78XXAB.} \end{array}$

^{16.} Load and line regulation are specified at constant junction temperature. Changes in Vo due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 19 V, I_{O} = 500 mA, T_{J} = T_{low} to 125°C (Note 17), unless otherwise noted)

		MC7812B/NCV7812B				MC7812C	;	
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	11.5	12	12.5	11.5	12	12.5	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 14.5 Vdc \leq V _{in} \leq 27 Vdc 15.5 Vdc \leq V _{in} \leq 27 Vdc	Vo	_ 11.4	- 12	_ 12.6	11.4 -	12 -	12.6 -	Vdc
Line Regulation, $T_J = 25^{\circ}C$ (Note 18) 14.5 Vdc $\leq V_{in} \leq 30$ Vdc 16 Vdc $\leq V_{in} \leq 22$ Vdc 14.8 Vdc $\leq V_{in} \leq 27$ Vdc, $I_O = 1.0$ A	Reg _{line}	- - -	7.5 2.2 -	240 120 -	- - -	3.8 0.3 -	24 24 48	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 18) 5.0 mA $\leq I_O \leq 1.5$ A	Reg _{load}	_	1.6	240	-	8.1	60	mV
Quiescent Current	Ι _Β	_	3.4	8.0	-	3.4	6.5	mA
Quiescent Current Change 14.5 Vdc \leq V $_{in}$ \leq 30 Vdc, I $_{O}$ = 1.0 A, T $_{J}$ = 25°C 15 Vdc \leq V $_{in}$ \leq 30 Vdc 5.0 mA \leq I $_{O}$ \leq 1.0 A	Δl_{B}	- - -	- - -	- 1.0 0.5	- - -	- - -	0.7 0.8 0.5	mA
Ripple Rejection 15 Vdc ≤ V _{in} ≤ 25 Vdc, f = 120 Hz	RR	-	60	-	55	60	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	_	2.0	_	_	2.0	-	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	_	1.1	_	_	1.1	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	_	-	0.2	_	Α
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	_	_	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	_	-0.8	_	_	-0.8	_	mV/°C

^{17.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 18. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 19 V, I_{O} = 1.0 A, T_{J} = T_{low} to 125°C (Note 19), unless otherwise noted)

		MC7812AE	B/MC7812AC/N	ICV7812AB	
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	11.75	12	12.25	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 14.8 Vdc \leq V _{in} \leq 27 Vdc	Vo	11.5	12	12.5	Vdc
Line Regulation (Note 20) 14.8 Vdc \leq V _{in} \leq 30 Vdc, I _O = 500 mA 16 Vdc \leq V _{in} \leq 22 Vdc, I _O = 1.0 A 14.5 Vdc \leq V _{in} \leq 27 Vdc, T _J = 25°C	Reg _{line}	- - -	3.8 2.2 6.0	18 20 120	mV
Load Regulation (Note 20) 5.0 mA \leq I _O \leq 1.5 A, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A	Reg _{load}	- -	- -	25 25	mV
Quiescent Current	I _B	-	3.4	6.0	mA
Quiescent Current Change 15 Vdc \leq V _{in} \leq 30 Vdc, I _O = 500 mA 14.8 Vdc \leq V _{in} \leq 27 Vdc, T _J = 25°C 5.0 mA \leq I _O \leq 1.0 A, T _J = 25°C	Δl _B		- - -	0.8 0.8 0.5	mA
Ripple Rejection 15 Vdc \leq V _{in} \leq 25 Vdc, f = 120 Hz, I _O = 500 mA	RR	55	60	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	_	10	-	μV/V _O
Output Resistance (f = 1.0 kHz)	r _O	-	1.1	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	-	-0.8	_	mV/°C

^{19.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 20. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 23 V, I_{O} = 500 mA, T_{J} = T_{low} to 125°C (Note 21), unless otherwise noted)

		MC7815B/NCV7815B						
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	14.4	15	15.6	14.4	15	15.6	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 17.5 Vdc \leq V _{in} \leq 30 Vdc 18.5 Vdc \leq V _{in} \leq 30 Vdc	Vo	_ 14.25	- 15	_ 15.75	14.25 –	15 -	15.75 –	Vdc
Line Regulation, T_J = 25°C (Note 22) 17.9 Vdc \leq V $_{in}$ \leq 30 Vdc 20 Vdc \leq V $_{in}$ \leq 26 Vdc	Reg _{line}	- -	8.5 3.0	300 150	- -	8.5 3.0	30 28	mV
Load Regulation, $T_J = 25^{\circ}C$ (Note 22) 5.0 mA $\leq I_O \leq 1.5$ A	Reg _{load}	_	1.8	300	-	1.8	55	mV
Quiescent Current	I _B	-	3.5	8.0	-	3.5	6.5	mA
Quiescent Current Change 17.5 Vdc \leq V $_{in}$ \leq 30 Vdc 17.5 Vdc \leq V $_{in}$ \leq 30 Vdc, I $_{O}$ = 1.0 A, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	Δl _B	- - -	- - -	- 1.0 0.5	- - -	- - -	0.8 0.7 0.5	mA
Ripple Rejection 18.5 Vdc ≤ V _{in} ≤ 28.5 Vdc, f = 120 Hz	RR	_	58	_	54	58	_	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	_	2.0	_	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	1.2	_	_	1.2	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	_	-	0.2	_	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	_	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	-	-1.0	_	_	-1.0	_	mV/°C

^{21.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 22. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

 $\textbf{ELECTRICAL CHARACTERISTICS} \; (V_{in} = 23 \; V, \; I_O = 1.0 \; A, \; T_J = T_{low} \; to \; 125^{\circ}C \; (Note \; 23), \; unless \; otherwise \; noted)$

		MC7815AB/MC7815AC			
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	14.7	15	15.3	Vdc
Output Voltage (5.0 mA \leq I _O \leq 1.0 A, P _D \leq 15 W) 17.9 Vdc \leq V _{in} \leq 30 Vdc	Vo	14.4	15	15.6	Vdc
Line Regulation (Note 24) 17.9 Vdc \leq V _{in} \leq 30 Vdc, I _O = 500 mA 20 Vdc \leq V _{in} \leq 26 Vdc 17.5 Vdc \leq V _{in} \leq 30 Vdc, I _O = 1.0 A, T _J = 25°C	Reg _{line}	- - -	8.5 3.0 7.0	20 22 20	mV
Load Regulation (Note 24) 5.0 mA \leq I $_{O} \leq$ 1.5 A, T $_{J} = 25^{\circ}$ C 5.0 mA \leq I $_{O} \leq$ 1.0 A 250 mA \leq I $_{O} \leq$ 750 mA	Reg _{load}	- - -	1.8 1.5 1.2	25 25 15	mV
Quiescent Current	I _B	-	3.5	6.0	mA
	Δl _B	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 18.5 $Vdc \le V_{in} \le 28.5 Vdc$, f = 120 Hz, $I_O = 500 \text{ mA}$	RR	60	80	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	_	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	1.2	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α
Average Temperature Coefficient of Output Voltage	TCV _O	-	-1.0	-	mV/°C

^{23.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 24. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS (V_{in} = 27 V, I_O = 500 mA, T_J = T_{low} to 125°C (Note 25), unless otherwise noted)

		MC7818B		MC7818C				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	17.3	18	18.7	17.3	18	18.7	Vdc
Output Voltage (5.0 mA \leq I $_O \leq$ 1.0 A, P $_D \leq$ 15 W) 21 Vdc \leq V $_{in} \leq$ 33 Vdc 22 Vdc \leq V $_{in} \leq$ 33 Vdc	Vo	- 17.1	- 18	_ 18.9	17.1 -	18 -	18.9 –	Vdc
Line Regulation, (Note 26) 21 Vdc \leq V _{in} \leq 33 Vdc 24 Vdc \leq V _{in} \leq 30 Vdc	Reg _{line}	- -	9.5 3.2	360 180	- -	9.5 3.2	50 25	mV
Load Regulation, (Note 26) 5.0 mA \leq I _O \leq 1.5 A	Reg _{load}	-	2.0	360	-	2.0	55	mV
Quiescent Current	I _B	_	3.5	8.0	-	3.5	6.5	mA
Quiescent Current Change 21 Vdc \leq V _{in} \leq 33 Vdc 5.0 mA \leq I _O \leq 1.0 A	Δl_{B}	- -	- -	_ 0.5	_ _	_ _	1.0 0.5	mA
Ripple Rejection 22 Vdc ≤ V _{in} ≤ 33 Vdc, f = 120 Hz	RR	-	57	-	53	57	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _{il} – V _O	-	2.0	_	-	2.0	_	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	1.3	-	-	1.3	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	-	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCVO	-	-1.5	-	-	-1.5	-	mV/°C

^{25.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 26. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

 $\textbf{ELECTRICAL CHARACTERISTICS} \; (V_{in} = 27 \; V, \; I_O = 1.0 \; A, \; T_J = T_{low} \; to \; 125^{\circ}C \; (Note \; 27), \; unless \; otherwise \; noted)$

			MC7818AC			
Characteristic	Symbol	Min	Min Typ Max		Unit	
Output Voltage (T _J = 25°C)	Vo	17.64	18	18.36	Vdc	
Output Voltage (5.0 mA \leq I $_{O} \leq$ 1.0 A, P $_{D} \leq$ 15 W) 21 Vdc \leq V $_{in} \leq$ 33 Vdc	Vo	17.3	18	18.7	Vdc	
Line Regulation (Note 28) 21 Vdc \leq V $_{in} \leq$ 33 Vdc, I $_{O} =$ 500 mA 24 Vdc \leq V $_{in} \leq$ 30 Vdc, I $_{O} =$ 1.0 A 24 Vdc \leq V $_{in} \leq$ 30 Vdc, I $_{O} =$ 1.0 A, T $_{J} =$ 25°C 20.6 Vdc \leq V $_{in} \leq$ 33 Vdc, I $_{O} =$ 1.0 A, T $_{J} =$ 25°C	Reg _{line}	- - - -	9.5 3.2 3.2 8.0	22 25 10.5 22	mV	
Load Regulation (Note 28) 5.0 mA \leq I $_{O} \leq$ 1.5 A, T $_{J} = 25^{\circ}$ C 5.0 mA \leq I $_{O} \leq$ 1.0 A 250 mA \leq I $_{O} \leq$ 750 mA	Reg _{load}	- - -	2.0 1.8 1.5	25 25 15	mV	
Quiescent Current	I _B	-	3.5	6.0	mA	
Quiescent Current Change 21 Vdc \leq V $_{in}$ \leq 33 Vdc, I $_{O}$ = 500 mA 21.5 Vdc \leq V $_{in}$ \leq 30 Vdc, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	Δl_{B}	- - -	- - -	0.8 0.8 0.5	mA	
Ripple Rejection 22 Vdc \leq V _{in} \leq 32 Vdc, f = 120 Hz, I _O = 500 mA	RR	53	57	-	dB	
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	Vdc	
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	_	μV/V _O	
Output Resistance f = 1.0 kHz	r _O	-	1.3	-	mΩ	
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	Α	
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	-	Α	
Average Temperature Coefficient of Output Voltage	TCV _O	-	-1.5	-	mV/°C	

^{27.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 28. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS ($V_{in} = 33 \text{ V}$, $I_O = 500 \text{ mA}$, $T_J = T_{low}$ to 125°C (Note 29), unless otherwise noted)

		MC7824B		MC7824C				
Characteristic	Symbol	Min	Тур	Max	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	23	24	25	23	24	25	Vdc
Output Voltage (5.0 mA \leq I $_O \leq$ 1.0 A, P $_D \leq$ 15 W) 27 Vdc \leq V $_{in} \leq$ 38 Vdc 28 Vdc \leq V $_{in} \leq$ 38 Vdc	Vo	- 22.8	- 24	_ 25.2	22.8 -	24 -	25.2 -	Vdc
Line Regulation, (Note 30) 27 Vdc \leq V _{in} \leq 38 Vdc 30 Vdc \leq V _{in} \leq 36 Vdc	Reg _{line}	- -	11.5 3.8	480 240	- -	2.7 2.7	60 48	mV
Load Regulation, (Note 30) 5.0 mA ≤ I _O ≤ 1.5 A	Reg _{load}	-	2.1	480	-	4.4	65	mV
Quiescent Current	Ι _Β	-	3.6	8.0	-	3.6	6.5	mA
	Δl_{B}	- -	- -	_ 0.5	_ _	_ _	1.0 0.5	mA
Ripple Rejection 28 Vdc ≤ V _{in} ≤ 38 Vdc, f = 120 Hz	RR	-	54	-	50	54	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	-	-	2.0	_	Vdc
Output Noise Voltage (T _A = 25°C) 10 Hz ≤ f ≤ 100 kHz	V _n	-	10	-	-	10	-	μV/V _O
Output Resistance f = 1.0 kHz	r _O	-	1.4	-	-	1.4	-	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	_	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	_	2.2	-	_	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCV _O	_	-2.0	_	_	-2.0	_	mV/°C

^{29.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 30. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

ELECTRICAL CHARACTERISTICS ($V_{in} = 33 \text{ V}$, $I_{O} = 1.0 \text{ A}$, $T_{J} = T_{low}$ to 125°C (Note 31), unless otherwise noted)

			MC7824AC		
Characteristic	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = 25°C)	Vo	23.5	24	24.5	Vdc
Output Voltage (5.0 mA \leq I $_{O}$ \leq 1.0 A, P $_{D}$ \leq 15 W) 27.3 Vdc \leq V $_{in}$ \leq 38 Vdc	Vo	23.2	24	25.8	Vdc
Line Regulation (Note 32) 27 Vdc \leq V _{in} \leq 38 Vdc, I _O = 500 mA 30 Vdc \leq V _{in} \leq 36 Vdc, I _O = 1.0 A 30 Vdc \leq V _{in} \leq 36 Vdc, T _J = 25°C 26.7 Vdc \leq V _{in} \leq 38 Vdc, I _O = 1.0 A, T _J = 25°C	Reg _{line}	- - - -	11.5 3.8 3.8 10	25 28 12 25	mV
Load Regulation (Note 32) 5.0 mA \leq I $_{O} \leq$ 1.5 A, T $_{J} = 25^{\circ}$ C 5.0 mA \leq I $_{O} \leq$ 1.0 A 250 mA \leq I $_{O} \leq$ 750 mA	Reg _{load}	- - -	2.1 2.0 1.8	15 25 15	mV
Quiescent Current	I _B	-	3.6	6.0	mA
Quiescent Current Change 27.3 Vdc \leq V $_{in}$ \leq 38 Vdc, I $_{O}$ = 500 mA 27 Vdc \leq V $_{in}$ \leq 38 Vdc, T $_{J}$ = 25°C 5.0 mA \leq I $_{O}$ \leq 1.0 A	Δl_{B}	- - -	- - -	0.8 0.8 0.5	mA
Ripple Rejection 28 Vdc \leq V _{in} \leq 38 Vdc, f = 120 Hz, I _O = 500 mA	RR	45	54	-	dB
Dropout Voltage (I _O = 1.0 A, T _J = 25°C)	V _I – V _O	-	2.0	_	Vdc
Output Noise Voltage ($T_A = 25^{\circ}C$) 10 Hz \leq f \leq 100 kHz	V _n	-	10	-	μV/V _O
Output Resistance (f = 1.0 kHz)	r _O	_	1.4	_	mΩ
Short Circuit Current Limit (T _A = 25°C) V _{in} = 35 Vdc	I _{SC}	-	0.2	-	А
Peak Output Current (T _J = 25°C)	I _{max}	-	2.2	_	Α
Average Temperature Coefficient of Output Voltage	TCV _O	-	-2.0	_	mV/°C

^{31.}T_{low} = 0°C for MC78XXC, MC78XXAC, = -40°C for NCV78XX, MC78XXB, MC78XXAB, and MC78XXAEB 32. Load and line regulation are specified at constant junction temperature. Changes in V_O due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.

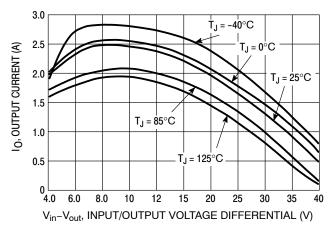


Figure 2. Peak Output Current as a Function of Input/Output Differential Voltage (MC78XXC, AC, B)

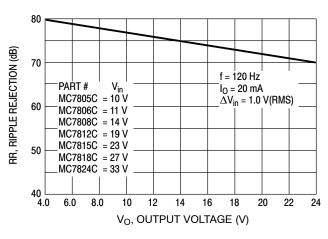


Figure 3. Ripple Rejection as a Function of Output Voltages (MC78XXC, AC, B)

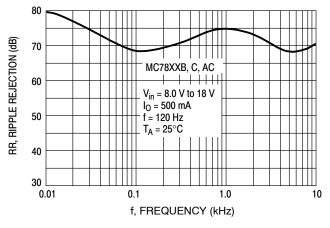


Figure 4. Ripple Rejection as a Function of Frequency (MC78XXC, AC, B)

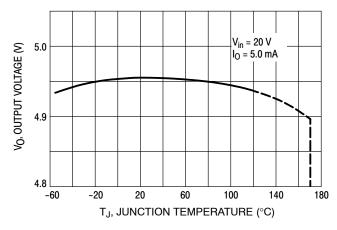


Figure 5. Output Voltage as a Function of Junction Temperature (MC7805C, AC, B)

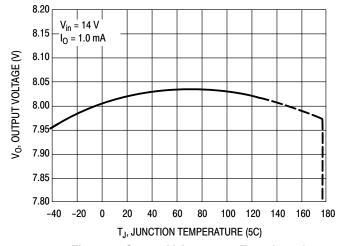


Figure 6. Output Voltage as a Function of Junction Temperature (MC7808AE)

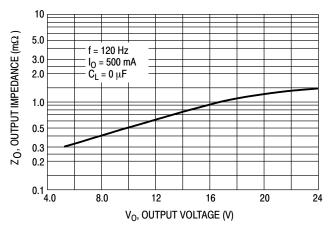


Figure 7. Output Impedance as a Function of Output Voltage (MC78XXC, AC, B)

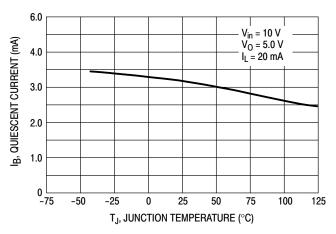


Figure 8. Quiescent Current as a Function of Temperature (MC78XXC, AC, B)

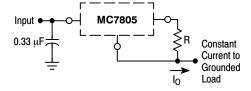
APPLICATIONS INFORMATION

Design Considerations

The MC7800 Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition, Internal Short Circuit Protection that limits the maximum current the circuit will pass, and Output Transistor Safe–Area Compensation that reduces the output short circuit current as the voltage across the pass transistor is increased.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long

wire lengths, or if the output load capacitance is large. An input bypass capacitor should be selected to provide good high–frequency characteristics to insure stable operation under all load conditions. A 0.33 μF or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Normally good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead.



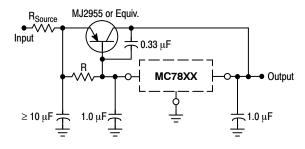
The MC7800 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC7805C is chosen in this application. Resistor R determines the current as follows:

$$I_0 = \frac{5.0 \text{ V}}{\text{R}} + I_B$$

 $I_B \cong 3.2$ mA over line and load changes.

For example, a 1.0 A current source would require R to be a 5.0 Ω , 10 W resistor and the output voltage compliance would be the input voltage less 7.0 V.

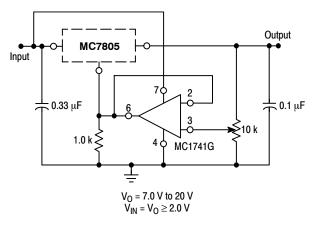
Figure 9. Current Regulator



XX = 2 digits of type number indicating voltage.

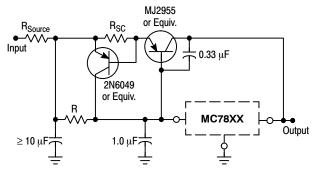
The MC7800 series can be current boosted with a PNP transistor. The MJ2955 provides current to 5.0 A. Resistor R in conjunction with the V_{BE} of the PNP determines when the pass transistor begins conducting; this circuit is not short circuit proof. Input/output differential voltage minimum is increased by V_{BE} of the pass transistor.

Figure 11. Current Boost Regulator



The addition of an operational amplifier allows adjustment to higher or intermediate values while retaining regulation characteristics. The minimum voltage obtainable with this arrangement is 2.0 V greater than the regulator voltage.

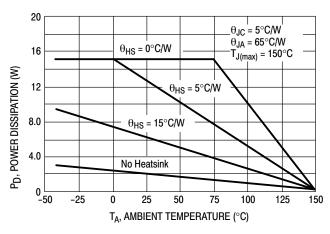
Figure 10. Adjustable Output Regulator



XX = 2 digits of type number indicating voltage.

The circuit of Figure 11 can be modified to provide supply protection against short circuits by adding a short circuit sense resistor, $R_{SC},$ and an additional PNP transistor. The current sensing PNP must be able to handle the short circuit current of the three-terminal regulator. Therefore, a four-ampere plastic power transistor is specified.

Figure 12. Short Circuit Protection



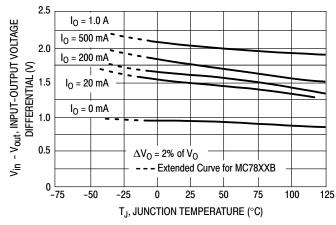


Figure 13. Worst Case Power Dissipation versus Ambient Temperature (Case 221A)

Figure 14. Input Output Differential as a Function of Junction Temperature (MC78XXC, AC, B)

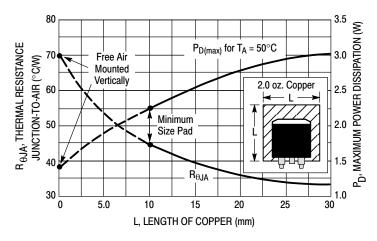


Figure 15. D²PAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

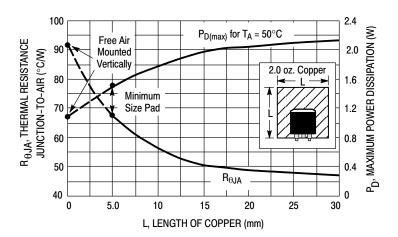


Figure 16. DPAK Thermal Resistance and Maximum Power Dissipation versus P.C.B. Copper Length

DEFINITIONS

Line Regulation – The change in output voltage for a change in the input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that the average chip temperature is not significantly affected.

Load Regulation – The change in output voltage for a change in load current at constant chip temperature.

Maximum Power Dissipation – The maximum total device dissipation for which the regulator will operate within specifications.

Quiescent Current – That part of the input current that is not delivered to the load.

Output Noise Voltage – The rms ac voltage at the output, with constant load and no input ripple, measured over a specified frequency range.

Long Term Stability – Output voltage stability under accelerated life test conditions with the maximum rated voltage listed in the devices' electrical characteristics and maximum power dissipation.

ORDERING INFORMATION

Device	Nominal Voltage	Operating Temperature Range	Package	Shipping [†]
MC7805ABD2TG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7805ABD2TR4G	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
NCV7805ABD2TR4G*	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7805ABTG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7805ACD2TG	5.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7805ACD2TR4G	5.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7805ACTG	5.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7805BD2TG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7805BD2TR4G	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7805BDTG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	75 Units / Rail
MC7805BDTRKG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
NCV7805BDTRKG*	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
MC7805BTG	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
NCV7805BTG*	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
NCV7805BD2TG*	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
NCV7805BD2TR4G*	5.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7805CD2TG	5.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7805CD2TR4G	5.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

ORDERING INFORMATION (continued)

Device	Nominal Voltage	Operating Temperature Range	Package	Shipping [†]
MC7805CDTG	5.0 V	T _J = 0°C to +125°C	DPAK (Pb-free)	75 Units / Rail
MC7805CDTRKG	5.0 V	T _J = 0°C to +125°C	DPAK (Pb-free)	2500 / Tape & Reel
MC7805CTG	5.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7806ACTG	6.0 V	$T_J = 0$ °C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7806BD2TG	6.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7806BD2TR4G	6.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7806BTG	6.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7806CTG	6.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7808ABD2TG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units / Rail
MC7808ABD2TR4G	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
NCV7808ABD2TR4G*	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7808ABTG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
MC7808ACTG	8.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units / Rail
MC7808AEBTG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
MC7808BD2TG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units / Rail
MC7808BD2TR4G	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
NCV7808BD2TR4G*	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7808BDTG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	75 Units / Rail
NCV7808BDTG*	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	75 Units / Rail
MC7808BDTRKG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
NCV7808BDTRKG*	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
MC7808BTG	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
NCV7808BTG*	8.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
MC7808CD2TG	8.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

ORDERING INFORMATION (continued)

Device	Nominal Voltage	Operating Temperature Range	Package	Shipping [†]
MC7808CD2TR4G	8.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7808CDTG	8.0 V	T _J = 0°C to +125°C	DPAK (Pb-free)	75 Units / Rail
MC7808CDTT5G	8.0 V	T _J = 0°C to +125°C	DPAK (Pb-free)	2500 / Tape & Reel
MC7808CDTRKG	8.0 V	$T_J = 0$ °C to +125°C	DPAK (Pb-free)	2500 / Tape & Reel
MC7808CTG	8.0 V	$T_J = 0$ °C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7809ABTG	9.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7809ACTG	9.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
NCV7809BD2TR4G*	9.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7809BTG	9.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
NCV7809BTG*	9.0 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7809CD2TG	9.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7809CD2TR4G	9.0 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7809CTG	9.0 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7812ABD2TG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7812ABD2TR4G	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7812ABTG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
NCV7812ABTG*	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
MC7812ACD2TG	12 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7812ACD2TR4G	12 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7812ACTG	12 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7812BD2TG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7812BD2TR4G	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
NCV7812BD2TR4G*	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7812BDTG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	75 Units / Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

ORDERING INFORMATION (continued)

Device	Nominal Voltage	Operating Temperature Range	Package	Shipping [†]
MC7812BDTRKG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
MC7812BTG	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
NCV7812BTG*	12 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7812CD2TG	12 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7812CD2TR4G	12 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7812CDTG	12 V	T _J = 0°C to +125°C	DPAK (Pb-free)	75 Units / Rail
MC7812CDTRKG	12 V	T _J = 0°C to +125°C	DPAK (Pb-free)	2500 / Tape & Reel
MC7812CTG	12 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units / Rail
MC7815ABD2TG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7815ABD2TR4G	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7815ABTG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7815ACD2TG	15 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7815ACTG	15 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units / Rail
MC7815BD2TG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7815BD2TR4G	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7815BDTG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	75 Units / Rail
MC7815BDTRKG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	DPAK (Pb-free)	2500 / Tape & Reel
MC7815BTG	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
NCV7815BTG*	15 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units / Rail
MC7815CD2TG	15 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7815CD2TR4G	15 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7815CDTG	15 V	T _J = 0°C to +125°C	DPAK (Pb-free)	75 Units / Rail
MC7815CDTRKG	15 V	T _J = 0°C to +125°C	DPAK (Pb-free)	2500 / Tape & Reel
MC7815CTG	15 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

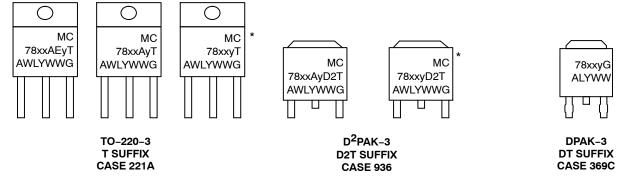
^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

ORDERING INFORMATION (continued)

Device	Nominal Voltage	Operating Temperature Range	Package	Shipping [†]
MC7818ACTG	18 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7818BTG	18 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7818CD2TR4G	18 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7818CTG	18 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7824ACTG	24 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail
MC7824BD2TG	24 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	50 Units /Rail
MC7824BD2TR4G	24 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	D ² PAK (Pb-free)	800 / Tape & Reel
MC7824BTG	24 V	$T_{J} = -40^{\circ}\text{C to } +125^{\circ}\text{C}$	TO-220 (Pb-free)	50 Units /Rail
MC7824CD2TG	24 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	50 Units /Rail
MC7824CD2TR4G	24 V	T _J = 0°C to +125°C	D ² PAK (Pb-free)	800 / Tape & Reel
MC7824CTG	24 V	T _J = 0°C to +125°C	TO-220 (Pb-free)	50 Units /Rail

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS



*This marking diagram also applies to NCV78xx family.

xx = 05, 06, 08, 09, 12, 15, 18, or 24

/ = B or C

A = Assembly Location

WL, L = Wafer Lot Y = Year

WW = Work Week
G = Pb-Free Device

^{*}NCV devices: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS





TO-220, SINGLE GAUGE CASE 221AB-01 **ISSUE A**

DATE 16 NOV 2010

- NOTES:

 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

 2. CONTROLLING DIMENSION: INCHES.

 3. DIMENSION 2 DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARTIES ARE ALLOWED.

 4. PRODUCT SHIPPED PRIOR TO 2008 HAD DIMENSIONS S = 0.045 0.055 INCHES (1.143 1.397 MM)

	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.020	0.024	0.508	0.61
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04





STYLE 1: PIN 1. 2. 3. 4.	EMITTER	STYLE 2: PIN 1. 2. 3. 4.		STYLE 3: PIN 1. 2. 3. 4.	CATHODE ANODE GATE ANODE
STYLE 5: PIN 1. 2. 3. 4.	GATE DRAIN SOURCE DRAIN	STYLE 6: PIN 1. 2. 3. 4.	ANODE CATHODE ANODE CATHODE	STYLE 7: PIN 1. 2. 3. 4.	CATHODE ANODE CATHODE ANODE
STYLE 9: PIN 1. 2. 3. 4.	GATE COLLECTOR EMITTER COLLECTOR	STYLE 10: PIN 1. 2. 3. 4.		STYLE 11: PIN 1. 2. 3. 4.	DRAIN SOURCE GATE SOURCE

STYLE 4: PIN 1. 2. 3. 4.	MAIN TERMINAL 2
2. 3.	CATHODE ANODE EXTERNAL TRIP/DELAY ANODE

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DESCRIPTION:	TO-220, SINGLE GAUGE		PAGE 1 OF 1

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b2 e **DATE 21 JUL 2015**

DPAK (SINGLE GAUGE) CASE 369C **ISSUE F** SCALE 1:1

DETAIL A

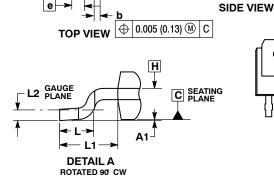
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NOTES:

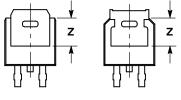
- IOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: INCHES. 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-
- MENSIONS b3, L3 and Z.
 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
 5. DIMENSIONS D AND E ARE DETERMINED AT THE
- OUTERMOST EXTREMES OF THE PLASTIC BODY.

 6. DATUMS A AND B ARE DETERMINED AT DATUM
- 7. OPTIONAL MOLD FEATURE.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.028	0.045	0.72	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
E	0.250	0.265	6.35	6.73
е	0.090	BSC	2.29 BSC	
Н	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.114 REF		2.90 REF	
L2	0.020 BSC		0.51 BSC	
L3	0.035	0.050	0.89	1.27
L4		0.040		1.01
Z	0.155		3.93	



NOTE 7



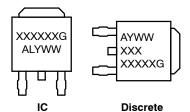
BOTTOM VIEW

BOTTOM VIEW ALTERNATE CONSTRUCTIONS

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
	2. DRAIN	2. CATHODE	2. ANODE	2. ANODE
	3. SOURCE	3. ANODE	3. GATE	3. CATHODE
	4. DRAIN	4. CATHODE	4. ANODE	4. ANODE
4. COLLECTOR	4. DRAIN	4. CATHODE	4. ANODE	4. ANODE

STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:
PIN 1. MT1	PIN 1. GATE	PIN 1. N/C	PIN 1. ANODE	PIN 1. CATHODE
2. MT2	COLLECTOR	CATHODE	2. CATHODE	ANODE
3. GATE	EMITTER	ANODE	RESISTOR ADJUST	3. CATHODE
4. MT2	COLLECTOR	4. CATHODE	4. CATHODE	4. ANODE

GENERIC MARKING DIAGRAM*



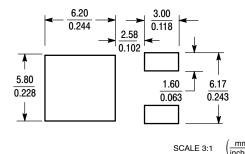
XXXXXX = Device Code = Assembly Location Α L = Wafer Lot Υ = Year WW = Work Week

G

*This information is generic. Please refer to device data sheet for actual part marking.

= Pb-Free Package

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

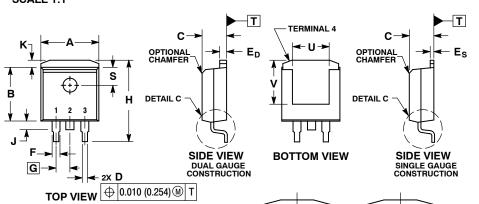
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DESCRIPTION:	DPAK (SINGLE GAUGE)		PAGE 1 OF 1

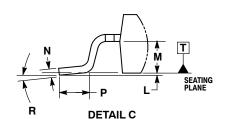
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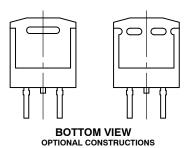


D²PAK CASE 936-03 ISSUE E

DATE 29 SEP 2015







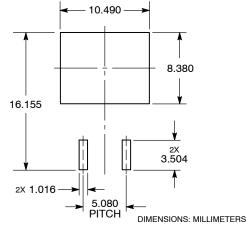
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCHES. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS

- A AND K.
 DIMENSIONS U AND V ESTABLISH A MINIMUM
- MOUNTING SURFACE FOR TERMINAL 4.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD
 FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.
- SINGLE GAUGE DESIGN WILL BE SHIPPED AF-TER FPCN EXPIRATION IN OCTOBER 2011.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.386	0.403	9.804	10.236	
В	0.356	0.368	9.042	9.347	
С	0.170	0.180	4.318	4.572	
D	0.026	0.036	0.660	0.914	
ED	0.045	0.055	1.143	1.397	
Es	0.018	0.026	0.457	0.660	
F	0.051 REF		1.295 REF		
G	0.100 BSC		2.540 BSC		
Н	0.539 0.579		13.691	14.707	
J	0.125 MAX		3.175 MAX		
K	0.050	0.050 REF		1.270 REF	
L	0.000	0.010	0.000	0.254	
M	0.088	0.102	2.235	2.591	
N	0.018	0.026	0.457	0.660	
P	0.058	0.078	1.473	1.981	
R	0°	8°	0°	8°	
S	0.116 REF		2.946 REF		
U	0.200 MIN		5.080 MIN		
V	0.250 MIN		6.350 MIN		

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXXXXX = Specific Device Code

= Assembly Location = Wafer Lot 1

= Year Υ ww = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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