GA04 Deliverable

M.U.P

massively underdeveloped project

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**Title page** A table where you describe the contribution to ideas and documentation of each team member (in percent).

**Evaluation result** A description of:

1. the changes made to the model in order to perform the evaluation

Each subsystem now has its own budget for MIPS and RAM resources, based upon the specification given in the assignment.

The predefined flow paths for Device Manager and Safety Manager have been connected successfully. Furthermore, we added a rotation\_inout\_flow to the Safety Manager for usage in scenario 2.

In the system configuration we created 3 different flow paths, the first one (F1) for scenario 1 and the last two (F2 & F3) for scenario 2.

We assigned the subsystems to processor and their memory banks based upon configuration 2 & 3 as can be seen in our AADL model. Based upon the specification we concluded that we could not assign the subsystems in configuration 1.

2. the result of the evaluations and your conclusions

Our result for scenario 1 the highest latency was 265ms, this was well below the requirement given. Our conclusion is that given the current system architecture the system fulfils the requirements.

The result for scenario 2 had a latency of 435ms with the asynchronous test and a latency of 345ms with the synchronous test. Our conclusion is that for the system to fulfil the requirements, the system has to be run synchronous.

**Challenges** A discussion (max. 1 page) of the challenges you have encountered in:

1. understanding

2. extending

the AADL model. Describe also how you addressed a specific challenge and whether you

perceive that you have succeeded or not.

the shit sucked

**Reflection** A discussion (max. 1 page) on the benefits and liabilities of using a formal architecture specification and evaluation compared to the evaluation you performed in Assignment 3