

D:/gitt/MicrochipFor32
/bsp_MPU6050/inv_mpu.h

```
graph TD; A["D:/gitt/MicrochipFor32  
/bsp_MPU6050/inv_mpu.h"] --> B["main.h"]; A --> C["varint.h"]; C --> D["stdint.h"];
```

main.h

varint.h

stdint.h