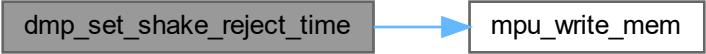


dmp\_set\_shake\_reject\_time



```
graph LR; A[dmp_set_shake_reject_time] --> B[mpu_write_mem]
```

A diagram showing a flow from a gray box labeled 'dmp\_set\_shake\_reject\_time' to a white box labeled 'mpu\_write\_mem'. A blue arrow points from the gray box to the white box.

mpu\_write\_mem