

D:/gitt/MicrochipFor32
/bsp_Device/bsp_Calculus.h

```
graph TD; A["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_Calculus.h"] --> B["stdlib.h"]; A --> C["varint.h"]; C --> D["stdint.h"];
```

stdlib.h

varint.h

stdint.h