

D:/gitt/MicrochipFor32
/bsp_Device/bsp_math/bsp_CRC.h

```
graph BT; A["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_ModBus  
/bsp_ModBus.cpp"] --> C["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_math/bsp_CRC.h"]; B["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_math/bsp_CRC.cpp"] --> C;
```

The diagram illustrates a dependency relationship in a code project. At the top, a grey box represents the header file `bsp_CRC.h` located at `D:/gitt/MicrochipFor32/bsp_Device/bsp_math/`. Below it, two white boxes represent source files. The left box is `bsp_ModBus.cpp` at `D:/gitt/MicrochipFor32/bsp_Device/bsp_ModBus/`, and the right box is `bsp_CRC.cpp` at `D:/gitt/MicrochipFor32/bsp_Device/bsp_math/`. Blue arrows point from each source file box up to the header file box, indicating that both source files include or depend on the `bsp_CRC.h` header.

D:/gitt/MicrochipFor32
/bsp_Device/bsp_ModBus
/bsp_ModBus.cpp

D:/gitt/MicrochipFor32
/bsp_Device/bsp_math/bsp
_CRC.cpp