

D:/gitt/MicrochipFor32  
/bsp\_Device/bsp\_ModBus  
/bsp\_ModBus.h

```
graph TD; A["D:/gitt/MicrochipFor32 /bsp_Device/bsp_ModBus /bsp_ModBus.h"] --> B["varint.h"]; A --> C["usart.h"]; A --> D["stdarg.h"]; B --> E["stdint.h"];
```

varint.h

usart.h

stdarg.h

stdint.h