

D:/gitt/MicrochipFor32
/STL_Device/STL_Stack.h

```
graph TD; A["D:/gitt/MicrochipFor32  
/STL_Device/STL_Stack.h"] --> B["varint.h"]; A --> C["stdlib.h"]; B --> D["stdint.h"];
```

varint.h

stdlib.h

stdint.h