

D:/gitt/MicrochipFor32  
/bsp\_Device/bsp\_MAX6675.h

```
graph TD; Root["D:/gitt/MicrochipFor32 /bsp_Device/bsp_MAX6675.h"] --> varint["varint.h"]; Root --> spi["spi.h"]; varint --> stdint["stdint.h"]
```

varint.h

spi.h

stdint.h