

D:/gitt/MicrochipFor32
/bsp_Device/bsp_IIC_STM32.h

```
graph TD; Root["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_IIC_STM32.h"] --> gpio["gpio.h"]; Root --> bsp_IIC["bsp_IIC.h"]; Root --> varint["varint.h"]; bsp_IIC --> stdint["stdint.h"]; varint --> stdint;
```

gpio.h

bsp_IIC.h

varint.h

stdint.h