

D:/gitt/MicrochipFor32  
/bsp\_Device/bsp\_math/bsp  
\_CRC.cpp

```
graph TD; A["D:/gitt/MicrochipFor32  
/bsp_Device/bsp_math/bsp  
_CRC.cpp"] --> B["bsp_CRC.h"]; B --> C["varint.h"]; C --> D["stdint.h"];
```

bsp\_CRC.h

varint.h

stdint.h