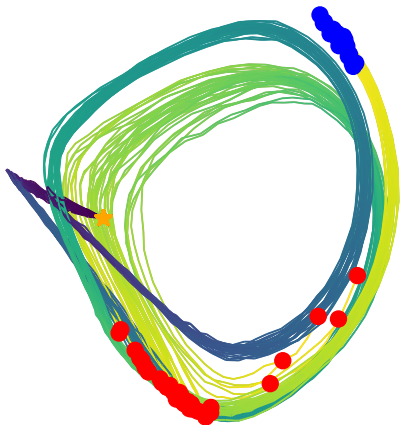


type1_model_21h; delay=1800

integ-PC2



integ-PC1