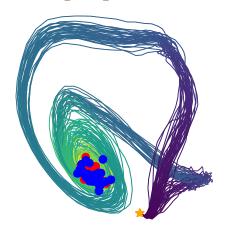
type1_model_8h; delay=1800



integ-PC2

integ-PC1