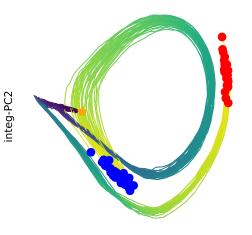
type1\_model\_21h; delay=900



integ-PC1