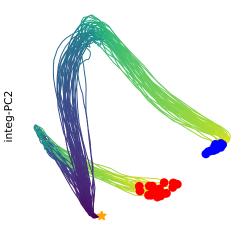
type1_model_8h; delay=300



integ-PC1