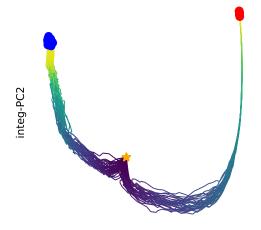
type0_model_3h; delay=900



integ-PC1