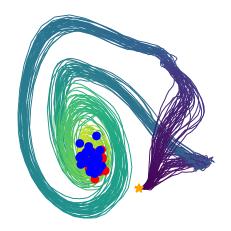
type1_model_8h; delay=1500



integ-PC2

integ-PC1