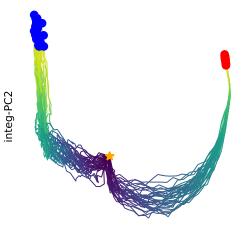
type0_model_3h; delay=300



integ-PC1