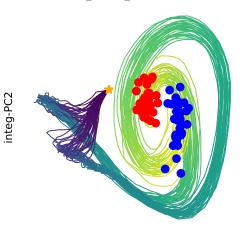
type1_model_8h; delay=900



integ-PC1