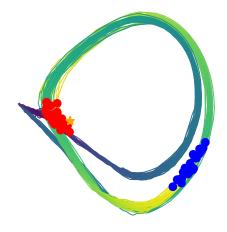
## type1\_model\_21h; delay=1500



integ-PC2

integ-PC1