$rcp\_tofino\_stateless\_alu\_for\_tofino\_1\_3\_stateless\_alu\_0\_0\_table$ ALL  $rcp\_tofino\_stateless\_alu\_for\_tofino\_1\_3\_stateless\_alu\_0\_2\_table$ ALL rcp\_tofino\_stateless\_alu\_for\_tofino\_1\_3\_stateful\_alu\_0\_0\_table ALL rcp\_tofino\_stateless\_alu\_for\_tofino\_1\_3\_stateful\_alu\_0\_1\_table ALL rcp\_tofino\_stateless\_alu\_for\_tofino\_1\_3\_stateful\_alu\_0\_2\_table ALL  $mac\_forward$ ALL buffer egress