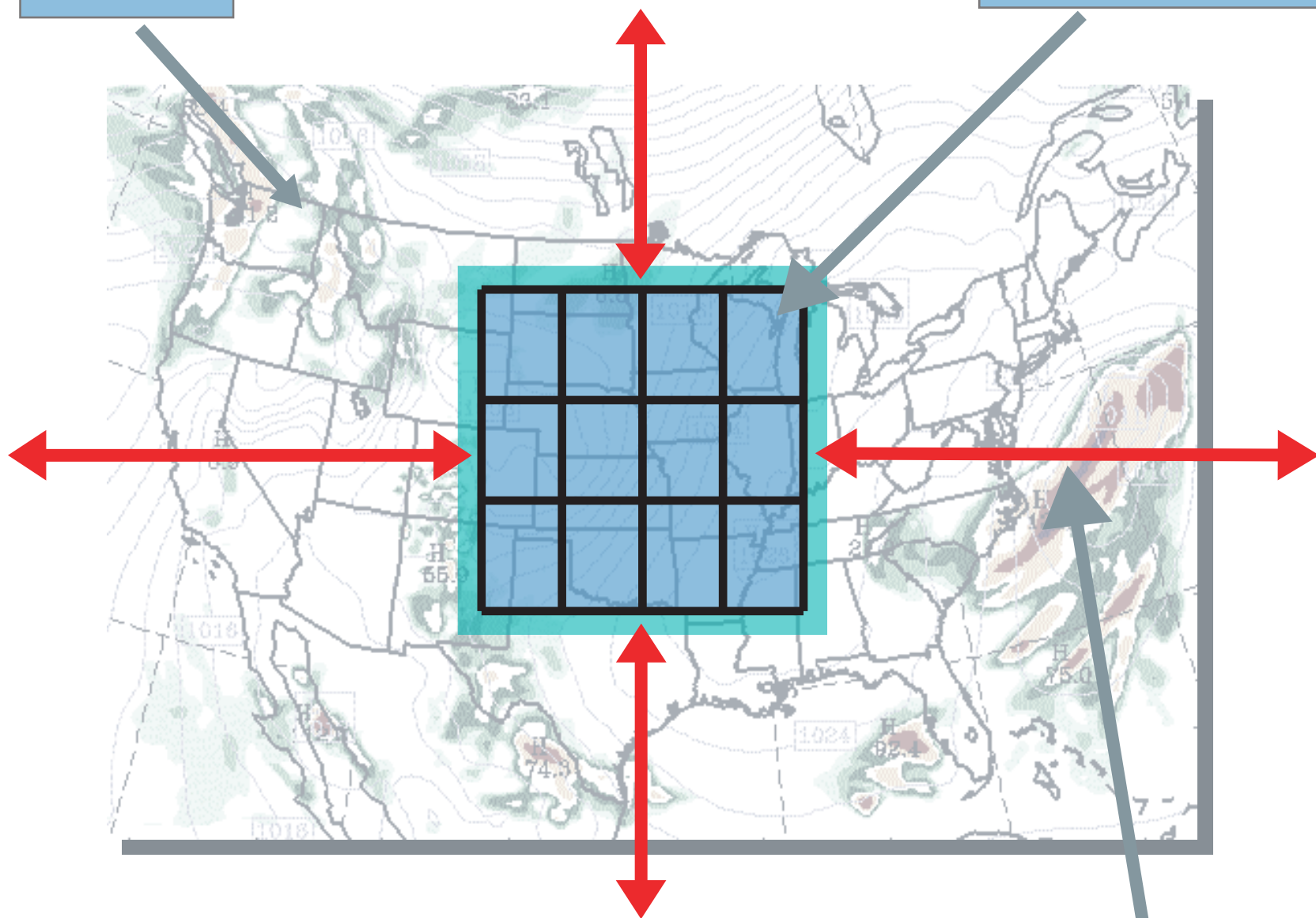


Logical domain

1 Patch, divided into multiple tiles



Inter-processor communication