

# XIAOKANG CHEN

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<https://xiaokchenedu.github.io/>

## EDUCATION

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### University of Hawai'i at Mānoa

Honolulu, Hawaii

*Bachelor of Science, Computer Engineering*

*Aug 2021 – Expected Graduation Fall 2024*

- BAM (Combined Bachelor's & Master's Degree) program for BS Computer Engineering/MS Electrical Engineering
- As a computer/electrical engineer, my primary interest lies in hardware/software design such as designing/building circuit boards and computer software.

## RELEVANT COURSEWORK

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**Electrical:** Basic Circuit Analysis I, Basic Circuit Analysis II, Physical Electronics, Discrete Math for Engineers, Engineering Electromagnetic I.

**Computer:** Programming for Engineers (Python), Object Oriented Programming (C/C++), Introduction to Digital Design, Introduction to Computer and Network Security, Digital Systems and Computer Design.

## EXPERIENCE

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### Cyber Security Training

Honolulu, Hawaii

*Office of Naval Research ROTC Cyber Security Training Program*

*Sep 2022 – May 2023*

- Learn the basic of Linux and security tools to examine computer and network security.
- Work on computer and network security, drones, Internet of Things (IoT), cloud, and mobile systems.
- Virtualization through type 2 hypervisor.

## PROJECTS / PROGRAM

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### Aerospace Technologies: Team Laniākea | <https://www.teamlaniakea.com/>

*Jan 2024 – Present*

- Create a functional, low cost satellite to take a picture of the Hawaiian Islands from space.
- Engaged in tasks involving solar cells and antennas.

### UHM SCADA LAB | <https://uhm-scada-lab.github.io/>

*Sep 2023 – Dec 2023*

- Designed and implemented a miniature town model to visually represent the principles of two closed-loop systems and one open-loop system.
- Engaged in Raspberry Pi programming using Python, concurrently applying circuit design principles that incorporated relays, motors, water level sensors, and other components.

### ALU Project | <https://xiaokchenedu.github.io/projects/ALU.html>

*Nov 2022 – Dec 2022*

- Created a combinational digital circuit featuring 8 distinct operations, each operating on 4 bits.
- Designed and simulated the 8 different operations using Falstad, and subsequently implemented the combinational circuit in Verilog

### UHM ICSpark Program | <https://icspark.github.io/>

*Jun 2021 – Aug 2021*

- Intro to Web Development Course.

## AWARDS & ACHIEVEMENTS

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**UHM Physics Olympic 2020:** 28 team challenge each other in five different events based on concepts of physics, our team won 3 event with 2 second place and 1 third place.

## TECHNICAL SKILLS

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**Computing:** C, C++, Python, HTML/CSS, Javascript, MATLAB, LaTeX, Verilog, VHDL

**Tools:** Visual Studio Code, JetBrains, Vim, Git, Linux/Unix, VirtualBox, VMWare

**Languages:** Mandarin (Native), English (Fluent)