# Xiaoxuan Yang

Ph.D. Candidate Electrical and Computer Engineering Department Duke University

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I am currently on the 2022-2023 academic job market and looking for tenure-track assistant professor position openings.

#### Research Interests

- Efficient Processing-in-Memory-based System Design
- Robust and Reliable Hardware-Software Co-Design for Non-Volatile Memory
- Generalized Machine Learning Algorithm for Robustness

### Education

**Duke University** 

Expected: May 2023 Advisors: Dr. Hai (Helen) Li, Dr. Yiran Chen Ph.D. Candidate in Electrical and Computer Engineering

Thesis: Improving the efficiency and robustness of in-memory computing in emerging technologies.

University of California, Los Angeles

M.S. in Electrical Engineering Advisor: Dr. Ramin Ramezani

Tsinghua University

Jul. 2016

Jun. 2018

B.S. in Electrical Engineering Advisor: Dr. Chen Shen

Thesis: Power system transient stability evaluation method based on measurement.

#### Awards

- Selected for Rising Stars in Electrical Engineering and Computer Science (EECS) @ UT Austin, 2022
- Best Research Award at ACM SIGDA Ph.D. Forum at Design Automation Conference (DAC), 2022
- ACM Travel Grant, 2022
- Duke Graduate School Conference Travel Award, 2022
- IGSC Student Travel Award in International Green and Sustainable Computing Conference (IGSC), 2021
- Duke Electrical and Computer Engineering (ECE) Diversity Award, 2018
- Henry Samueli Fellowship, UCLA, 2018
- Zheng-Geru Academic Scholarship, Tsinghua University, 2015
- Cai-Xiong Academic Scholarship, Tsinghua University, 2013

## Publications

## Journal Articles

- [1] X. Yang, H. Yang, J. R. Doppa, P. P. Pande, K. Chakrabarty, and H. H. Li. "ESSENCE: Exploiting Structured Stochastic Gradient Pruning for Endurance-aware ReRAM-based In-Memory Training Systems." To appear in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD).
- [2] C. Wu, X. Yang, Y. Chen, and M. Li. "Photonic Bayesian Neural Network using Programmed Optical Noises." IEEE Journal of Selected Topics in Quantum Electronics (JSTQE), in revision.
- [3] X. Yang, C. Wu, M. Li, and Y. Chen. "Tolerating Noise Effects in Processing-in-Memory Systems for Neural Networks: A Hardware—Software Codesign Perspective". Advanced Intelligent System, 2200029 (2022).
- [4] X. Yang\*, B. Taylor\*, A. Wu, Y. Chen, and L. O. Chua. "Research Progress on Memristor: From Synapses to Computing Systems." IEEE Transactions on Circuits and Systems I: Regular Papers (TCAS-I), vol. 69, no. 5, pp. 1845-1857, May 2022. (\* denotes the equal contribution) [Selected as TCAS-I Highlight]
- [5] C. Wu, X. Yang, H. Yu, R. Peng, I. Takeuchi, Y. Chen, and M. Li. "Harnessing Optoelectronic Noises in a Photonic Generative Network." Science Advances 8, no. 3 (2022): eabm2956.

#### Conference Proceedings

- [6] X. Yang, S. Li, Q. Zheng and Y. Chen. "Improving the Robustness and Efficiency of PIM-based Architecture by SW/HW Co-Design." To appear in Asia and South Pacific Design Automation Conference (ASP-DAC), 2023.
- [7] J. Henkel, H. H. Li, A. Raghunathan, M. B. Tahoori, S. Venkataramani, X. Yang, and G. Zervakis. "Approximate Computing and the Efficient Machine Learning Expedition." In *International Conference on Computer-Aided Design* (ICCAD), 2022.
- [8] X. Yang, H. Yang, J. Zhang, H. H. Li, and Y. Chen. "On Building Efficient and Robust Neural Network Designs." In Asilomar Conference on Signals, Systems, and Computers (ASILOMAR), 2022.
- [9] **X. Yang\***, H. Yang\*, N. Z. Gong, and Y. Chen. "HERO: Hessian-Enhanced Robust Optimization for Unifying and Improving Generalization and Quantization Performance" In Proceedings of 59th Design Automation Conference (DAC), pp. 25-30. 2022. (\* denotes the equal contribution) [Rank First in the Track]
- [10] C. Wu, X. Yang, H. Yu, I. Takeuchi, Y. Chen, and M. Li. "Optical Generative Adversarial Network based on Programmable Phase-change Photonics." In *CLEO: Science and Innovations*, pp. STu1G-3. Optical Society of America, 2021.
- [11] X. Yang, S. Belakaria, B. K. Joardar, H. Yang, J. R. Doppa, P. P. Pande, K. Chakrabarty, and H. H. Li. "Multi-Objective Optimization of ReRAM Crossbars for Robust DNN Inferencing under Stochastic Noise." In Proceedings of the 40th International Conference on Computer-Aided Design (ICCAD), pp. 1-9. 2021.
- [12] X. Yang, B. Yan, H. Li, and Y. Chen. "ReTransformer: ReRAM-based Processing-In-Memory Architecture for Transformer Acceleration." In *Proceedings of the 39th International Conference on Computer-Aided Design (ICCAD)*, pp. 1-9. 2020. [Rank First in the Track]

## Manuscripts

- [13] S. Li, E. Hanson, Q. Zheng, X. Yang, and Y. Chen. "Hardware-Algorithm Joint Optimization for Sparse DNN."
- [14] C. Wolters, B. Taylor, E. Hanson, X. Yang, U. Schlichtmann, and Y. Chen. "Biologically Plausible Learning on Neuromorphic Hardware Architectures."

#### Presentations

#### **Oral Presentations**

ICCAD, San Diego.
 Efficient Processing-in-Memory Design for Transformer-based Models.
 ASILOMAR, Hybrid.
 On Building Efficient and Robust Neural Network Designs.
 Course Seminar at George Mason University, Virtual.
 Improving the Efficiency and Robustness of In-Memory Computing in Emerging Technologies.
 ICCAD, Hybrid.
 Multi-Objective Optimization of ReRAM Crossbars for Robust DNN Inferencing under Stochastic Noise.

• ICCAD, Virtual.

Nov. 2020

ReTransformer: ReRAM-based Processing-In-Memory Architecture for Transformer Acceleration.

#### Posters

• ACM Student Research Contest at ICCAD, San Diego.	Nov. 2022
• Rising Stars Workshop 2022 at UT Austin, Austin.	Oct. 2022
• ATHENA Annual Showcase at Duke University, Durham.	Aug. 2022
• ACM SIGDA Ph.D. Forum at DAC, San Francisco. [Best Research Award]	Jul. 2022
• Intern Poster Session at KLA Corporation, Milpitas.	Aug. 2019



More information about teaching, service, and experience is available on *xiaoxuan-yang.github.io*. I am more than happy to answer questions and take suggestions.

Website QR