# Xiaoxuan Yang

Research Scientist and Incoming Assistant Professor University of Virginia

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#### Research Interests

- Efficient Processing-in-Memory-based System Design
- Robust and Reliable Hardware-Software Co-Design for Non-Volatile Memory
- Generalized Machine Learning Algorithm for Robustness

## Professional Experience

## University of Virginia (UVA)

Aug. 2023 – present

Research Scientist, Charles L. Brown Department of Electrical and Computer Engineering Incoming Assistant Professor starting in Fall 2024

Stanford University

Aug. 2023 – present

Visiting Postdoctoral Scholar, Robust Systems Group

Host: Dr. Subhasish Mitra May 2019 – Aug. 2019

KLA Corporation
Research Intern, Advanced Algorithm Group

Mentor: Dr. Heng (Helen) Liu

Sohu, Inc.
Technology Intern, Changyan Forum Group

Jun. 2017 - Aug. 2017 Mentor: Mr. Chao Chen

## Education

**Duke University** 

Jun. 2023

Ph.D. in Electrical and Computer Engineering

Advisors: Dr. Hai Helen Li and Dr. Yiran Chen

Thesis: Improving the efficiency and robustness of in-memory computing in emerging technologies.

#### University of California, Los Angeles (UCLA)

Jun. 2018

M.S. in Electrical Engineering

Advisor: Dr. Ramin Ramezani

#### Tsinghua University

Jul. 2016

B.S. in Electrical Engineering

Advisor: Dr. Chen Shen

Thesis: Power system transient stability evaluation method based on measurement.

## Awards

- Best Student Poster Award in the Area of Artificial Intelligence and Neuromorphic Engineering, First author by B.S. student Christopher Wolters, MWSCAS, 2023
- Machine Learning and Systems Rising Star, MLCommons, 2023
- Rising Scholars Postdoc Fellow, University of Virginia, School of Engineering and Applied Science, 2023
- NSF iREDEFINE Fellow, ECE Department Heads Association Annual Conference, 2023
- Third Place of ACM Student Research Competition SRC at International Conference on Computer-Aided Design (ICCAD), 2022
- Rising Star in Electrical Engineering and Computer Science (EECS), 2022
- Best Research Award at ACM SIGDA Ph.D. Forum at Design Automation Conference (DAC), 2022
- Travel Awards for CRA-WP Early & Mid-Career Mentoring Workshop 2023, iREDEFINE Workshop 2023, ACM SRC @ ICCAD 2022, ACM Ph.D. Forum @ DAC 2022, and IGSC 2021
- Duke Graduate School Conference Travel Award, 2022

- Duke Electrical and Computer Engineering Conference Travel Fellowship, 2022
- Duke Electrical and Computer Engineering Diversity Award, 2018
- Henry Samueli Fellowship, UCLA, 2018
- Zheng-Geru Academic Scholarship, Tsinghua University, 2015
- Cai-Xiong Academic Scholarship, Tsinghua University, 2013

#### **Publications**

## Journal Articles

- [1] X. Yang, H. Yang, J. R. Doppa, P. P. Pande, K. Chakrabarty, and H. H. Li. "ESSENCE: Exploiting Structured Stochastic Gradient Pruning for Endurance-aware ReRAM-based In-Memory Training Systems." IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD), vol. 42, no. 7, pp. 2187-2199, July 2023.
- [2] C. Wu, X. Yang, Y. Chen, and M. Li. "Photonic Bayesian Neural Network using Programmed Optical Noises." *IEEE Journal of Selected Topics in Quantum Electronics (JSTQE)*, vol. 29, no. 2: Optical Computing, pp. 1-6, March-April 2023.
- [3] X. Yang, C. Wu, M. Li, and Y. Chen. "Tolerating Noise Effects in Processing-in-Memory Systems for Neural Networks: A Hardware–Software Codesign Perspective". Advanced Intelligent System, 2200029 (2022).
- [4] X. Yang\*, B. Taylor\*, A. Wu, Y. Chen, and L. O. Chua. "Research Progress on Memristor: From Synapses to Computing Systems." *IEEE Transactions on Circuits and Systems I: Regular Papers (TCAS-I)*, vol. 69, no. 5, pp. 1845-1857, May 2022. [Selected as TCAS-I Highlight]
- [5] C. Wu, X. Yang, H. Yu, R. Peng, I. Takeuchi, Y. Chen, and M. Li. "Harnessing Optoelectronic Noises in a Photonic Generative Network." Science Advances 8, no. 3 (2022): eabm2956.

## Conference Proceedings

- [6] C. Wolters, B. Taylor, E. Hanson, **X. Yang**, U. Schlichtmann, and Y. Chen. "Biologically Plausible Learning on Neuromorphic Hardware Architectures." In *IEEE International Midwest Symposium on Circuits and Systems (MWSCAS)*, 2023. [Best Student Poster Award]
- [7] X. Yang, S. Li, Q. Zheng, and Y. Chen. "Improving the Robustness and Efficiency of PIM-based Architecture by SW/HW Co-Design." In *Proceedings of the 28th Asia and South Pacific Design Automation Conference (ASP-DAC)*, pp. 618-623. 2023.
- [8] J. Henkel, H. Li, A. Raghunathan, M. B. Tahoori, S. Venkataramani, X. Yang, and G. Zervakis. "Approximate Computing and the Efficient Machine Learning Expedition." In *Proceedings of the 41st International Conference on Computer-Aided Design (ICCAD)*, pp. 1-9. 2022.
- [9] X. Yang, H. Yang, J. Zhang, H. Li, and Y. Chen. "On Building Efficient and Robust Neural Network Designs." In 2022 56th Asilomar Conference on Signals, Systems, and Computers (ASILOMAR), pp. 317-321. 2022.
- [10] X. Yang\*, H. Yang\*, N. Z. Gong, and Y. Chen. "HERO: Hessian-Enhanced Robust Optimization for Unifying and Improving Generalization and Quantization Performance" In *Proceedings of 59th Design Automation Conference (DAC)*, pp. 25-30. 2022. [Rank First in the Track]
- [11] C. Wu, X. Yang, H. Yu, I. Takeuchi, Y. Chen, and M. Li. "Optical Generative Adversarial Network based on Programmable Phase-change Photonics." In *CLEO: Science and Innovations*, pp. STu1G-3. Optical Society of America, 2021.

- [12] X. Yang, S. Belakaria, B. K. Joardar, H. Yang, J. R. Doppa, P. P. Pande, K. Chakrabarty, and H. H. Li. "Multi-Objective Optimization of ReRAM Crossbars for Robust DNN Inferencing under Stochastic Noise." In *Proceedings of the 40th International Conference on Computer-Aided Design (ICCAD)*, pp. 1-9. 2021.
- [13] X. Yang, B. Yan, H. H. Li, and Y. Chen. "ReTransformer: ReRAM-based Processing-In-Memory Architecture for Transformer Acceleration." In *Proceedings of the 39th International Conference on Computer-Aided Design (ICCAD)*, pp. 1-9. 2020. [Rank First in the Track]

## Mentorship and Teaching Experience

#### • Summer Intern Mentor in Duke CEI Lab

Summer 2022

12-Week Research Project Exploration

- I mentor one high-school student on the neural network mixed quantization project with an iterative approach. The project is summarized in a technical report.
- I mentor one undergraduate student on the thesis work of biologically plausible learning on neuromorphic hardware architectures. The research paper has been accepted to MWSCAS, and the student won the student poster award for his first research paper.

## • TA for Enterprise Storage Architecture

Fall 2020

Instructor: Dr. Tyler K Bletsch

- I am the sole TA for this graduate course, working on the gradings of homework, code projects, and exams.

#### • TA for Introduction to Signals and Systems

Spring 2020

Instructor: Dr. Vahid Tarokh

- As a TA for this undergraduate course, I take part in the design and grading of homework and tests.

## • TA for Neural Signal Processing

Spring 2018

Instructor: Dr. Kao Jonathan

- I work as TA for this undergraduate- and graduate-level course. Aside from grading, I prepare the jupyter notebook templates for homework and lead two weekly discussion sections.

#### Service

#### Organizing Service

- Organizing Committee Member, 2023 NSF PI Meeting of the Computer Systems Research (CSR) Program
- Session Chair, Reconfigurable Accelerators Meet Heterogeneous Architectures at DAC 2023
- Session Chair, Repeal Murphy's Law: Avoid Errors at DAC 2022

#### Journal Review

- ACM Transactions on Design Automation of Electronic Systems (TODAES)
- ACM Transactions on Embedded Computing Systems (TECS)
- IEEE Design & Test (D&T)
- IEEE Embedded Systems Letters (ESL)
- IEEE Journal on Emerging and Selected Topics in Circuits and Systems (JETCAS)
- IEEE Journal of Exploratory Solid-State Computational Devices and Circuits (JxCDC)
- IEEE Transactions on Circuits and Systems I: Regular Papers (TCAS-I)
- IEEE Transactions on Computers (TC)
- IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD)
- IEEE Transactions on Emerging Topics in Computing (TETC)
- IEEE Transactions on Neural Networks and Learning Systems (TNNLS)
- IEEE Transactions on Very Large Scale Integration Systems (TVLSI)
- Science China Information Sciences

#### Conference Review

• ACM Student Research Contest (SRC) at International Conference on Computer-Aided Design (ICCAD)

- AAAI Conference on Artificial Intelligence (AAAI)
- Embedded System Software Competition (ESSC) at Embedded Systems Week (ESWEEK)
- IEEE International Conference on Artificial Intelligence Circuits & Systems (AICAS)
- Asilomar Conference on Signals, Systems, and Computers (ASILOMAR)

## University Service

• Panelist for "Science & Engineering Exploration in Durham (SEED)" at 2022 First Year Students Orientation

## **Education Outreach**

• Volunteer in "COSMOS Education Toolkit @ Inspiring Minds" at Hillside High School, Durham

## **Presentations**

Oral Presentations	
• ASP-DAC, Hybrid. Improving the Robustness and Efficiency of PIM-based Architecture by SW/HW Co-design	Jan. 2023
• ICCAD, San Diego. Efficient Processing-in-Memory Design for Transformer-based Models	Nov. 2022
• ACM Student Research Contest at ICCAD (Final Round), San Diego. [Third Place] Improving the Efficiency and Robustness of In-Memory Computing in Emerging Technologies	Oct. 2022
• ASILOMAR, Hybrid. On Building Efficient and Robust Neural Network Designs	Oct. 2022
• ICCAD, Hybrid.  Multi-Objective Optimization of ReRAM Crossbars for Robust DNN Inferencing under Stochastic	Nov. 2021 c Noise
• ICCAD, Virtual. ReTransformer: ReRAM-based Processing-In-Memory Architecture for Transformer Acceleration	Nov. 2020

## **Invited Seminars**

• Improving the Efficiency and Robustness of In-Memory Computing in Emerging Technologies	
Mar. 2023	
Feb. 2023	
Oct. 2022	