

Xiaoyang Lu

917-755-1369 | xlu40@hawk.iit.edu | 819 Pomeroon Street, Naperville, IL

EDUCATION

Illinois Institute of Technology

Ph.D. in Computer Science, Department of Computer Science

Chicago, IL

Aug 2017 – May 2024

New York University

M.S. in Computer Engineering, Department of Electrical and Computer Engineering

New York, NY

Aug 2015 – May 2017

Zhejiang University

B.E. in Electronic Science and Technology

Hangzhou, China

Aug 2011 – July 2015

RESEARCH EXPERIENCE

Research Assistant Professor

June 2023 – Present

Illinois Institute of Technology

Chicago, IL

- Conduct comprehensive research in memory-centric computer architectures, scalable memory systems, and high-performance computing domains
- Explore hardware/software co-design accelerators for ML workloads, significantly improving computational speed and efficiency
- Investigate the potential of processing in-memory (PIM) architectures to reduce data movement and increase computational efficiency

Research Assistant

Jan 2020 – May 2023

Illinois Institute of Technology

Chicago, IL

- Focused on memory performance optimization, developing advanced models and innovating ML-assisted architectural designs
- Created intelligent frameworks to boost cache performance, emphasizing efficiency and streamlined design
- Developed advanced machine learning models to optimize computer architecture and system performance
- Mentored graduate students in their research projects, enhancing their academic growth and practical skills

Research Aide

May 2020 – Aug 2020

Argonne National Laboratory

Lemont, IL

- Conducted comprehensive performance testing on disaggregated memory systems, identifying key areas for improvement
- Developed and refined performance models for disaggregated memory systems, enhancing predictive accuracy and system efficiency
- Quantified and mitigated interference in disaggregated memory systems, ensuring optimal operation and reliability

PUBLICATIONS

- [ASPLOS 2024] ACES: Accelerating Sparse Matrix Multiplication with Adaptive Execution Flow and Concurrency-Aware Cache Optimizations
Xiaoyang Lu*, Boyu Long*, Xiaoming Chen, Yinhe Han, Xian-He Sun
In the Proceedings of the International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS), 2024
- [HPCA 2024] CHROME: Concurrency-Aware Holistic Cache Management Framework with Online Reinforcement Learning
Xiaoyang Lu, Hamed Najafi, Jason Liu, Xian-He Sun
In the Proceedings of the International Symposium on High-Performance Computer Architecture (HPCA), 2024
- [HPCA 2023] CARE: A Concurrency-Aware Enhanced Lightweight Cache Management Framework
Xiaoyang Lu, Rujia Wang, Xian-He Sun
In the Proceedings of the International Symposium on High-Performance Computer Architecture (HPCA), 2023

- **[JCST 2023]** The Memory-Bounded Speedup Model and its Impacts in Computing
Xian-He Sun, **Xiaoyang Lu**
Journal of Computer Science and Technology, 2023, 38(1): 64-79
- **[WSC 2022]** A Generalized Model For Modern Hierarchical Memory System
Hamed Najafi, **Xiaoyang Lu**, Jason Liu, Xian-He Sun
In the Proceedings of the Winter Simulation Conference (WSC), 2022
- **[ICCD 2021]** Premier: A Concurrency-Aware Pseudo-Partitioning Framework for Shared Last-Level Cache
Xiaoyang Lu, Rujia Wang, Xian-He Sun
In the Proceedings of the 39th International Conference on Computer Design (ICCD), 2021
- **[ISLPED 2021]** CoPIM: A Concurrency-Aware PIM Workload Offloading Architecture for Graph Applications
Liang Yan, Mingzhe Zhang, Rujia Wang, Xiaoming Chen, Xingqi Zou, **Xiaoyang Lu**, Yinhe Han, Xian-He Sun
In the Proceedings of the International Symposium on Low Power Electronics and Design (ISLPED), 2021
- **[ICCD 2020]** APAC: An Accurate and Adaptive Prefetch Framework with Concurrent Memory Access Analysis
Xiaoyang Lu, Rujia Wang, Xian-He Sun
In the Proceedings of the 38th International Conference on Computer Design (ICCD), 2020

LEADERSHIP EXPERIENCE

Teaching Assistant

Aug 2017 – May 2022

Illinois Institute of Technology

Chicago, IL

- Assisted in teaching five graduate courses, each with 9-60 students, covering topics such as Java Programming (CS 401), Software Engineering (CS 487), Parallel and Distributed Processing (CS 546), Advanced Operating Systems (CS 550), and Advanced Computer Architecture (CS 570)
- Developed and prepared comprehensive course materials, including laboratory experiments, lectures, exams, homework, and practice problems
- Led weekly lab sessions and problem-solving discussions for groups of up to 30 students, enhancing their understanding and application of course materials
- Supervised and guided students in final projects, provided detailed feedback, and graded exams and weekly homework assignments

ACADEMIC HONORS AND AWARDS

- 2024 DAC PhD Forum Travel Grant
- 2024 Illinois Institute of Technology College of Computing Best Poster Award
- 2024 ASPLOS Student Travel Award
- 2023 Top 100 Chips Achievements (2022-2023)
- 2023 HPCA Student Travel Award
- 2015 New York University Scholarship
- 2015 Zhejiang University Excellent Bachelor Thesis Award

SERVICES

Invited Reviewer for Journals & Transactions:

- IEEE Transactions on Parallel and Distributed Systems
- IEEE Transactions on Network Science and Engineering
- Journal of Systems Architecture
- Future Generation Computer Systems
- Simulation: Transactions of the Society for Modeling and Simulation International