Francisco Marí Prats

Contact information

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🐙 github.com/XicuM

Languages

Catalan Native speaker

Spanish Native speaker

English B2 First Certificate

About me

Birth Date 23/06/1999

I am enthusiastic and enjoy learning new things and finding practical solutions to everyday challenges. I have experience working in multidisciplinary teams, where I focus on being organized and dependable to help achieve our goals efficiently.

Interests and hobbies

Programming | Philosophy

Aikido | Graphic design

Water skiing | Guitar playing

Electronic Engineer with 2 years of experience in **BCN eMotorsport**, the Formula Student team from Barcelona, where specialized in **Power Electronics** and took the role of **Team Leader**. Now, I am researching about AI hardware accelerators at at the Barcelona Supercomputing Center.

Professional experience

Dec 2024 - Present
Research Engineer
Barcelona Supercomputing Center, *Barcelona*

- Implementation in RTL (SystemVerilog) and evaluation of a domain-specific hardware accelerator for sparse tensor based AI workloads, in the OpenPiton framework.

Sep 2023 - Sep 2024
Team Leader
BCN eMotorsport, *Universitat Politècnica de Catalunya*

- Led and coordinated departments in a Formula Student team focused on designing, manufacturing and testing an electric, driverless car.
- Managed team organization, documentation software, event planning, budgeting, and sponsorship agreements.

Feb 2022 - Sep 2024
Power Electronics Engineer
BCN eMotorsport, *Universitat Politècnica de Catalunya*

- Modeled and simulated control algorithms for permanent magnet drives.
- Programmed embedded SoC with FPGA for three-phase inverter control.
- Worked on lithium cell behavior to enhance battery performance.

Feb 2022 - Jul 2022
Software Engineer Trainee
Premium PSU, *Hospitalet de Llobregat*

- Performed firmware programming and automation scripting for electric vehicle chargers on the Floox team at Premium PSU.

Education

Sep 2022 - Jun 2025 > Overall Grade: 8.87 / 10
Master's Degree in Electronic Engineering
ETSETB, *Universitat Politècnica de Catalunya*

Sep 2018 - Jun 2022 > Overall Grade: 9.15 / 10
B.Sc. in Electronic Engineering and Telecommunications
ETSETB, *Universitat Politècnica de Catalunya*

Technical Skills

C/C++, Python, Pandas, Matlab, Simulink, VHDL, SystemVerilog, KiCad, Altium, Xilinx Vivado, Altera Quartus II, Cadence Virtuoso, Linux, ROS1, Notion, Wordpress, Adobe inDesign, Microsoft Office, Google Drive