

Topologically sorting VDM-SL definitions for Isabelle/HOL translation

Leo Freitas¹ and Nick Battle

School of Computing, Newcastle University,
leo.freitas@newcastle.ac.uk

Abstract. There is an ecosystem of VDM libraries and extensions that includes a translation and proof environment for VDM in Isabelle ¹. Translation works for a large subset of VDM-SL and further constructs are being added on demand. A key impediment for novice users is the fact Isabelle/HOL requires all definitions to be declared before they are used, where (mutually) recursive definitions **must** be defined in tandem. In this paper, we describe a solution to this problem, which will enable wider access to the translator plugin to novice users as well real models.

Keywords: VSCode, VDM, Sorting

1 Introduction

The Vienna Development Method (VDM) has been widely used both in industrial contexts and academic ones covering several domains of the fields of Security [3, 4], Fault-Tolerance [7], Medical Devices [5], among others. We extend VDM specification support with a suite of tools and mathematical libraries ².

VDM also has an Visual Studio Code (VS Code) IDE with multiple features, including a translation strategy to Isabelle/HOL [8]. Nevertheless, the user needs to follow a strict and specific specification style. Otherwise, translation will either fail or be impossible. For example, every Isabelle/HOL theory **must** be written within a file of the same name, whereas VDM files might be moduleless or have multiple modules. Translation of such VDM modules will have to impose the required Isabelle style. Thus, users have to be aware of such issues, or else translation will fail.

There are various such issues, which are listed in various example files in the distribution, and explained in detail in [2]. These VDM “idioms” are important not only to enable translation, but also to ensure proofs will be manageable and proof strategies will be easier to identify.

Nevertheless, a major impediment for use is the fact VDM specification **must** be ordered (*i. e.*, have declarations defined before they are used). This is rather unnatural to most VDM users, which tend to write specifications top-down, from larger/complex concepts to simpler/easier definitions. Also, it makes translation of legacy models hard because they were not written with such order requirement in mind. Manual

¹ https://github.com/leouk/VDM_Toolkit/

² https://github.com/leouk/VDM_Toolkit/

rearrangement is not difficult, yet it is laborious and error prone, hence creating a barrier to entry for the translator.

Solving this specific order requirement is the key contribution of this paper. We present the solution following the style by Naur's N-Queens algorithm [6]: we provide a historical account to how various developments within the VDM tools eventually led to the possibility of module sorting.

2 Background

VDM translation is performed by traversing its abstract syntax tree (AST) and issuing corresponding Isabelle/HOL, for every part of the VDM concrete syntax that are compatible for translation. For example, VDM union types are quite expressive. They even allow some wacky definitions like this cross union selection example.

```
types
  TUnion1 = int | nat
  inv u == (is_nat(u) => u > 0) and (is_int(u) => u < 0);

  TUnion2 = seq of nat | set of real;

functions
  f: TUnion2 * TUnion1 -> bool
  f(u2, u1) ==
    (is_int(u2) =>
      ((is_(u1, seq of nat) => u2 in set elems u1)
       and
        (is_(u1, set of real) => u2 in set u1))
     )
    and
    (is_nat(u2) =>
      ((is_(u1, seq of nat) => not u2 in set elems u1)
       and
        (is_(u1, set of real) => not u2 in set u1))
     );
```

VDM union types will create a maximal type set over the united types, where invariants are preserved. This can create quite complex (and confusing) selection and invariant checking processes. Of course, rarely such specifications are written. Yet, from a translation point of view, these are some of the challenges around VDM's expressivity with respect to how they can be translated to Isabelle.

Another rather complex definition space in VDM are those for values and patterns. For example, one could define some pretty involved patterns in values and types like:

```
types
  T = set of nat inv {a,b,c} == a < b and b < c;
values
  [i,j]: seq of nat = [1,2];
  {k,m}: set of nat = {2,1};
```

The type definition states it is an ordered set of explicitly three `nat` values, whereas the values definitions bind each name to its corresponding value (*i. e.*, $i=1$, $j=2$, where k and m will be a random choice within the set!). This poses various challenges for

translation, given that Isabelle requires a stricter bindings between names, types and values. Practically, such examples are rarely an impediment (or rarely written), yet have to be handled by the sorting algorithm as well. In particular, value definitions like those of $[i, j]$ effectively create two separate value definitions for both i and j .

That means the translator caters for the subset of VDM that is “tamer” (and mostly used). The translator has a VDM analysis tool (named `exu`³), which checks for various such syntactic conondrums and limitations, telling users what to do and where problems are. Fixing such errors and warnings is important to ensure that translations are possible and without type errors for the user to solve on their own.

`exu` also provides support to prepare users for translation (*e. g.*, there are over 50 kinds of “error” and 20 “warnings” the translator can issue). For example, it checks that any function call within a function definition ought to include that function’s precondition, if one exist. Yet, one key hindrance remained: ordering of module definitions and their dependencies. This hindrance will be addressed in this paper and have recently been implemented in the latest versoin of the translator (see Section 5).

3 Important Historical Developments

Before getting to our solution, it is important to identify key (unrelated) extensions to the main VDM tools. They were the stepping stones that eventually enabled `exu` to sort definitions for translation. We highlight the motivation for such extensions, as well as how they are implemented in the main VDM tools below.

3.1 Load time problems

When working with large VDM models [?] (*e. g.*, 150+ modules, 60+ KLOC), typecheck time (*e. g.*, 2 – 4 minutes) can become a hindrance, whereas initialisation time (*e. g.*, 15+ minutes) can compromise further development.

Such long loading times occurred because the VDM typechecker performs multiple passes through the AST in order to establishes whether all modules are consistent. The number of passes depends on the number of module inter dependencies (*i. e.*, module A imports module B and vice-versa), order of declarations within modules (*i. e.*, using definitions before defining them), *etc.*. *LF: Nick, any more here?*

Users of large models would have to keep track of dependencies by carefully checking import chains. For example, we had to carefully craft dependencies and keep their documentation explicitly. This was error prone and hard to maintain.

Verbose output. To address this, an extension was added to VDMJ that would track top-level type dependencies across modules. A simple and easy solution was for the typechecker to issue verbose output about every top-level definition dependencies as it traversed the VDM AST. This required a tree visitor looking for dependencies and a mechanism for discovering free variables throughout the AST. This verbose output enabled users to know how many passes have taken place, and whether cyclic dependencies

³ The name stems from the Yoruba divinity *Èṣù* that was the gate keeper (and messenger) between mortals and deities.

existed and where. Users would then go and fix them manually. This meant one could use the verbose output to minimise passes by lowering dependencies, hence minimising load time.

Dependency and free variable visitors. In VDMJ [1], visitors are used to make specialised traversals over the AST. For identifying module ordering, there are two visitors. The dependency visitor traverses the tree [LF: Nick, explain here what the dependencies and free variable visitors do?](#).

Topological sort of module dependencies. With the information gathered about dependencies, it was possible to create a topological sort of module names, where the result would be a list of module names with the fewest passes possible. This effectively solved (in most cases) the dependency warnings from the verbose output, hence giving the end user the ordered list of module names to load. This could then be given to VDMJ at load time directly. This was enabled output of a acyclic directed graph view of module dependencies as a `dot` file.

3.2 Recursive cycles detection and removal.

[LF: Nick, maybe comment here the issues about mutual recursion and recursive type dependencies? And the tools / functionality they created?](#)

4 Exu ordering

Topological sort of dependencies worked inter modules and resolved the long load time issues. Nevertheless, it did not consider definitions within module themselves. Moreover, module dependency search did not consider local (*e. g.*, `let-in` definitions) and function name spaces, where type name spaces were not checked. It was also related to top-level definition (*i. e.*, types, values, functions, *etc.*) names rather than their structure.

This required a different topological sort algorithm than the one available for module dependencies. The new algorithm consisted in five stages:

1. Collect all named definitions
2. Process non-function space dependencies
 - visit all definition dependencies
 - create any missing `inv_T`
 - link type with definition space
3. Process function definition name-space dependencies
 - visit name-space dependencies
 - ignore recursive calls
 - link named dependencies
4. Topological sort
 - Checks whether topological sorting is needed
 - Kahn's algorithm DAG sorting of top-level names
5. Module reconstruction

- Organise top-level names to separate type from function name-spaces
- Reorder module definitions
- Optionally re-typechecks module

The first stage effectively flattens the module top-level definitions into a single list of everything being defined within the module. For example, explicit functions might define pre/postconditions as well as measures, whereas types might define invariant, equality or order predicates. Each of these definitions are explicit function themselves.

The second stage searches for top-level definition dependencies, where it creates invariant function definitions for all types that do not have them explicitly declared. This is important to ensure that every type T without explicitly state invariants still depend on the function `inv_T`. This effectively creates a function-space dependency between user-declared types, values and functions. Such synthetic constructs are structurally equivalent to what the typechecker would create had the user written something like `inv_T = true` explicitly. This caters for the way both records and named typed invariant functions are constructed. For example, records create a function from the record name to boolean, whereas a named type $S = \text{RHS}$ creates a function from the RHS type to boolean. Next, for declared values, there are no implicit functions to create, yet the named (type) dependency link is noted. This deeply process values, which can have multiple local definitions, as described in the examples above in Section 2.

The third stage searches for free-variable dependencies across all top-level declarations, taking into account local contexts, such as function parameters or locally bound variables (*e. g.*, quantified variables, let-def constructs, *etc.*). This search also finds recursive, as well as mutually recursive, function calls. The former are ignored/removed, given they are treated differently by the translator. The latter must be resolved, otherwise the topological search stage will fail, given it requires a directed acyclic graph (DAG).

LF: Nick, could you add/say (or correct) here what other differences there are between `TCDefinitionDependencies` and `TCFreeVariableVisitor`?

The fourth stage is the actual topological sort algorithm used by VDMJ to sort module names described above. It is a variation of Kahn's DAG sort algorithm⁴. The DAG sort algorithm also looks for (and does not tolerate) cyclic dependencies within the given graph. Such cyclic dependencies have to be resolved earlier. In the case of definitions, cyclic dependencies can only occur between mutually recursive type or function definitions. Fortunately, VDMJ already has a mechanism for removing cyclic dependencies, which we apply here as well. This works well in practice. In the general case, however, this might fail, depending on the complexity of the mutual recursion(s) involved. LF: Nick, anything to add here?

The first three stages are considered as the module definition processing phase. They create all the necessary structures and implicit definitions needed for sorting. Nevertheless, before the sorting itself, it is important to provide two sources of information. First, the sort has to be made based on so-called graph start points: nodes without incoming links. Second, sorting is only to be attempted if the module does contain use of definitions before their declaration. That is because if you sort an already sorted module you can get an inconsistent result (*i. e.*, a sorting order that does not prevent use before declaration).

⁴ https://en.wikipedia.org/wiki/Topological_sorting#Kahn's_algorithm

Thus, we ensure that Kahn's algorithm is only run when necessary. Whether it will be necessary is discovered based on a variation of VDMJ's verbose output warning for cyclic dependencis check. *LF: Nick, anything else to say here regarding non-stability?*

Finally, for the fifth stage, if module sorting is not required, then the topological sorting is skipped and the module is not modified. Otherwise, if the sorting is required, then it is necessary to reorganise the topologically sorted list of names to take into account the two separate names spaces for types and functions at a module top-level definitions (prior to flattening definitions in the first stage).

For example, consider the module M below. It defines types and functions top-down, in use before declaration fashion, where some definitions do not have explicitly defined invariants.

```

1  module M
2  exports all
3  definitions
4  types
5      --@doc uses types S and T before their declaration; implicit inv_Rec needed
6      Rec:: s: S t:T;
7
8      --@doc uses type T and function tail before declaring them.
9      S = T inv s == head(s) > 0 and len tail(s) > 0;
10
11      --@doc implicit inv_T needed
12      T = seq1 of nat;
13
14  functions
15      tail: seq1 of nat -> seq of nat
16      tail(s) == tl s;
17
18      head: seq1 of nat -> nat
19      head(s) == hd s;
20  end M

```

If you run `exu` (with the `debug` flag set) on this module you get this output.

```

Calling Exu VDM analyser...
Calculating declaration dependencies for module 'M'...
Printed dependencies for module M.dot at ./M.dot
M'T declared after Rec
M'S declared after Rec
M'T declared after S
tail declared after S
tail declared after inv_S
Found 5 definition use before declaration. Topological sorted required.
Original names : Rec, S, T, tail, head
Start points   : Rec, S
Sorted names   : tail, head, inv_T, inv_S, T, inv_Rec, S, Rec
Organised names: tail, head, T, S, Rec
Exu successfully sorted module M definitions

```

The original names are the user defined top-level names in declaration order, which are available after the first stage. The start points are those names at the top of the graph (*i. e.*, with no incoming links), which are available after the third stage as a sorted list of names by their declared location. The sorted names are the names after the fourth stage. Notice that it includes dependencies over (implicitly declared) type invariants for R and

T. Finally, the organised names are those with both name-spaces consolidated containing only names within the originally declared set.

Furthermore, notice that the order between functions `head` and `tail` and type `T` is “unstable” (*i. e.*, they are incomparable in this case). That is, whatever order they are declared is valid, so long as they are declared before `S`, which has to be declared before `Rec`. It is these kinds of incomparability between certain names that makes sorting modules where all names are declared before use can give irrelevant sorts. We try to minimise this by sorting the starting points by declaration order, but that cannot entirely prevent irrelevant sorts.

The algorithm satisfies the invariant that the original and organised name sets are equal, and that the sorted names list contains all organised names. This way, all names are accounted and no implicitly created names (*e. g.*, `inv_T`) are part of the result.

Exu also produces a `dot` file view of the dependencies, which is similar to VDMJ’s order plugin output for module name dependencies (see Figure 1). It depicts the user declared dependencies before sorting. It includes corresponding source line locations, where starting nodes are tagged red with an inverted triangle shape, implicitly create invariants are double circles, and (terminal) nodes without outgoing links are shaped as a triangle. For complex (and large) modules, such visualisation “clues” can be invaluable

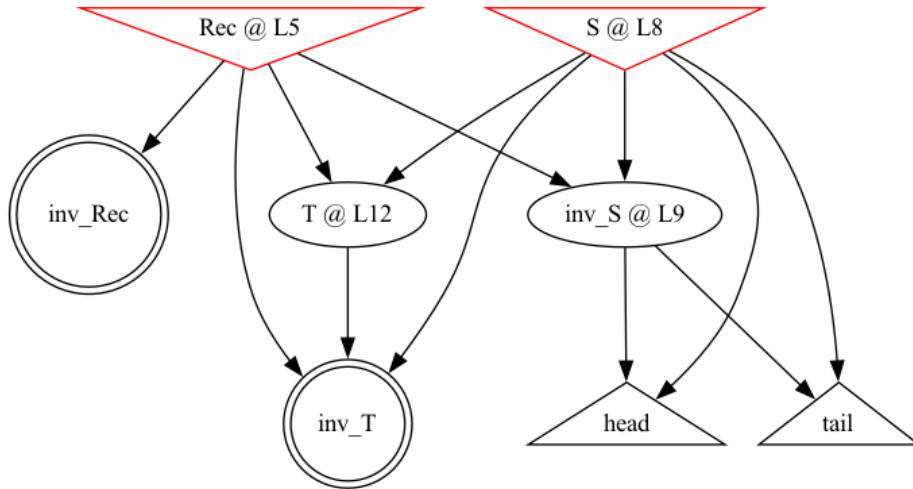


Fig. 1: `M.vdmsl` definition dependencies.

to figure out what tweaks to make in case unstable sorts produce results that are not entirely declaration before use and might generate Isabelle errors.

5 VDM-VSCoDe integration

6 Applications and Examples

Other examples of use are provided in the `ISQ.vdms1` distribution⁵. They show various ISQ measurement systems in practical use.

7 Results and discussion

In this paper, we presented .

Future work. Future.

Acknowledgements. Acks.

References

1. Battle, N.: VDMJ User Guide. Tech. rep., Fujitsu Services Ltd., UK (2009)
2. Freitas, L.: A VDM translation strategy to Isabelle (Nill Full game) (2016)
3. Kulik, T., Macedo, H.D., Talasila, P., Larsen, P.G.: Modelling the HUBCAP Sandbox Architecture In VDM – a Study In Security. In: Fitzgerald, J.S., Oda, T., Macedo, H.D. (eds.) Proceedings of the 18th International Overture Workshop. pp. 20–34. Overture (December 2020)
4. Kulik, T., Talasila, P., Greco, P., Veneziano, G., Marguglio, A., Sutton, L.F., Larsen, P.G., Macedo, H.D.: Extending the formal security analysis of the hubcap sandbox. In: Macedo, H.D., Thule, C., Pierce, K. (eds.) Proceedings of the 19th International Overture Workshop. Overture (10 2021)
5. Macedo, H.D., Larsen, P.G., Fitzgerald, J.: Incremental Development of a Distributed Real-Time Model of a Cardiac Pacing System using VDM. In: Cuellar, J., Maibaum, T., Sere, K. (eds.) FM 2008: Formal Methods, 15th International Symposium on Formal Methods. Lecture Notes in Computer Science, vol. 5014, pp. 181–197. Springer-Verlag (2008)
6. Naur, P.: An Experiment in Program Development. BIT 12, 347–365 (1972)
7. Nilsson, R., Lausdahl, K., Macedo, H.D., Larsen, P.G.: Transforming an industrial case study from VDM++ to VDM-SL. In: Pierce, K., Verhoef, M. (eds.) The 16th Overture Workshop. pp. 107–122. Newcastle University, School of Computing, Oxford (July 2018), TR-1524
8. Rask, J.K., et al.: Advanced VDM Support in Visual Studio Code. In: 20th International Overture Workshop. pp. 35–50 (2022)

⁵ https://github.com/leouk/VDM_Toolkit/