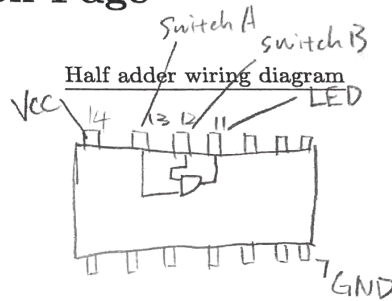
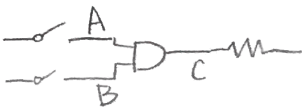


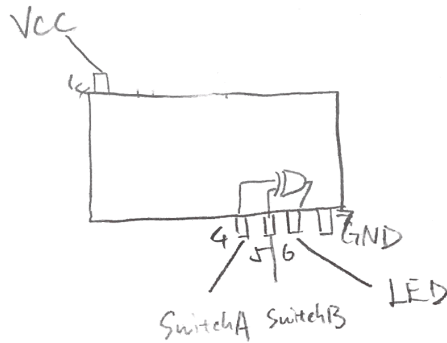
Circuit Demonstration Page

Half adder schematic

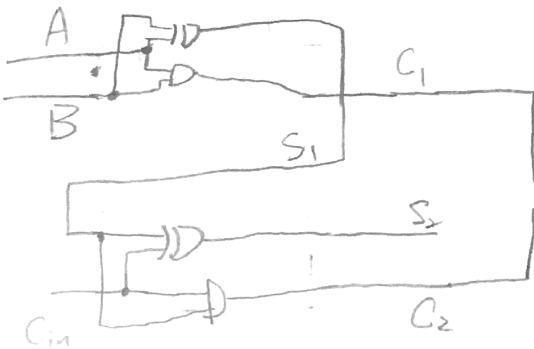


Half adder:

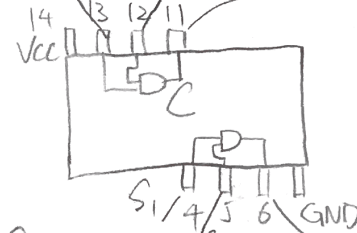
| A | B | C | S |
|---|---|---|---|
| 0 | 0 | 0 | 0 |
| 0 | 1 | 0 | 1 |
| 1 | 0 | 0 | 1 |
| 1 | 1 | 1 | 0 |



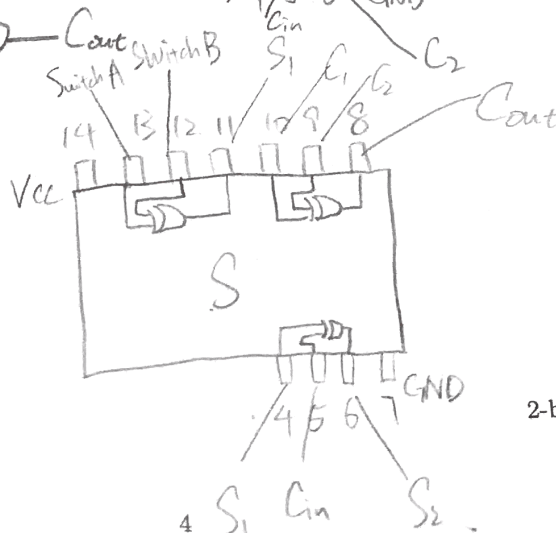
Full adder schematic



Full adder wiring diagram



Full adder:



2-bit adder:

BP