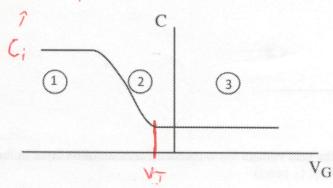
Name	

1) Consider the C-V characteristic of a MOS structure, below.

oxide capacitance

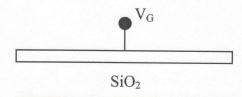


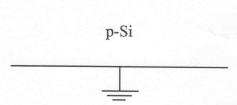
1.a) Identify the three regimes of interface charge (i.e. accumulation, depletion or inversion corresponding to each circle. (2 points)

- 1 Accumulation
- 2 Depletion
- 3 Inversion
- 1.b) On the diagram, identify the oxide capacitance. (1 point)
- 1.c) On the diagram, identify the threshold voltage. (1 point)
- 1.d) Is the semiconductor n-type, p-type or intrinsic? Explain. (1 point)

Vacc is when V6 <0, p-type
or
Vinu is when V6 >0

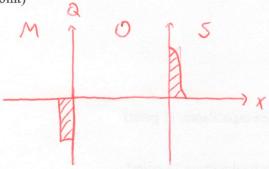
2) Consider a MOS capacitor with a p-Si substrate.





2.a) What is the sign of the gate voltage  $V_G$  to create an *accumulation* layer in the semiconductor at the Si/SiO<sub>2</sub> interface? (1 point)

2.b) Draw the corresponding *block charge* diagram in the semiconductor, the oxide and the metal. (1 point)



2.c) Draw the energy band diagram across the whole system (i.e. metal, oxide and semiconductor) when the semiconductor is in *inversion*, and indicate schematically the position of E<sub>i</sub>, E<sub>F</sub>, E<sub>c</sub> and E<sub>v</sub>, and the positions of the Si/SiO<sub>2</sub> and the Metal/SiO<sub>2</sub> interfaces. (3 points)

