

Essentials of MOSFETs

**Unit 5:**  
**Additional Topics**

**Lecture 5.4:**  
**Review of PN Junctions**

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# Transistors

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MOSFET  
MOST  
IGFET  
DMOS  
HEXFET  
VMOS  
TFT  
MISFET  
JFET  
VFET  
MESFET  
MOSFET  
HEMT  
TEGFET

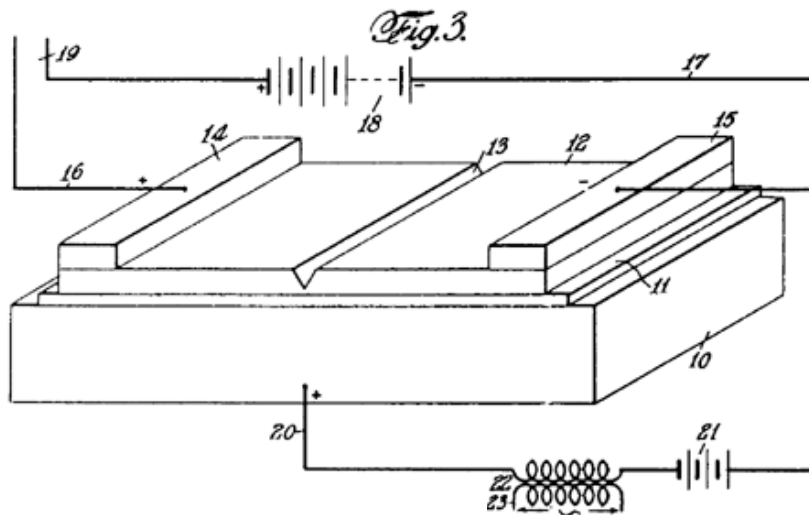
HFET  
DHFET  
HIGFET  
SISFET  
PBT  
LRTFET  
VMT  
**BJT**  
**HBT**  
**DHBT**  
THETA  
RST  
BICFET  
RTBT

RBT  
RHET  
QWBRTT  
TETTRAN  
SIT  
NWFET  
CNT FET  
SB FET  
BTBT FET

induced base transistor  
planar doped barrier transistor  
metal base transistor  
Stark-effect transistor  
delta-doped channel heterojunction FET

***Bipolar  
transistors are  
barrier-  
controlled  
transistors too.***

# Invention (and discovery) of the transistor

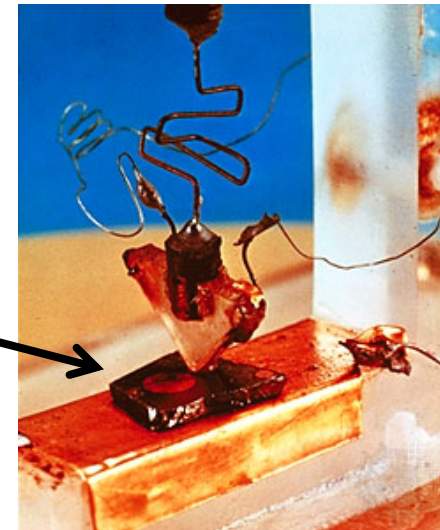


Field-Effect Transistor  
Lillienfield, 1925  
Heil, 1935

“base”



Bardeen, Schockley,  
and Brattain, 1947



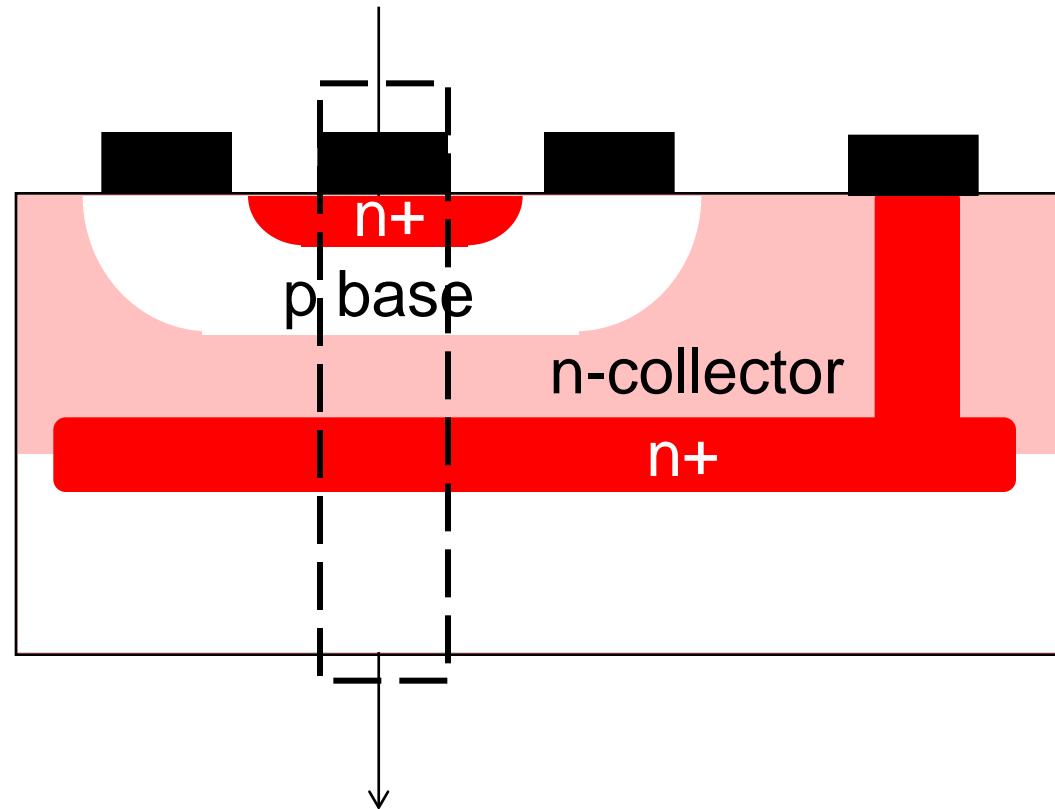
Lundstrom: 2018

Lucent / Bell Labs

# Bipolar transistors

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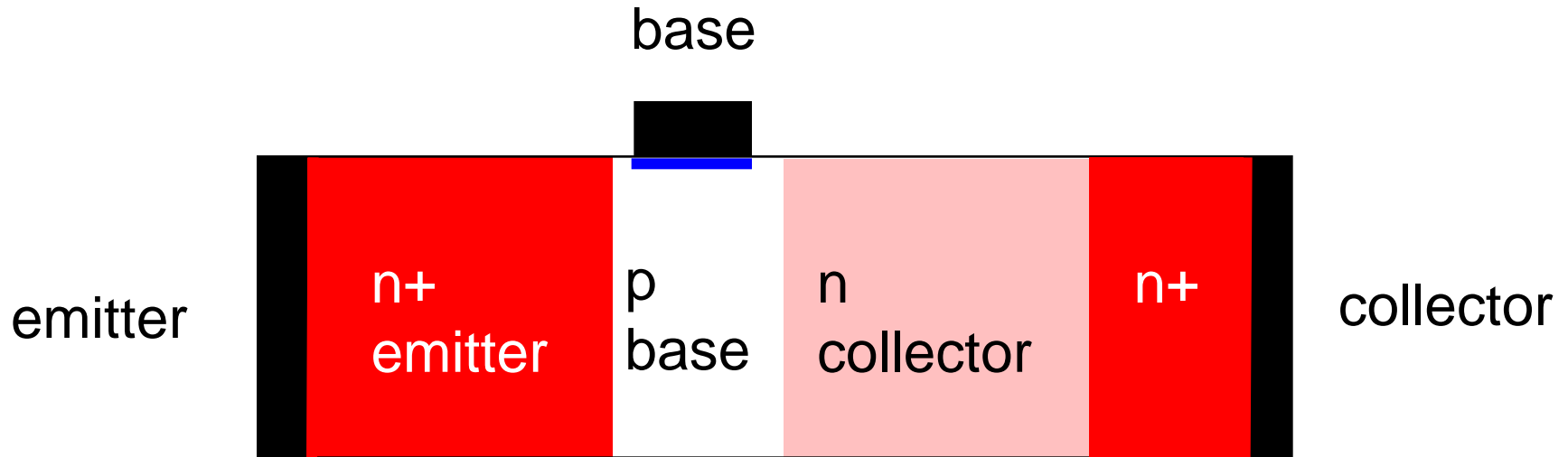
## *Double diffused Bipolar Junction Transistor (BJT)*



“ideal” 1D BJT

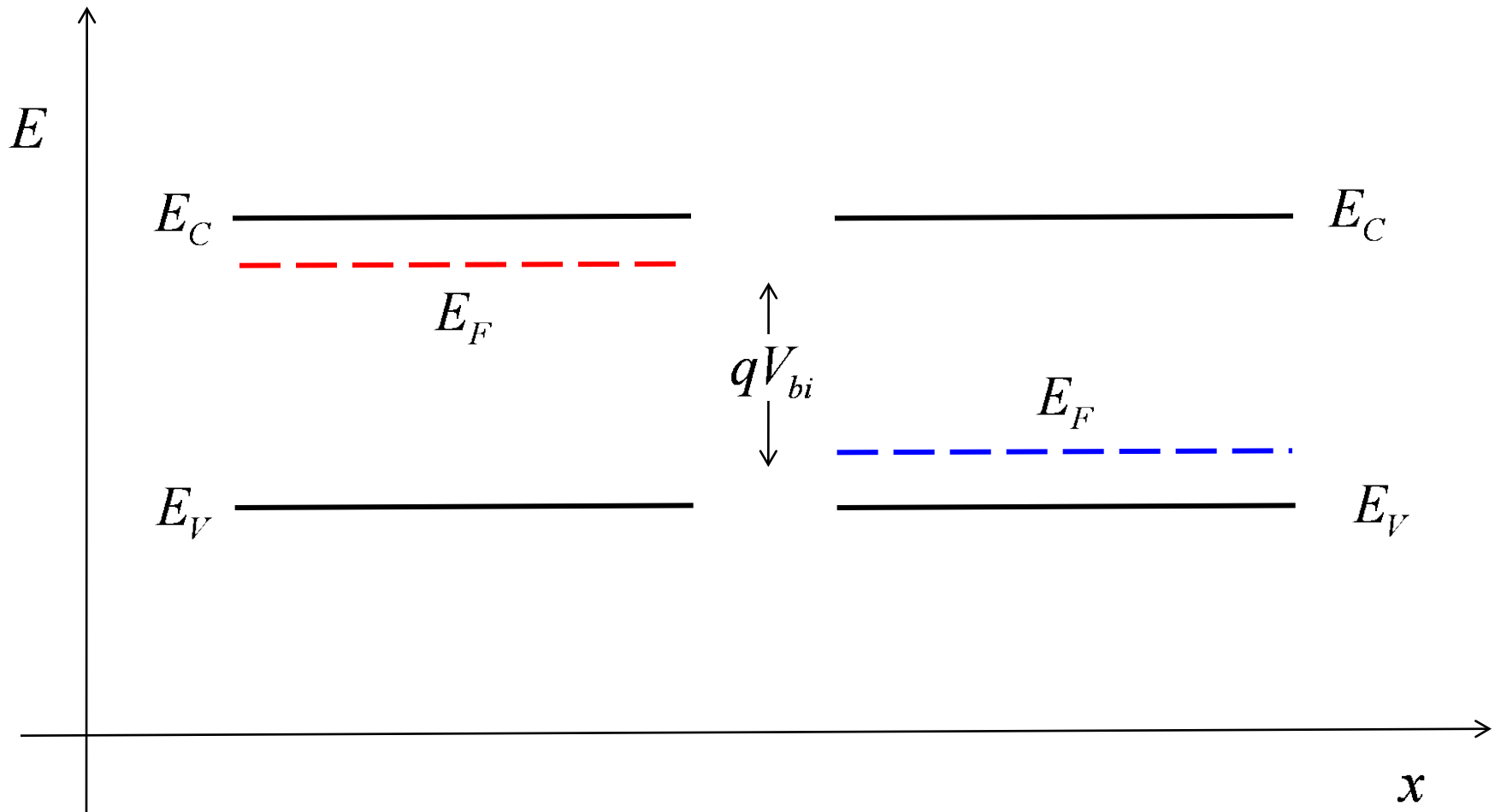
# Bipolar transistors

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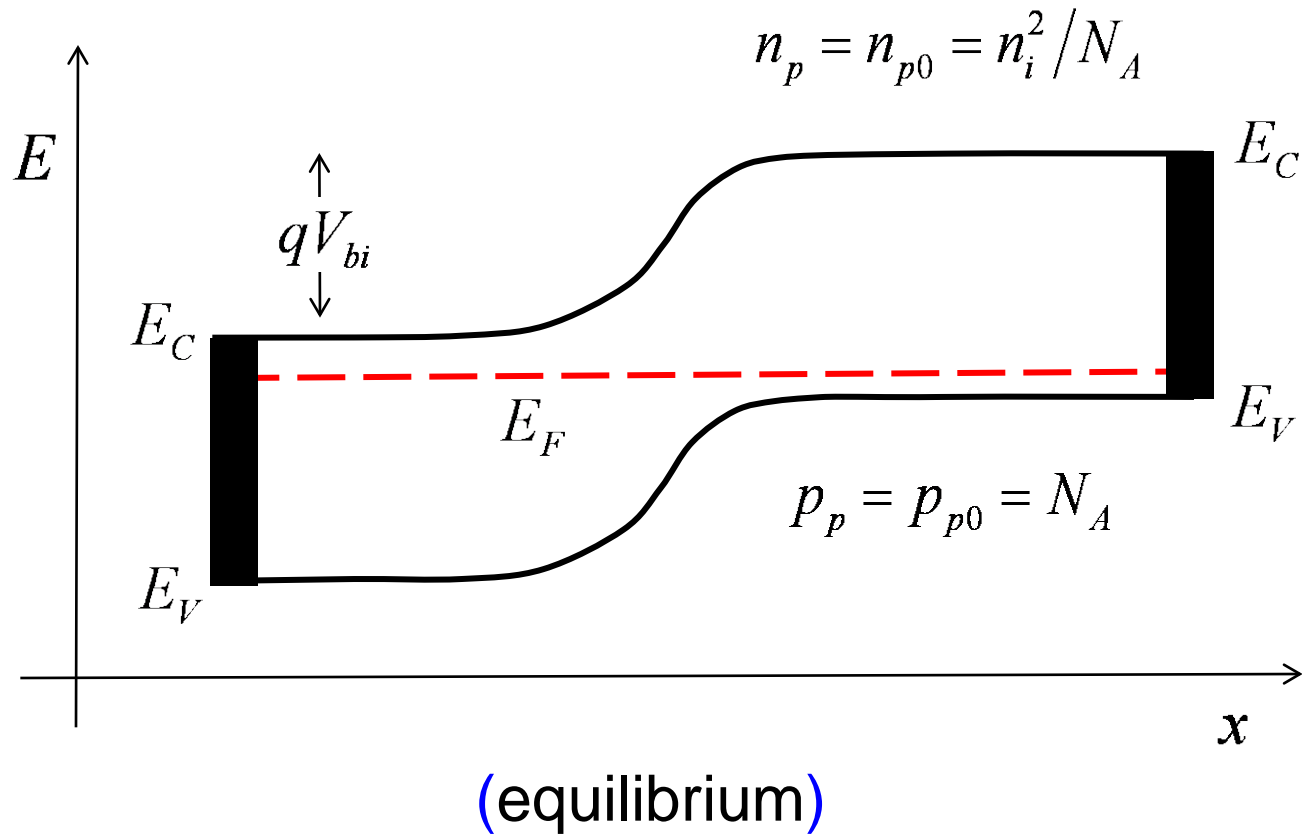


Just as the MOS-C is the heart of a MOSFET, BJT's are made up of a basic building block: The PN junction.

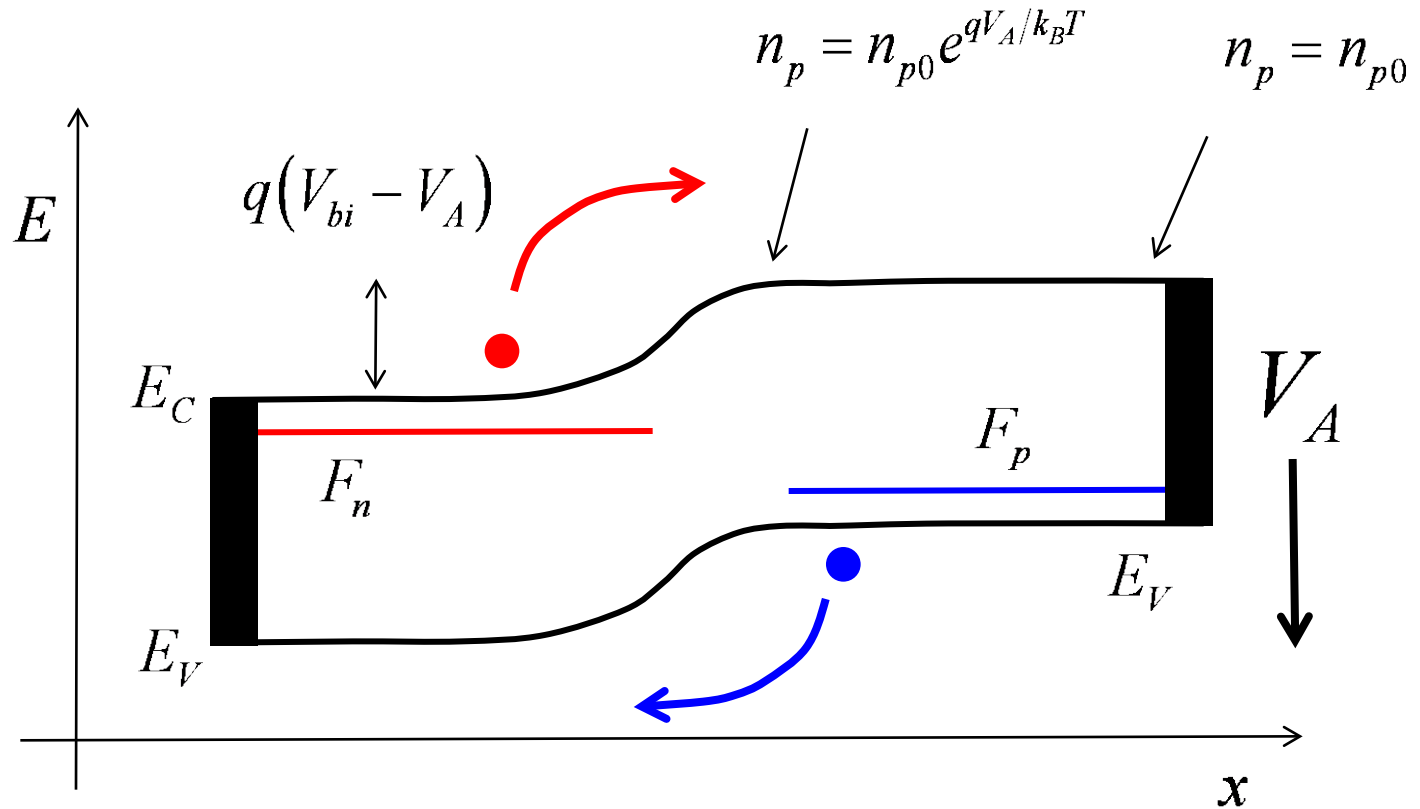
# Primer on PN junctions



# Equilibrium energy band diagram



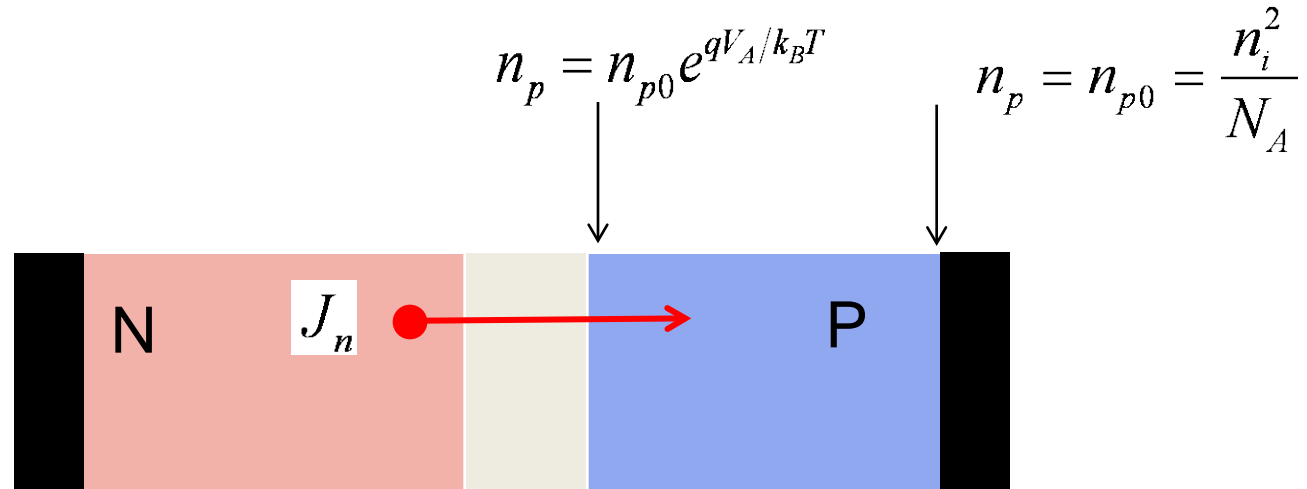
# Forward bias



(forward bias)



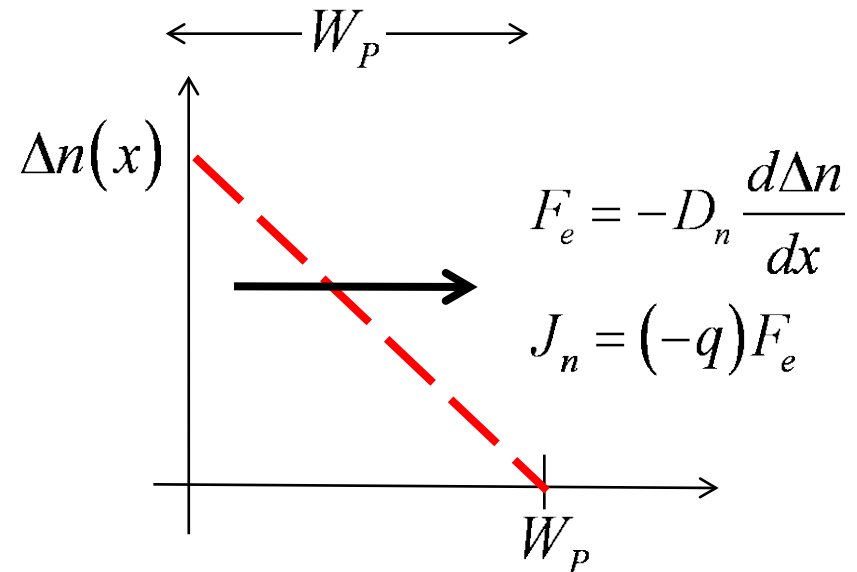
# Electron current in a FB PN junction



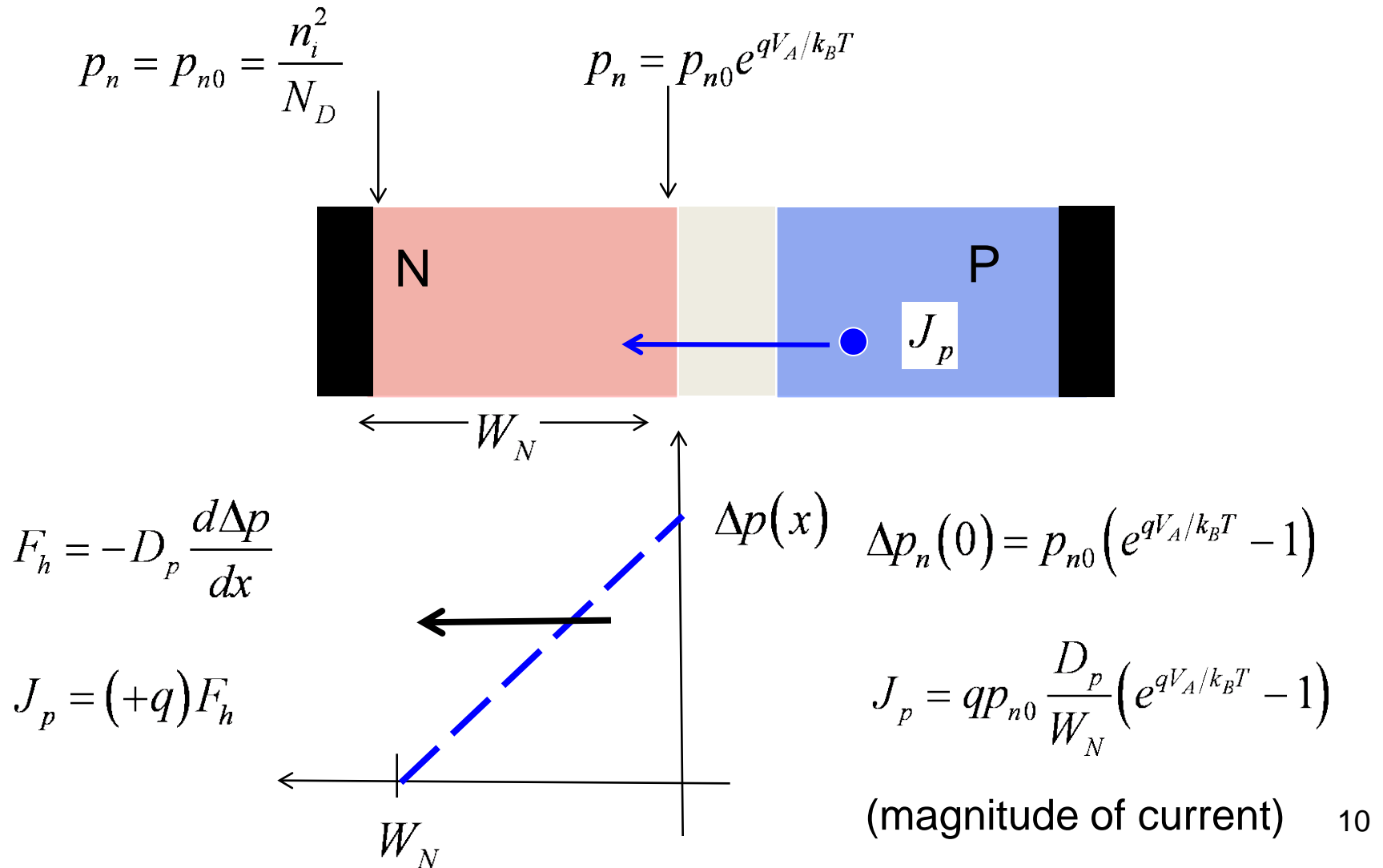
$$\Delta n_p(0) = n_{p0} (e^{qV_A/k_B T} - 1)$$

$$J_n = q n_{p0} \frac{D_n}{W_P} (e^{qV_A/k_B T} - 1)$$

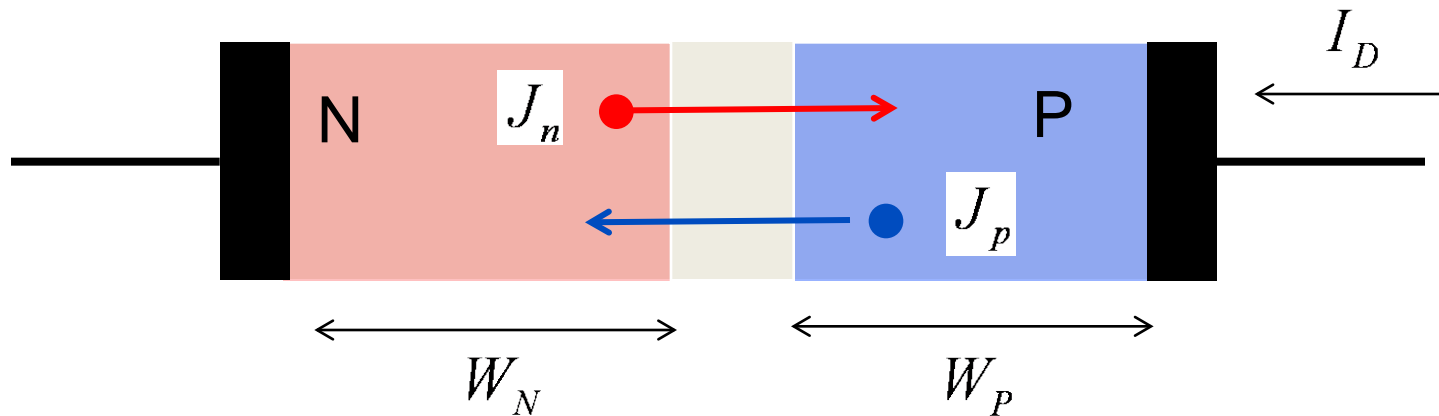
(magnitude of current)



# Hole current in a FB PN junction



# Current in a FB PN junction

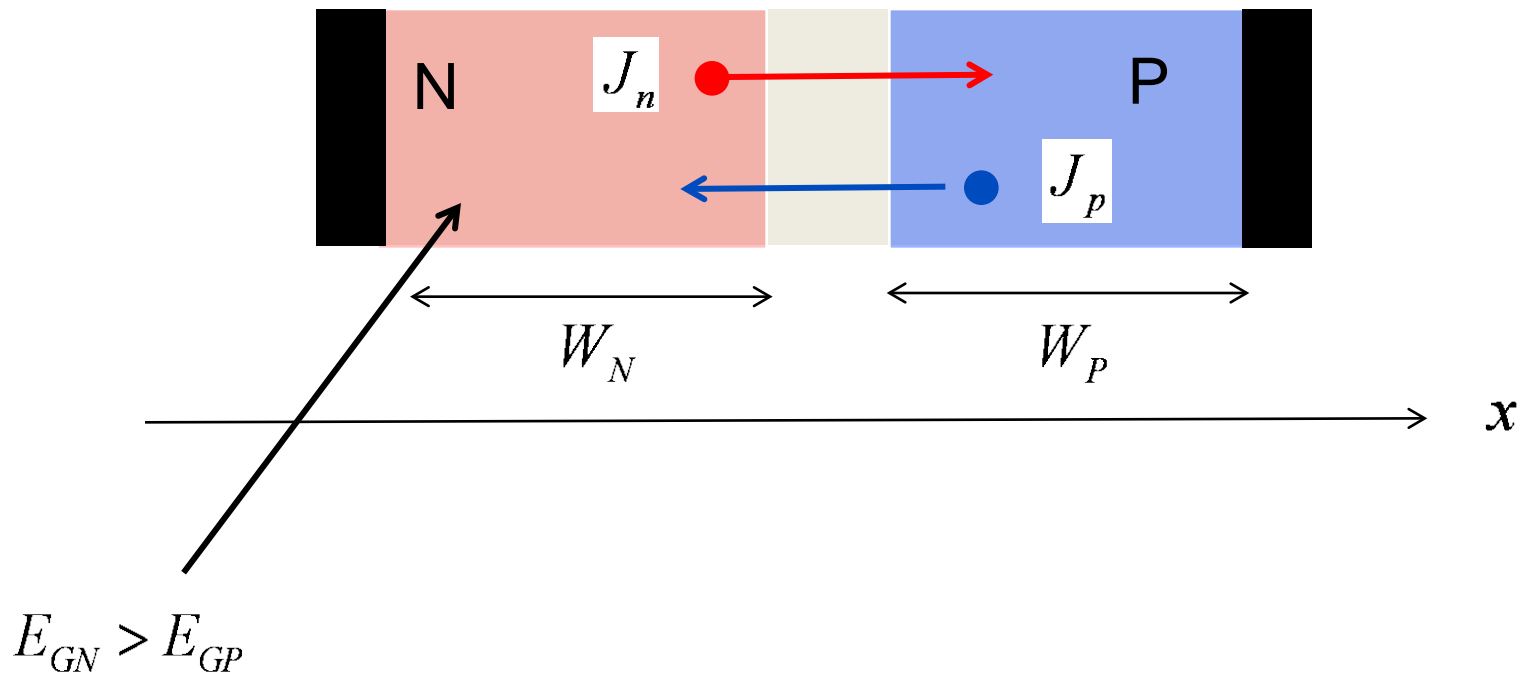


$$J_n = q \frac{D_n}{W_P} \frac{n_i^2}{N_A} \left( e^{qV_A/k_B T} - 1 \right)$$

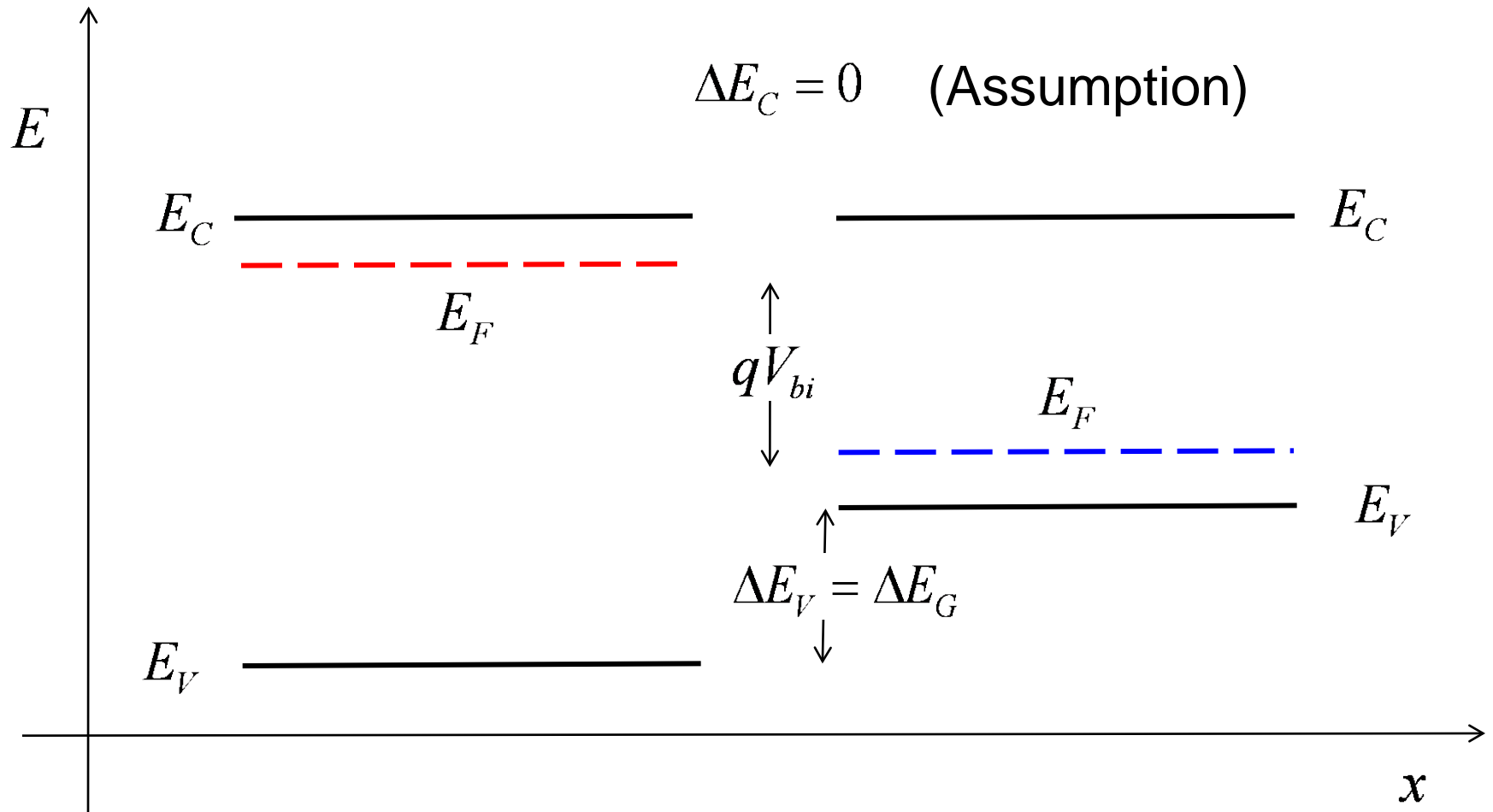
$$J_p = q \frac{D_p}{W_N} \frac{n_i^2}{N_D} \left( e^{qV_A/k_B T} - 1 \right)$$

$$I_D = A_D (J_n + J_p)$$

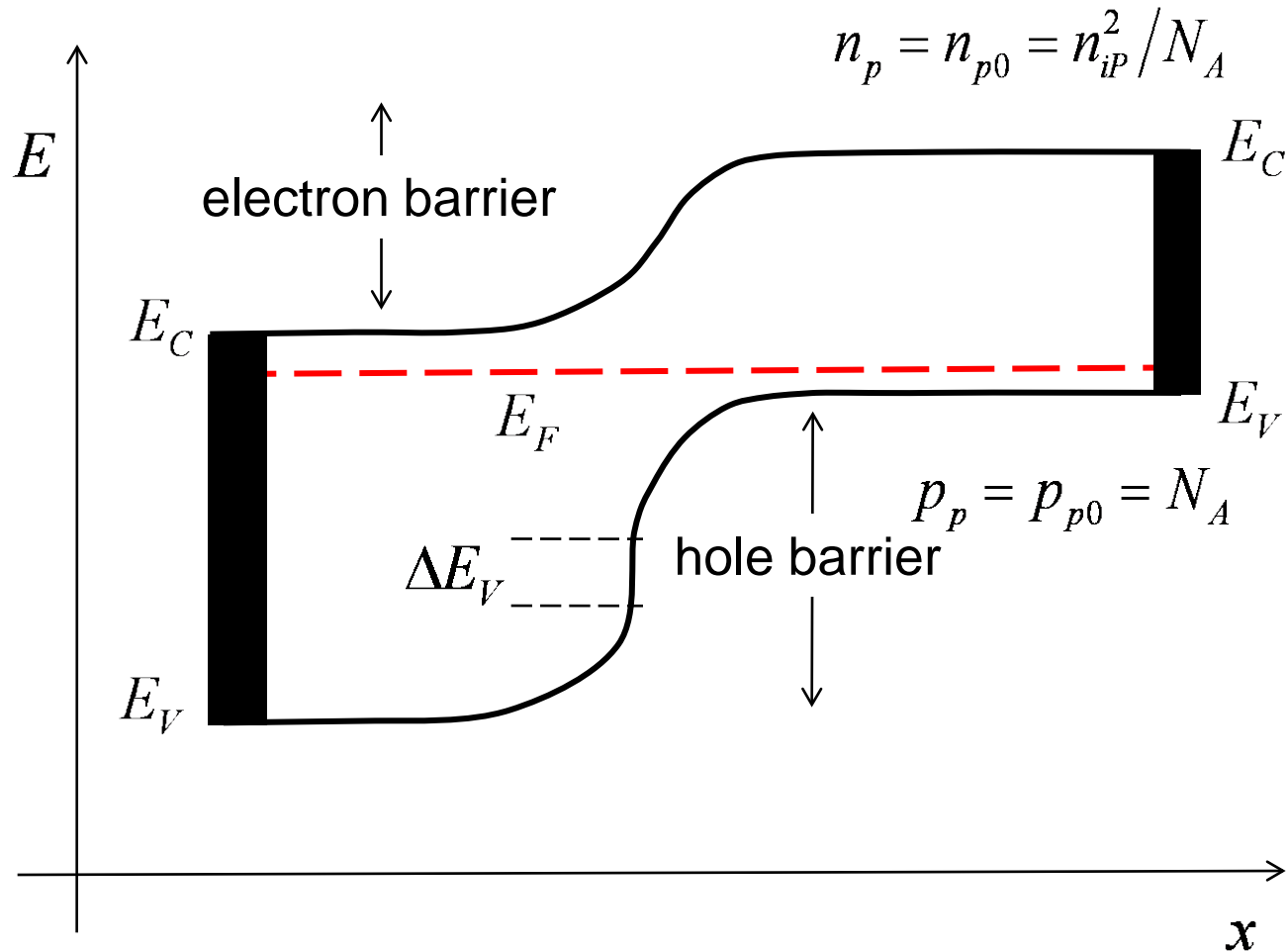
# Heterojunction diodes



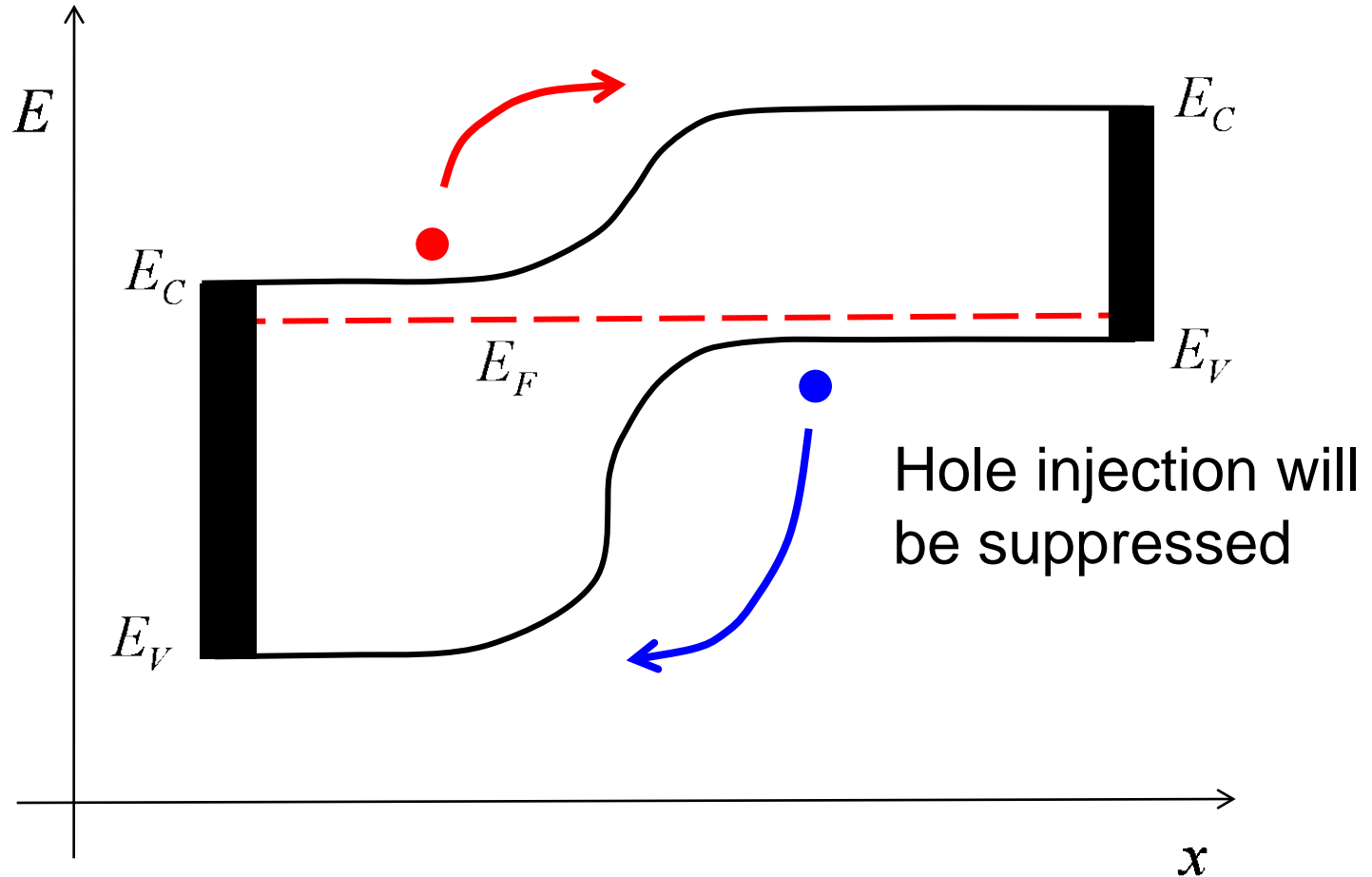
# Np heterojunction



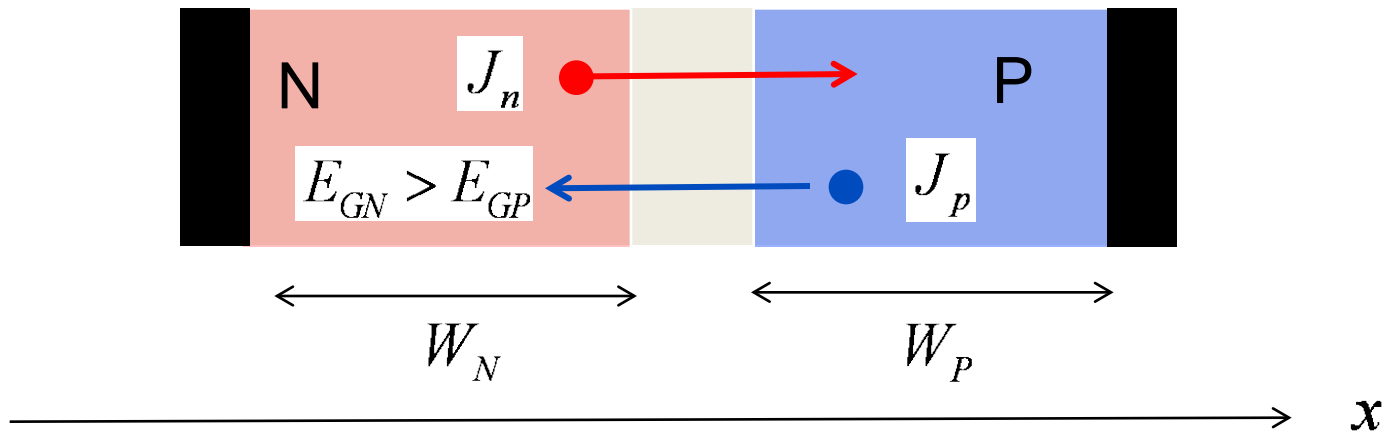
# Equilibrium energy band diagram



# Equilibrium energy band diagram



# Current in a FB Np heterojunction



$$J_n = q \frac{D_n}{W_P} \frac{n_{iP}^2}{N_A} \left( e^{qV_A/k_B T} - 1 \right)$$

$$J_p = q \frac{D_p}{W_N} \frac{n_{iN}^2}{N_D} \left( e^{qV_A/k_B T} - 1 \right)$$

$$n_i^2 \propto e^{-E_G/k_B T}$$

$$E_{GN} > E_{GP}$$

$$n_{iN}^2 \ll n_{iP}^2$$

$$J_p \ll J_n$$



# Summary

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- 1) PN diodes are bipolar devices – current is due to electrons and holes.
- 2) A forward bias lowers the barrier for electron and hole injection (i.e. it is a barrier-controlled device).
- 3) The current increases exponentially with forward bias (a 60 mV increase in FB increases  $I_D$  by 10X).
- 4) It is possible to suppress one of the two current components by using heterojunctions.

## Next topic

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An NPN bipolar transistor consists of two PN junctions with a shared P region.

Since we understand PN junctions, we can understand BJTs.

That is our topic for the next lecture.