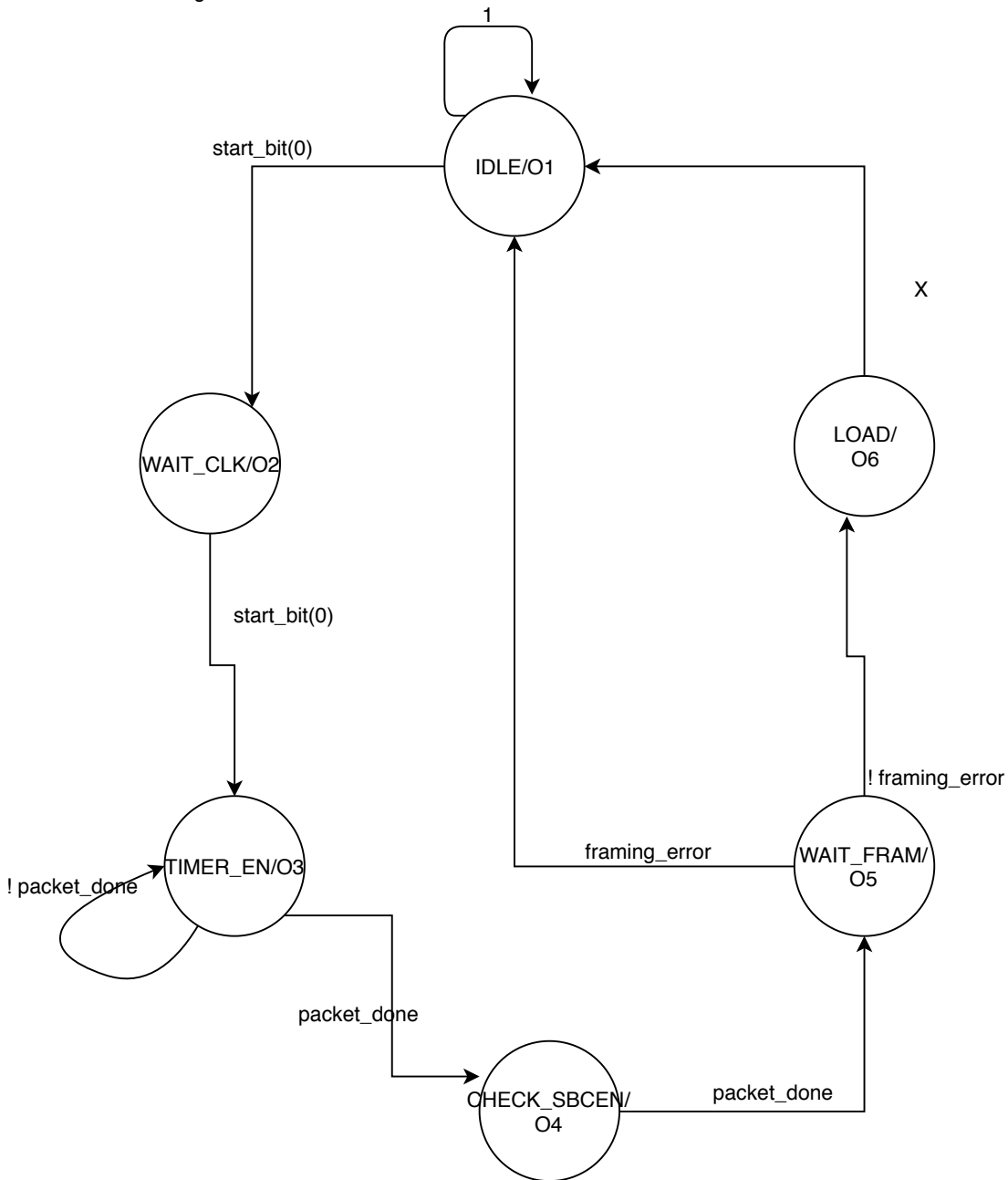


State Diagram RCU



O1: enable\_timer = 0;  
load\_buffer = 0;  
SBC\_enable = 0;  
SBC\_clear = 0;

O2: enable\_timer = 0;  
load\_buffer = 0;  
SBC\_enable = 0;  
SBC\_clear = 1;

O3: enable\_timer = 1;  
load\_buffer = 0;  
SBC\_enable = 0;  
SBC\_clear = 0;

O4: enable\_timer = 0;  
load\_buffer = 0;  
SBC\_enable = 1;  
SBC\_clear = 0;

O5: enable\_timer = 0;  
load\_buffer = 0;  
SBC\_enable = 1;  
SBC\_clear = 0;

O6: enable\_timer = 0;  
load\_buffer = 1;  
SBC\_enable = 0;  
SBC\_clear = 0;