



CSCI 3753 - Godley - Operating Systems

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SECTIONS	1	Started on	Wednesday, 24 July 2019, 8:35 PM
		State	Finished
2	2	Completed on	Wednesday, 24 July 2019, 8:41 PM
		Time taken	5 mins 41 secs
3	3	Marks	16.00/16.00
		Grade	10.00 out of 10.00 (100%)

Question **1**

Correct

Mark 1.00 out of 1.00

The ____ instruction interrupts the OS to signal to switch into ____ mode, which is needed for a system call.

Select one:

☐ interrupt : user

☐ interrupt : kernel

☒ trap : kernel

☐ trap : user

Your answer is correct.

The correct answer is: trap : kernel

Question **2**

Correct

Mark 1.00 out of 1.00

Which of the following is NOT the responsibility of the OS?

Select one:

☒ compiling a program

☐ Memory management

☐ File system

☐ Process management

☐ Device management

Your answer is correct.

The correct answer is: compiling a program

CU

Schedules

Xinyu Jiang

Correct

Mark 1.00 out of 1.00

SECTIONS

1

2

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-

Question 4

Correct

Mark 1.00 out of 1.00

What is the interface between the virtual machine and the host machine is called?

Select one:

☐ OS forces programs to give up CPU

☒ Programs voluntarily and quickly give up CPU before their execution is completed.

☐ Programs run in a round-robin fashion.

☐ None of the above

Your answer is correct.

The correct answer is: Programs voluntarily and quickly give up CPU before their execution is completed.

Question 5

Correct

Mark 1.00 out of 1.00

Which of the following are NOT in a process's address space?

Select one:

☒ a. page table

☐ b. Code

☐ c. stack

☐ d. heap

Your answer is correct.

The correct answer is: page table



Mark 1.00 out of 1.00

1

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Question 7

Correct

7

Mark 1.00 out of 1.00

8

9

Select one:

- ☒ a. Dispatching
- ☐ b. Context switching
- ☐ c. Indexing
- ☐ d. Bootloading

Your answer is correct.

The correct answer is: Dispatching

All modern operating systems are _____ multi-tasked.

Select one:

- ☐ cooperatively
- ☒ preemptively

Your answer is correct.

The correct answer is: preemptively

Question 8

Correct

Mark 1.00 out of 1.00

Mode bit = 0 indicates user mode

Select one:

- ☐ True
- ☒ False ✓

The correct answer is 'False'.

Question 9

Correct

Mark 1.00 out of 1.00

In a process control block which of the following is used to retrieve the information on the next instruction to be executed?

Select one:

- ☐ a. Process state
- ☐ b. Process number
- ☒ c. Program counter
- ☐ d. CPU registers

Your answer is correct.

The correct answer is: Program counter



Mark 1.00 out of 1.00

Your answer is correct.

The correct answer is: device controller



 a. Interrupt driven

☐ b. Volatile

☐ c. Polling

☐ d. Disconnected

Your answer is correct.

The correct answer is: Interrupt driven

Mark 1.00 out of 1.00

Select one:

☐ a. DMA is made possible through specialized hardware

☐ b. DMA sends an interrupt to the CPU when it is finished

- c. If DMA hardware is present for I/O transfers, it will always invoke itself to do any I/O transfer

☐ d. The purpose of DMA is to free up the CPU during expensive I/O transfers

Your answer is correct.

The correct answer is: If DMA hardware is present for I/O transfers, it will always invoke itself to do any I/O transfer

Mark 1.00 out of 1.00

Select one:

- a. stack

☐ b. code

☐ c. heap

☐ d. data

Your answer is correct.

The correct answer is: stack



Select one or more:

- Your answer is correct.

The correct answers are: Different major and different minor numbers. , The same major number with different minor numbers. , Different major numbers and the same minor number.

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Data retention summary
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