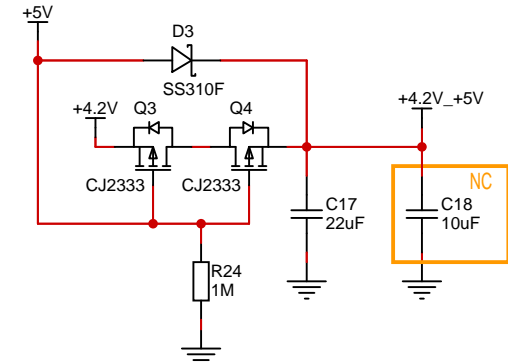
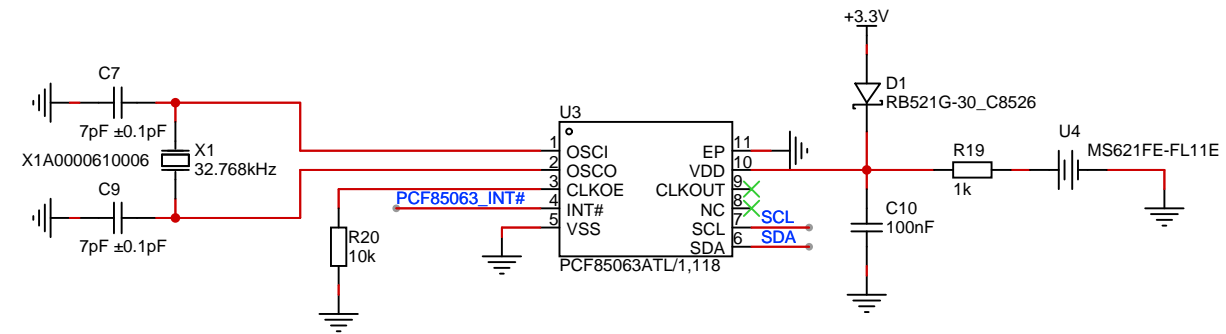
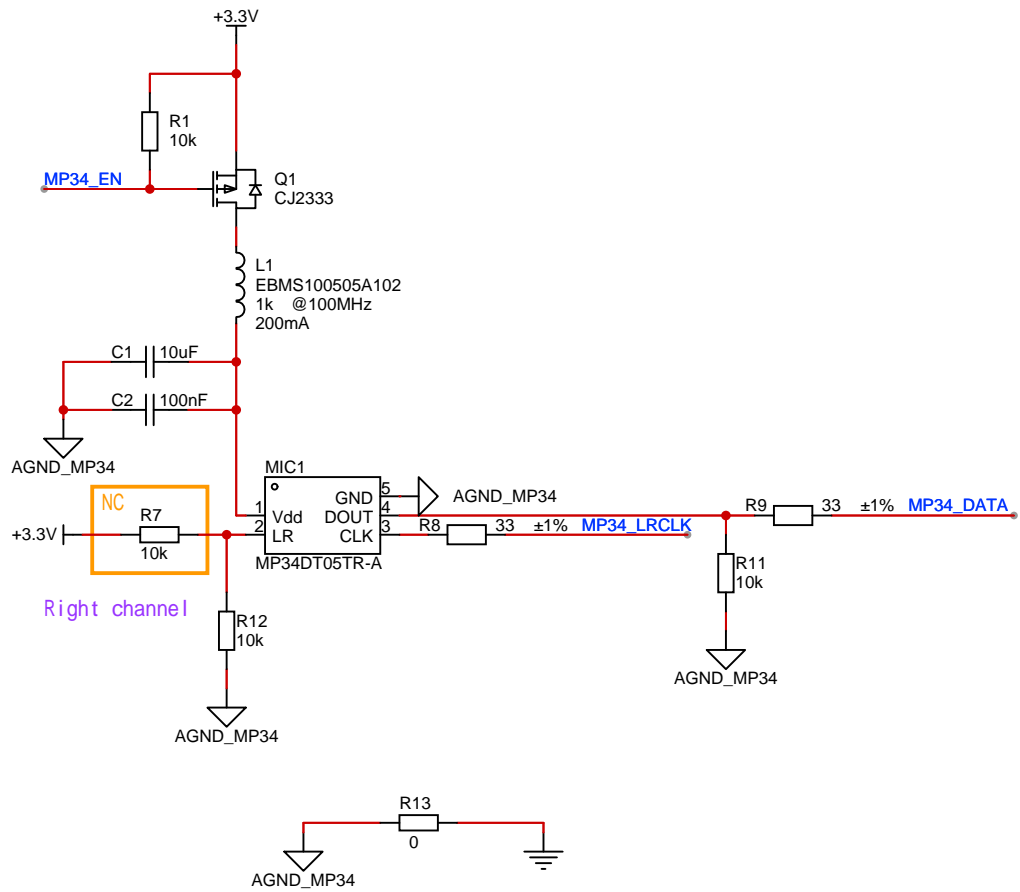
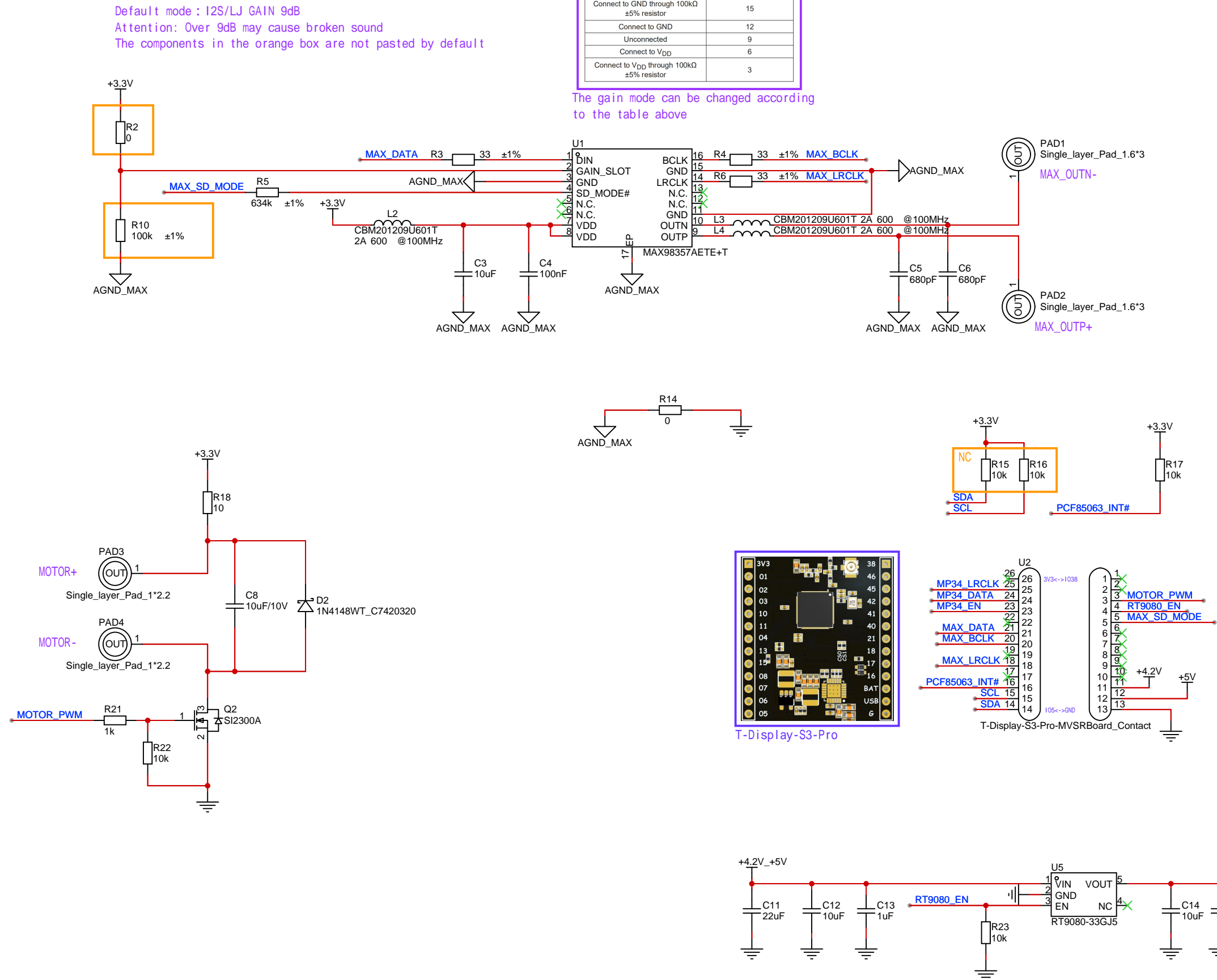


Table 5. SD_MODE Control		
SD_MODE STATUS	SELECTED CHANNEL	
High	VDD_MP34 > R2 trip point	
Pullup through R <sub>MP34EN</sub>	R2 trip point > VDD_MP34 > R1 trip point	Left
Pullup through R <sub>MP34EN</sub>	R1 trip point > VDD_MP34 > R2 trip point	Right
Low	R2 trip point > VDD_MP34	(Left + right)
Table 6. Examples of SD_MODE Pullup Resistor Values		
LOGIC VOLTAGE LEVEL (V <sub>DD</sub> ) (V)	R <sub>PULL</sub> (kΩ)	R <sub>LOAD</sub> (kΩ)
1.8	69.8	350
3.3	210.2	634

Table 8. Gain Selection	
GAIN_SLOT	PS/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V <sub>DD</sub>	6
Connect to V <sub>DD</sub> through 100kΩ ±5% resistor	3

The gain mode can be changed according to the table above



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Ver: **V1.1**

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