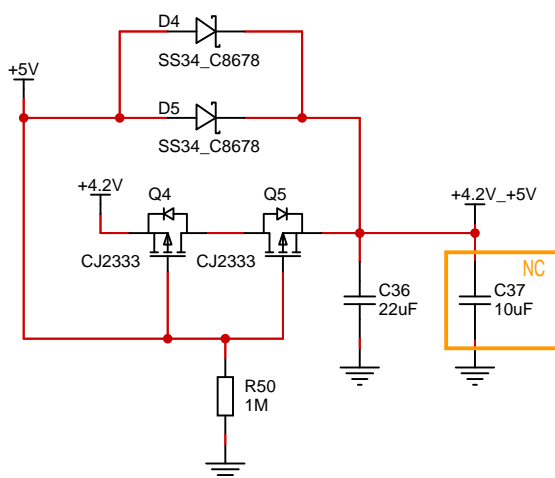
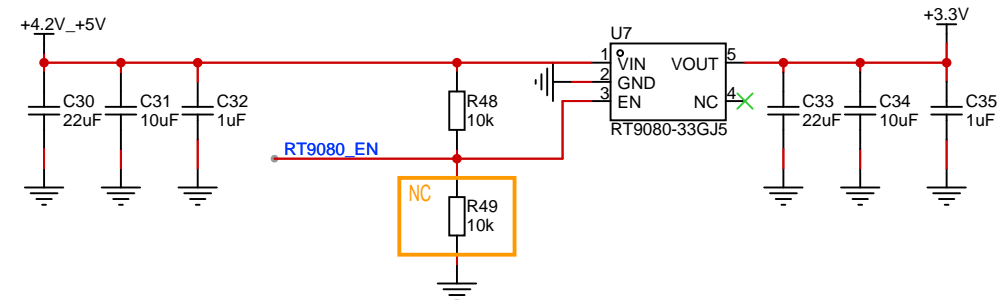
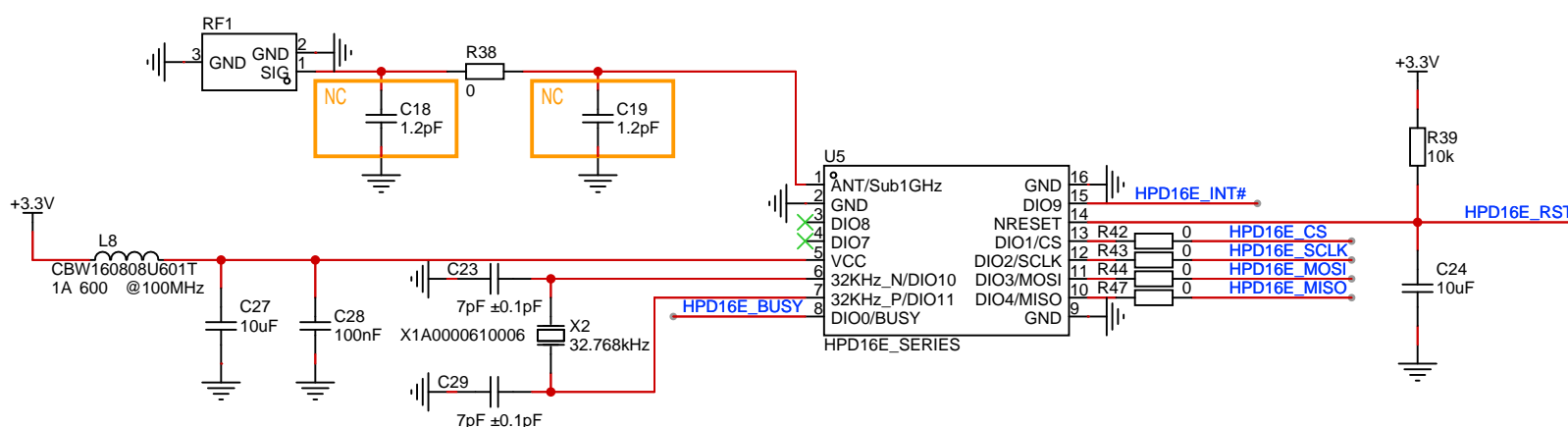
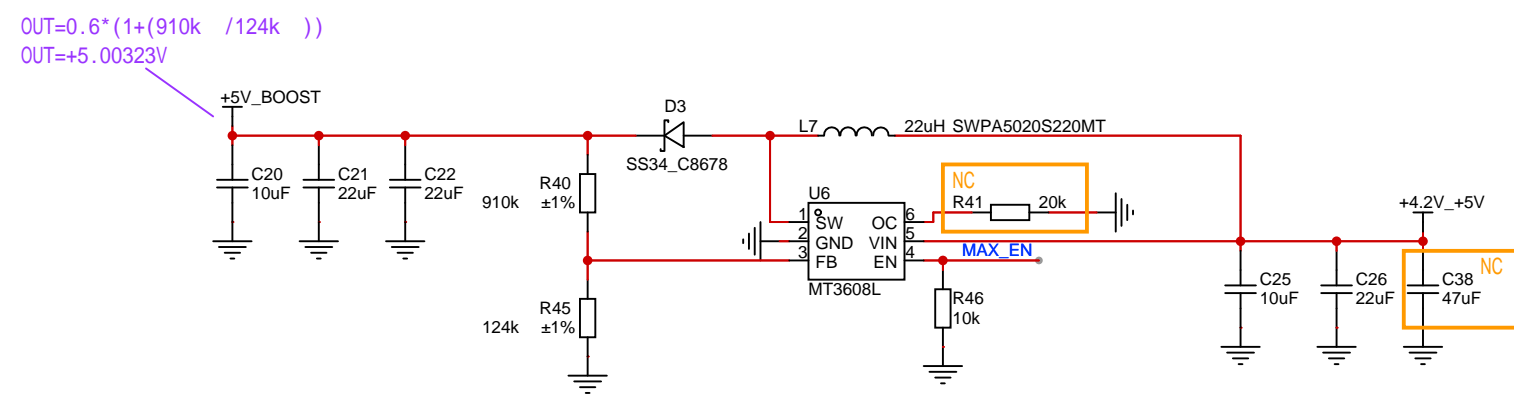
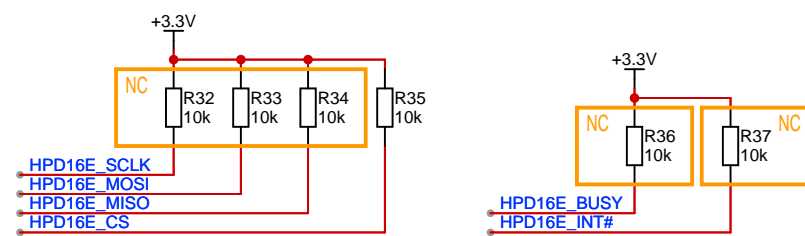
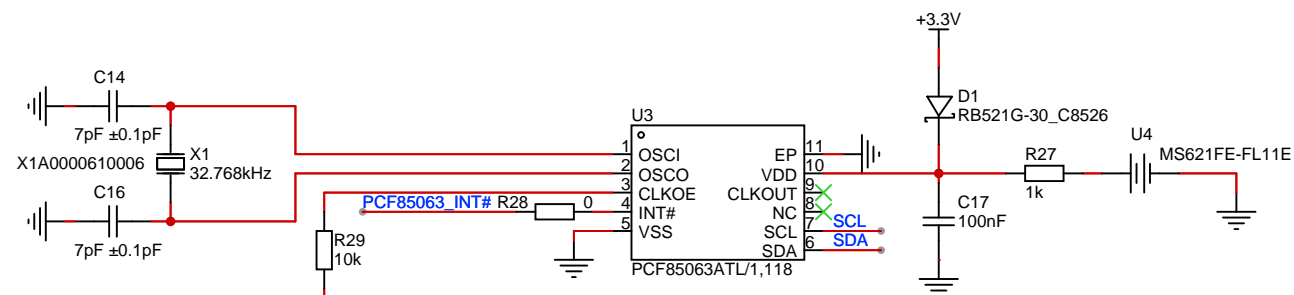
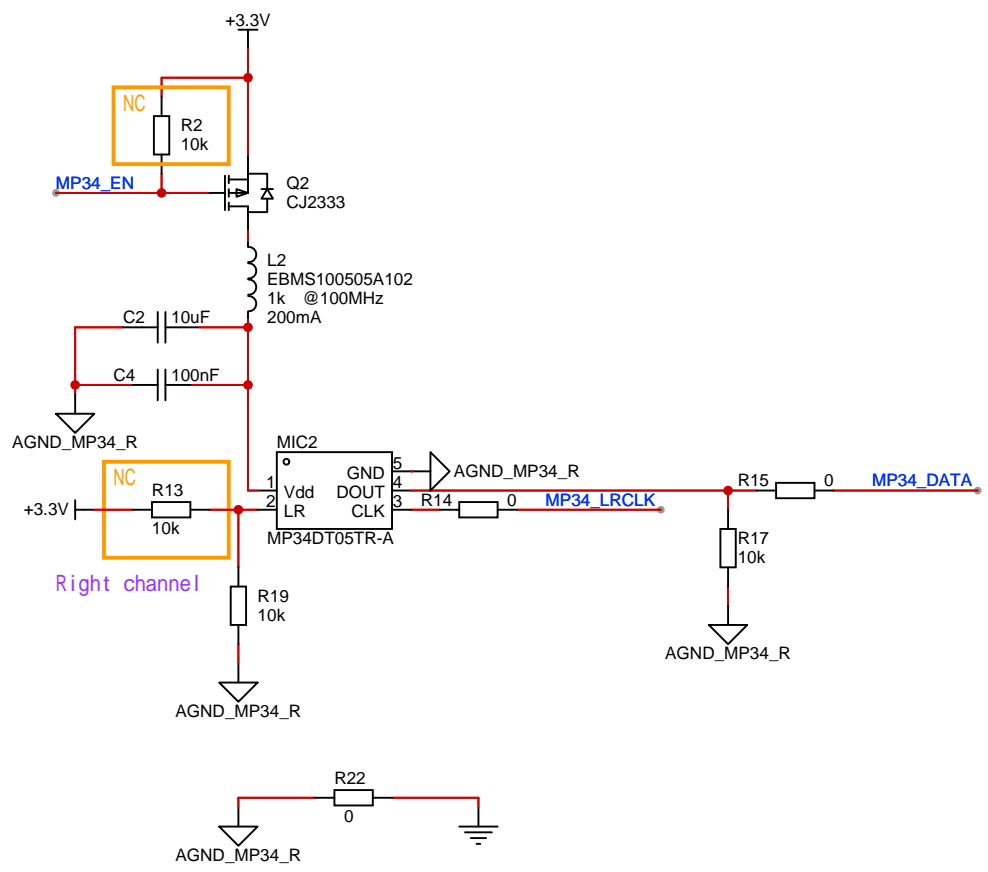
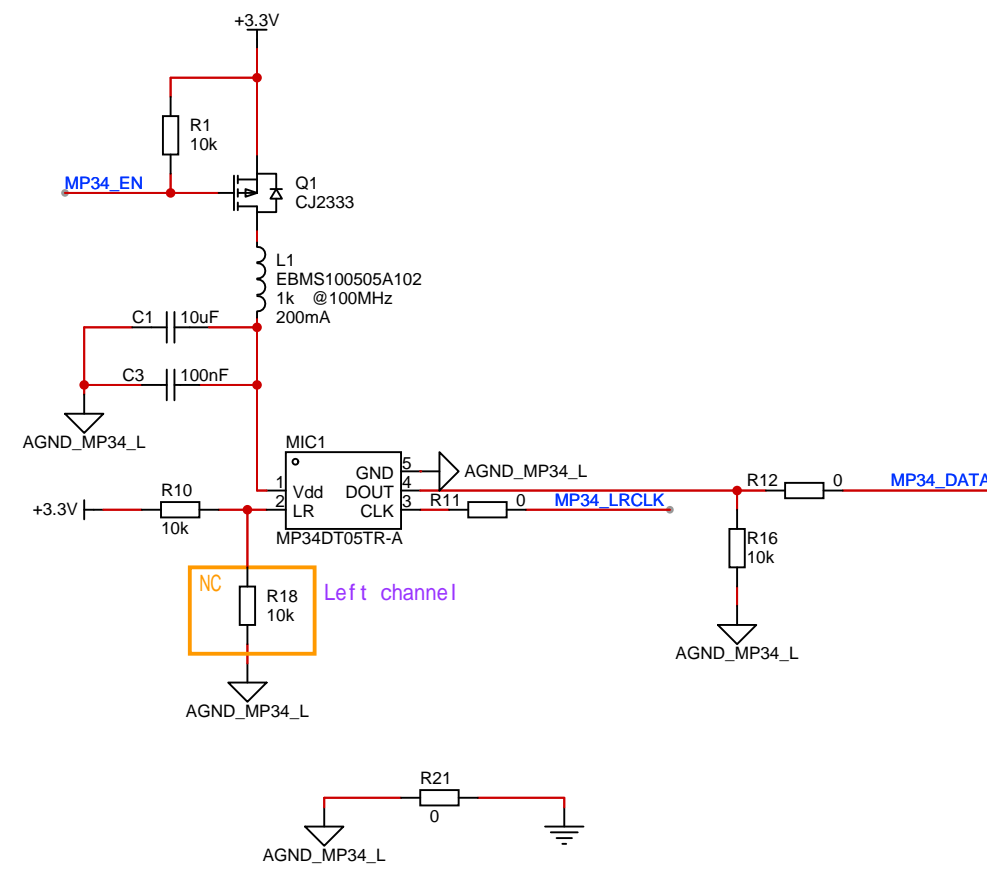
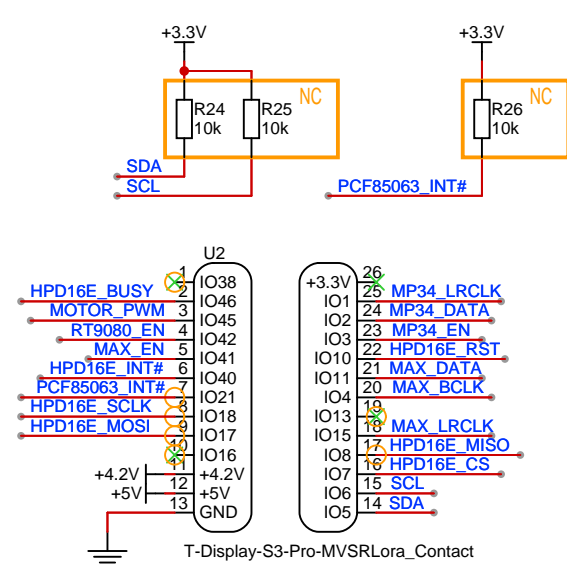
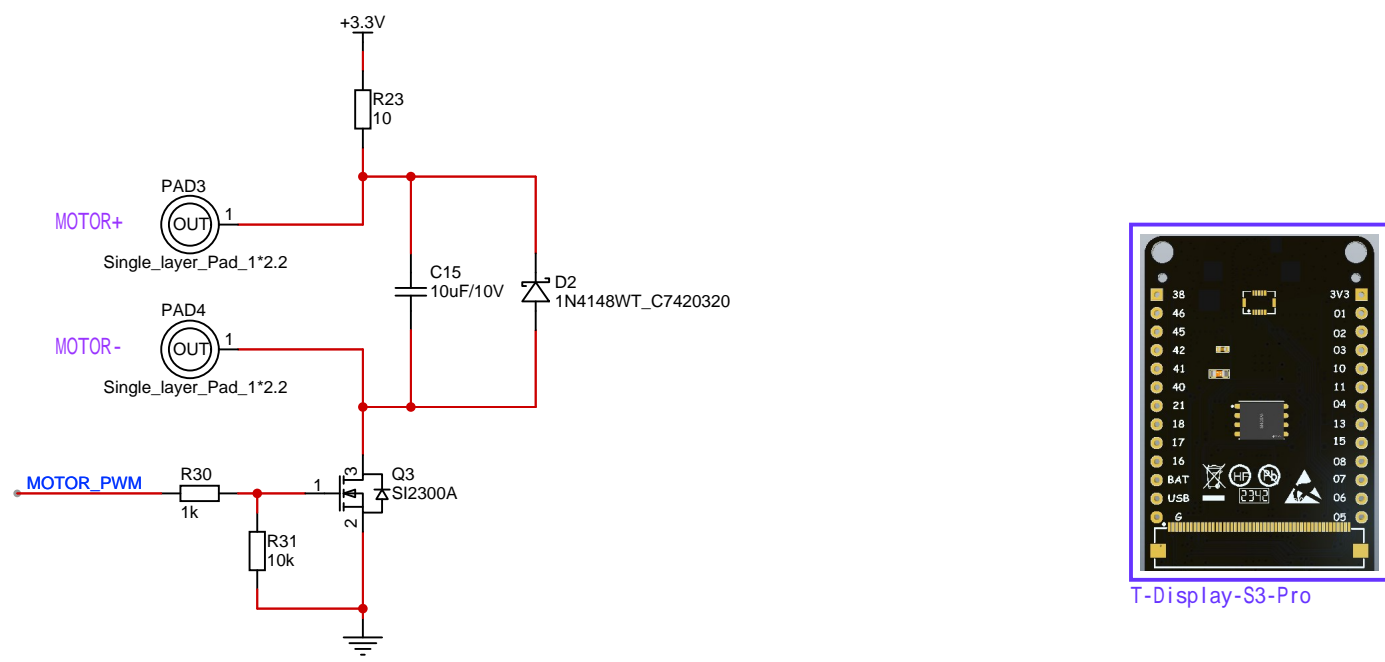
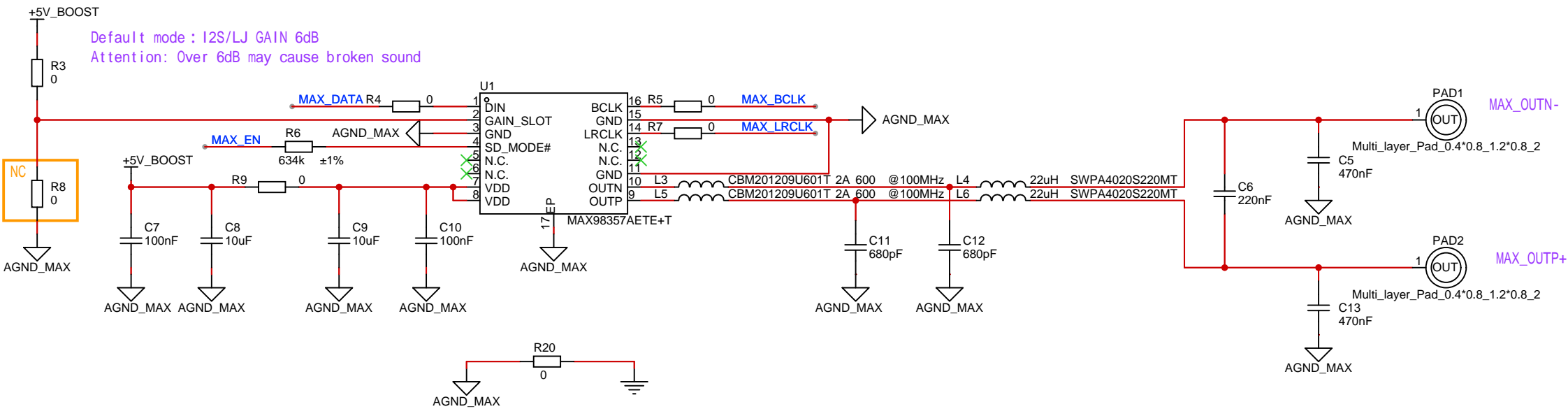


SD_MODE STATUS		SELECTED CHANNEL
High	V _{DDIOVCC} > 82 trip point	Left
Pullup through R _{PULLUP}	82 trip point > V _{DDIOVCC} > 81 trip point	Right
Pullup through R _{PULLUP}	81 trip point > V _{DDIOVCC} > 80 trip point	(LH2 < LH3)
Low	80 trip point > V _{DDIOVCC}	Struckdown

LOGIC VOLTAGE LEVEL (V _{DDIOVCC}) (V)	R _{PULLUP} (kΩ)	R _{PULLUP} (kΩ)
1.2	0.9	300
3.3	210.2	624

GAIN_SLOT	I²S/I²J GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V _{DD} through 100kΩ ±5% resistor	3

The gain mode can be changed according to the table above

Title: **T-Display-S3-Pro-MVSRLora**Ver: **V1.0**

Company: **LILYGO**

License: **GPL 3.0**

Data:2024-12-14 LastEditTime:2024-12-14