

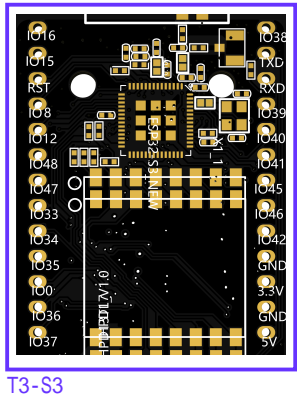
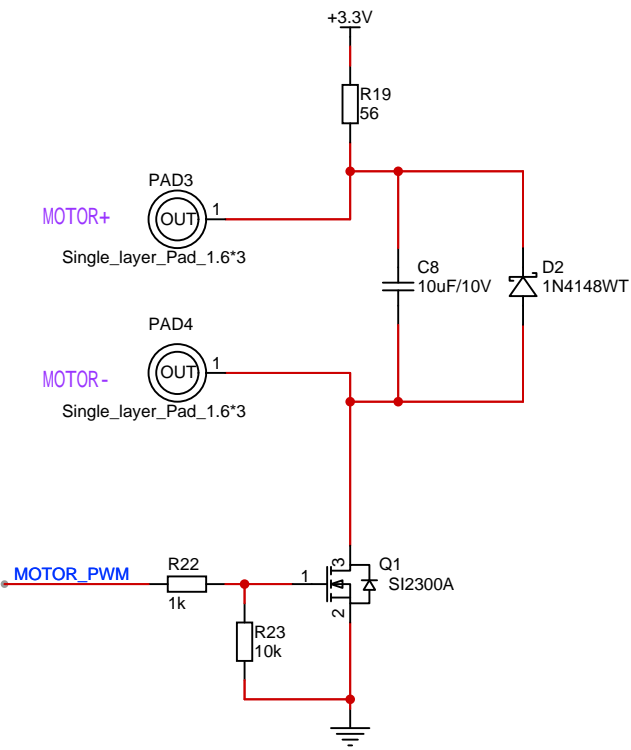
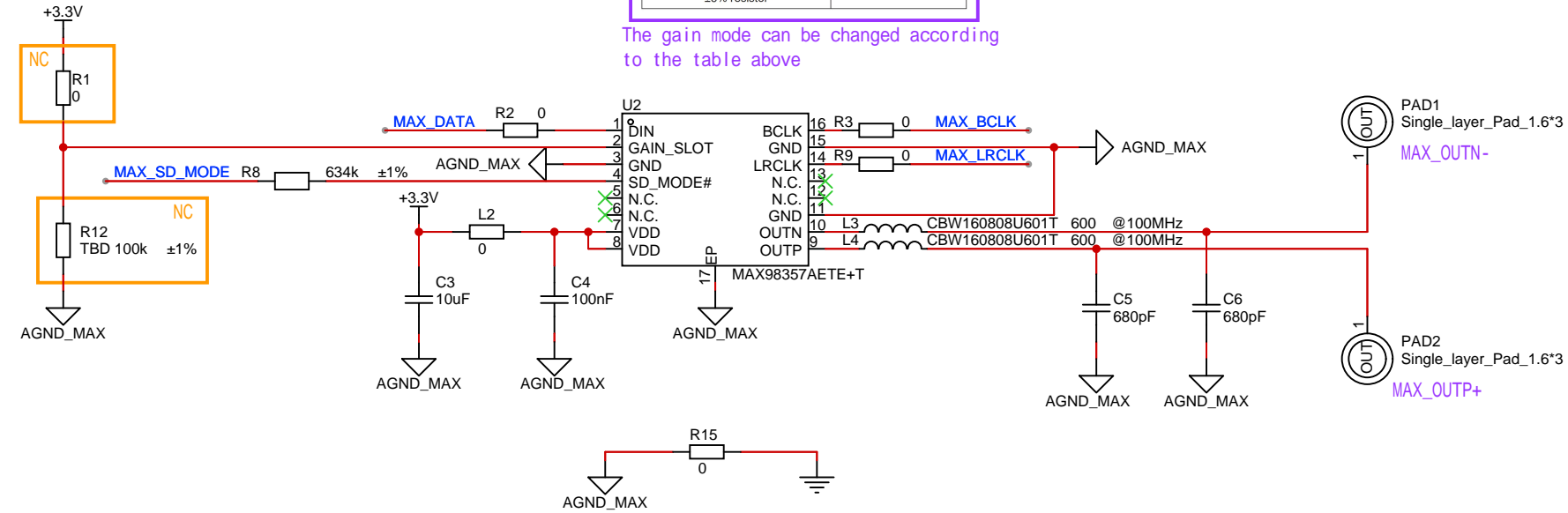
Table 5. SD_MODE Control		
SD_MODE STATUS		SELECTED CHANNEL
High	V <sub>SD_MODE</sub> > B2 trip point	Left
Pullup through R <sub>SMALL</sub>	B2 trip point < V <sub>SD_MODE</sub> < B1 trip point	Right
Pullup through R <sub>LARGE</sub>	B1 trip point < V <sub>SD_MODE</sub> < B0 trip point	(Left2 + right2)
Low	B0 trip point < V <sub>SD_MODE</sub>	Shutdown

Table 6. Examples of SD_MODE Pullup Resistor Values		
LOGIC VOLTAGE LEVEL (V <sub>DDIO</sub> ) (V)	R <sub>SMALL</sub> (kΩ)	R <sub>LARGE</sub> (kΩ)
1.8	69.8	300
3.3	210.2	634

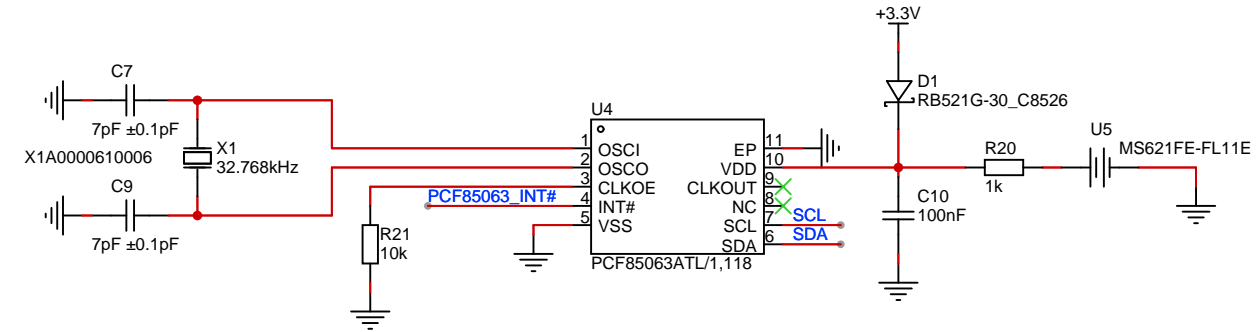
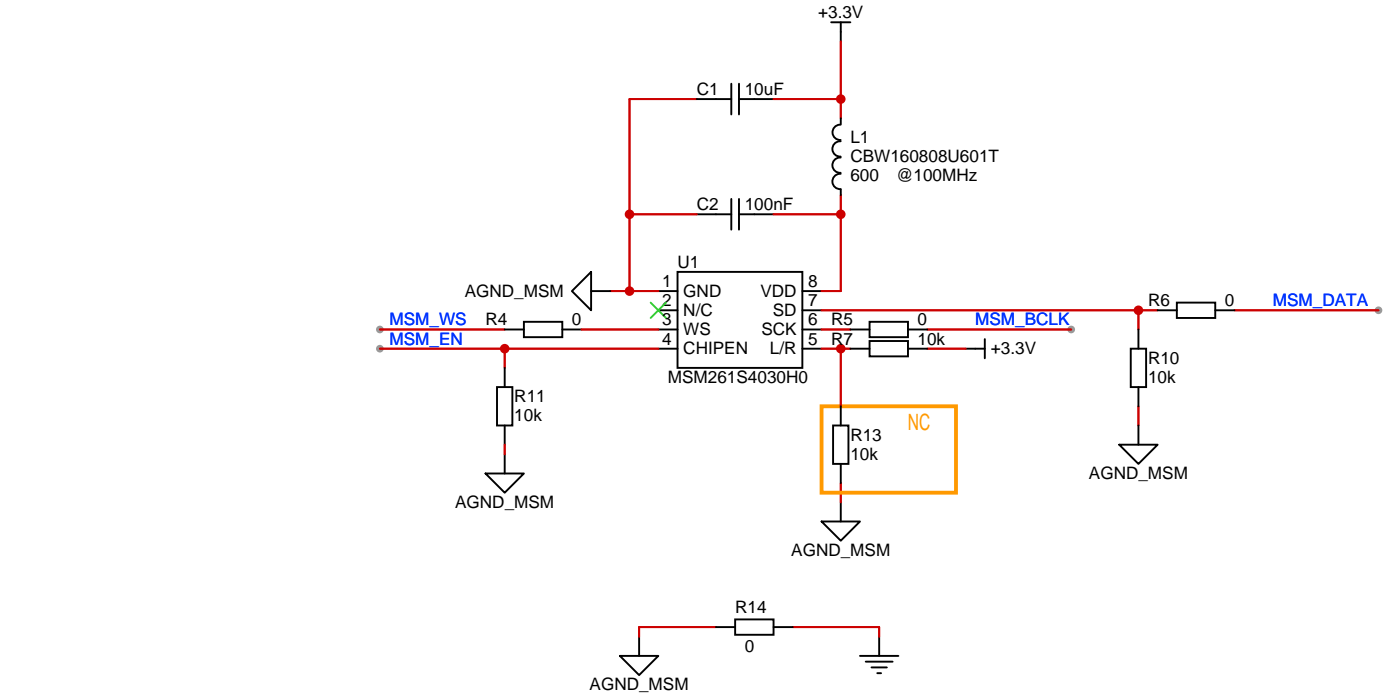
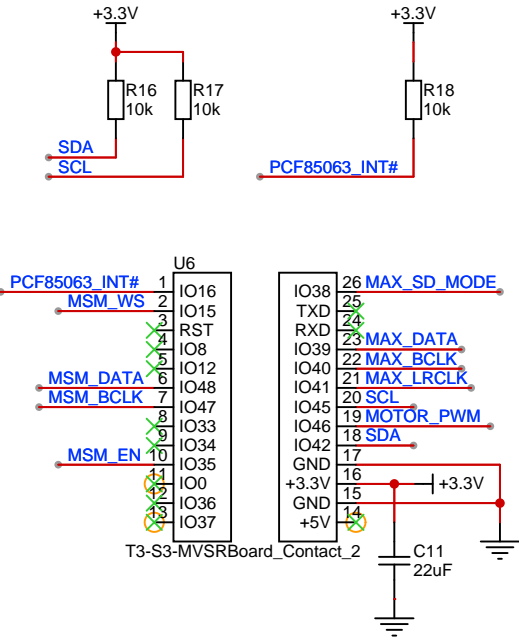
Table 8. Gain Selection	
GAIN_SLOT	I <sup>2</sup> S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V <sub>DD</sub>	6
Connect to V <sub>DD</sub> through 100kΩ ±5% resistor	3

The gain mode can be changed according to the table above

Default mode: I2S/LJ GAIN 6dB  
Attention: Over 6dB may cause broken sound



T3-S3



Title: **T3-S3-MVSRBoard**

Ver: **V1.0**



Company: **LILYGO**

License: **GPL 3.0**

Data: **2024-11-06** LastEditTime: **2024-11-06**