

Table 5. SD_MODE Control		
SD_MODE STATUS		SELECTED CHANNEL
High	V _{SD_MODE} > B2 trip point	Left
Pullup through R _{SMALL}	B2 trip point > V _{SD_MODE} > B1 trip point	Right
Pullup through R _{LARGE}	B1 trip point > V _{SD_MODE} > B0 trip point	(Left2 + right2)
Low	B0 trip point > V _{SD_MODE}	Shutdown

Table 6. Examples of SD_MODE Pullup Resistor Values		
LOGIC VOLTAGE LEVEL (V _{DDIO}) (V)	R _{SMALL} (kΩ)	R _{LARGE} (kΩ)
1.8	69.8	300
3.3	210.2	634

Table 8. Gain Selection	
GAIN_SLOT	I ² S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V _{DD} through 100kΩ ±5% resistor	3

The gain mode can be changed according to the table above

Default mode : I2S/LJ GAIN 6dB
Attention: Over 6dB may cause broken sound

