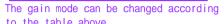
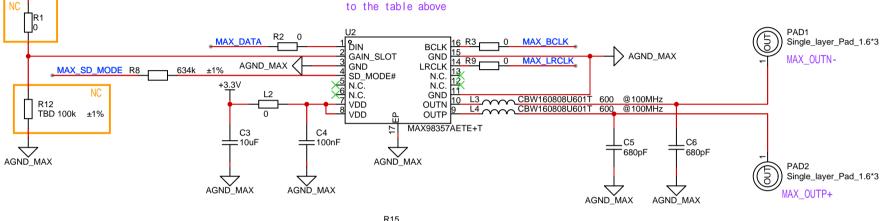


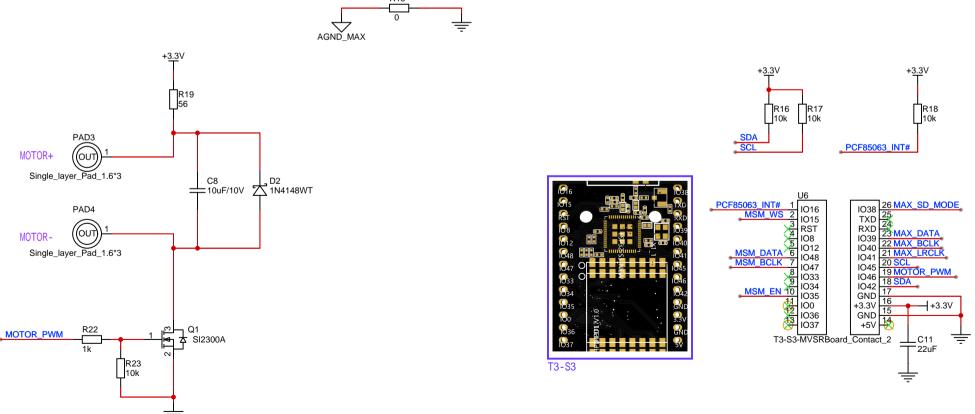
GAIN_SLOT	I2S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V _{DD} through 100kΩ ±5% resistor	3

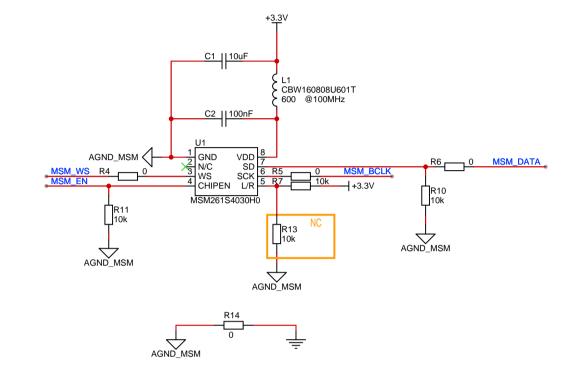
Default mode: I2S/LJ GAIN 6dB

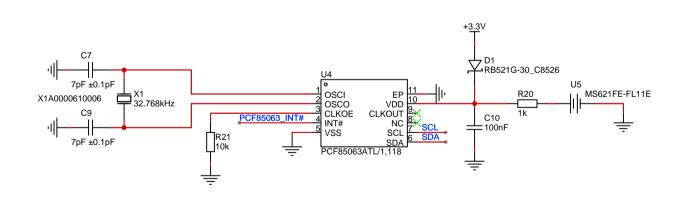
Attention: Over 6dB may cause broken sound











Title: T3-S3-MVSRBoard Ver: V1.0

Company: LILYGO License: GPL 3.0

Data: 2024-11-06 LastEditTime: 2024-11-06