

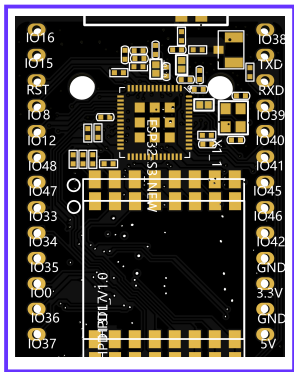
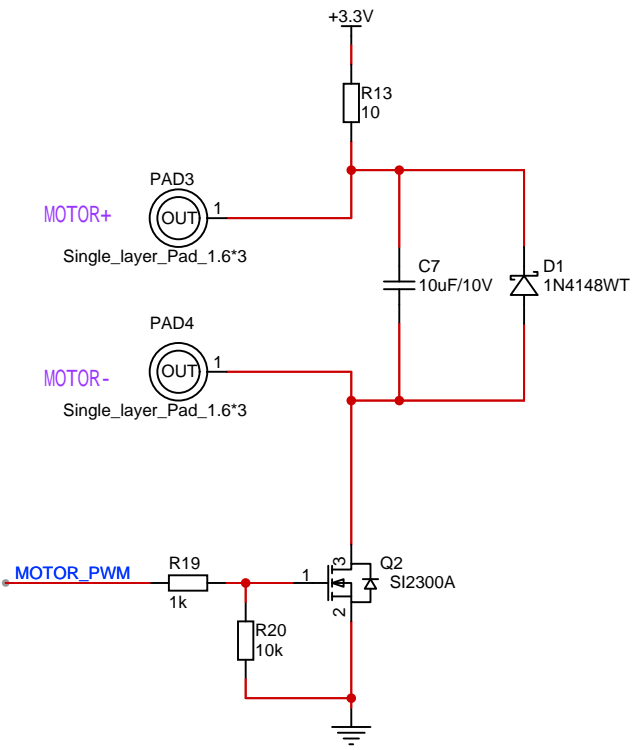
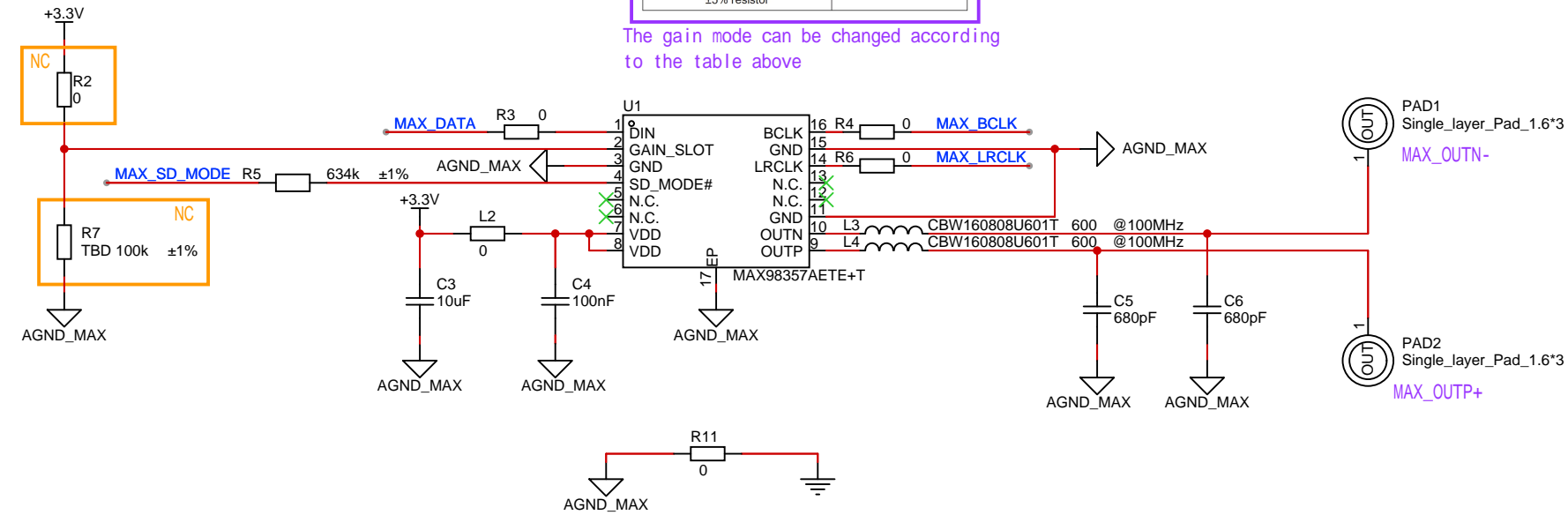
SD_MODE STATUS	SELECTED CHANNEL
High	Left
Pullup through R _{SMALL}	Right
Pullup through R _{LARGE}	(Left2 + right2)
Low	Shutdown

LOGIC VOLTAGE LEVEL (V _{DDIO}) (V)	R _{SMALL} (kΩ)	R _{LARGE} (kΩ)
1.8	69.8	300
3.3	210.2	634

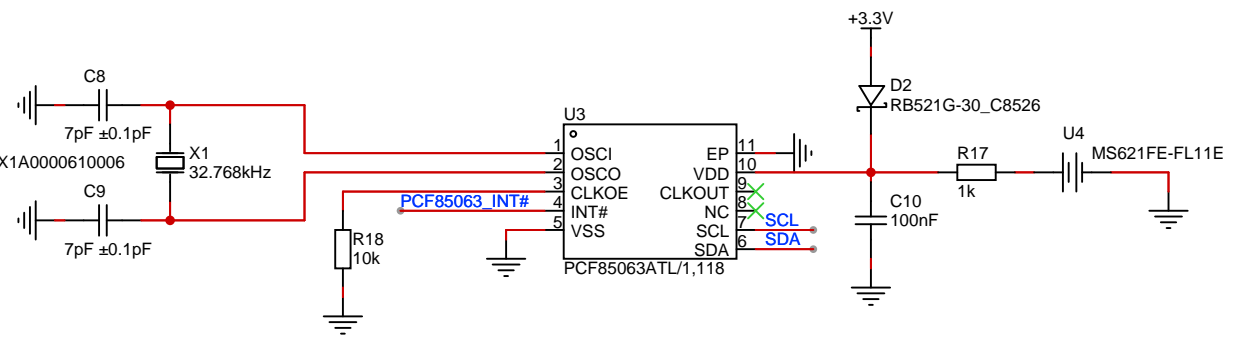
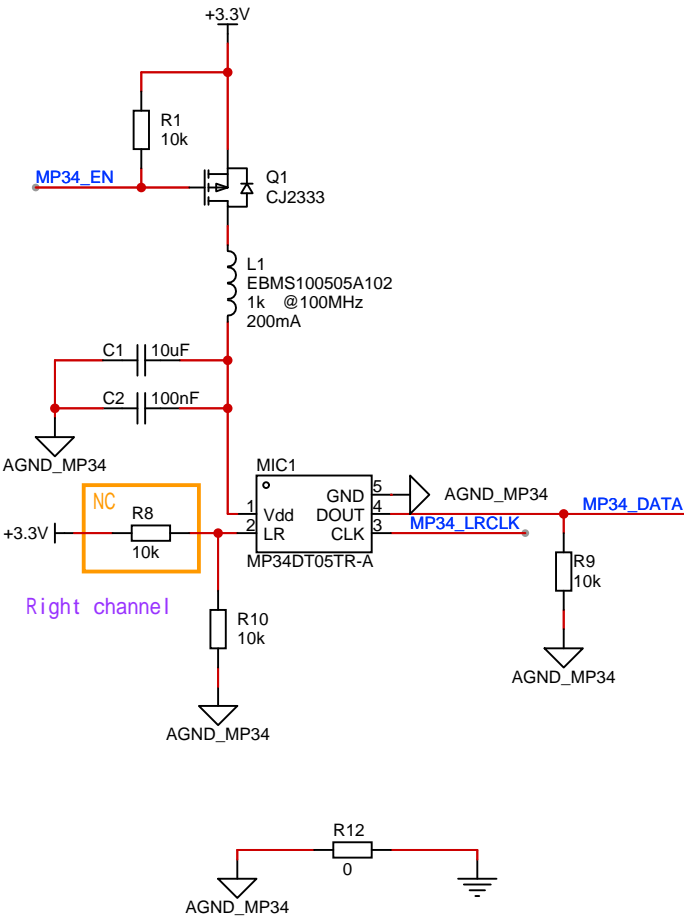
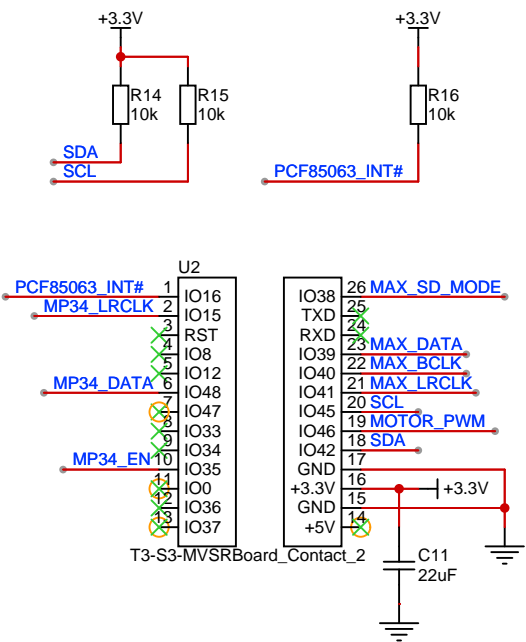
GAIN_SLOT	I ² S/LJ GAIN (dB)
Connect to GND through 100kΩ ±5% resistor	15
Connect to GND	12
Unconnected	9
Connect to V _{DD}	6
Connect to V _{DD} through 100kΩ ±5% resistor	3

Default mode : I2S/LJ GAIN 6dB
Attention: Over 6dB may cause broken sound

The gain mode can be changed according to the table above



T3-S3



Title:

T3-S3-MVSRBoard

Ver:

V1.1

LILYGO®

Company:

LILYGO

License:

GPL 3.0

Data:

2024-11-06

LastEditTime:

2025-08-25