

Shenzhen Wanghong Technology Co., LTD

Tel: 18027661539

Product Specification

Part Name:	OLED Display Module
Customer Part ID:	
WangHong ID:	WH-X096-2864KSWEG02-A6
Ver: 1.0	
Customer:	
Approved by	
From: WangHong	technology Co.,Ltd.
Approved by	

Notes:

- 1. Please contact wanghong technology Co.,Ltd. before assigning your product based on this module specification.
- 2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by wanghong technology Co.,Ltd. for any intellectual property claims or other problems that may result from application based on the module described herein.



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RevisedHistory

Part Number	Revision	Revision Content	Revised on
WH-X096-2864KSWEG02-A6	1.0	New	2024-05-08



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1. Basic Specifications

1.1 Display Specifications

1) Display Mode: Passive Matrix

2) Display Color: Monochrome (White)

3) Drive Duty: 1/64 Duty

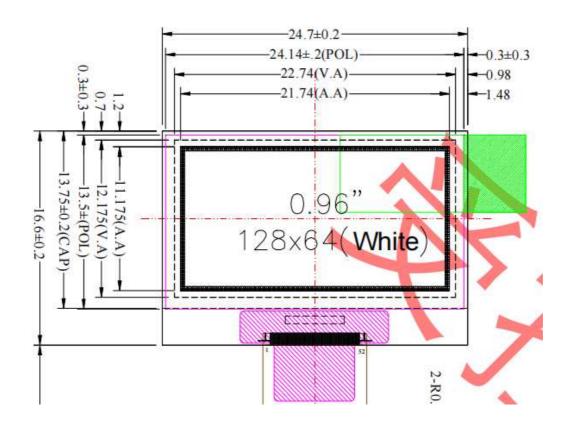
1.2 Mechanical Specifications

1) Outline Drawing: According to the annexed outline drawing

2) Number of Pixels: 128 x 64

3) PCB Size: 26 x 26 x 2.65 (mm)
 4) Active Area: 21.744 x 11.175 (mm)
 5) Pixel Pitch: 0.17 x 0.17 (mm)
 6) Pixel Size: 0.154 x 0.154 (mm)

1.3 Active Area / Memory Mapping & Pixel Construction

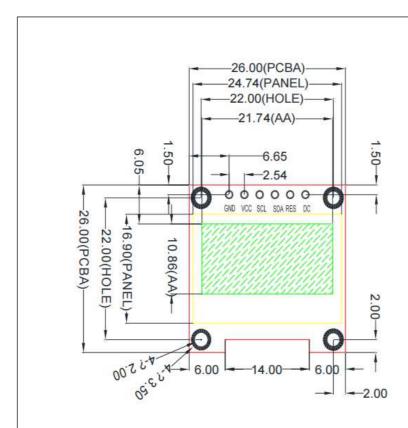


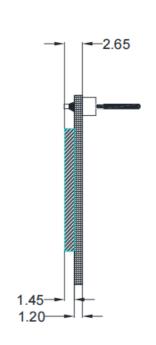


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1.4 Mechanical Drawing





Pin	Pin Descriptions						
Pin Symbol							
1 GND							
2	VCC						
3	SCL						
4	SDA						
5	RES						
6	DC						

NOTES:

1.DISPLAY TYPE: 0.96" OLED 2.VIEWING DIRECTION: All

3.POLARIZER MODE: TRANSMISSIVE/NORMALLY BLACK

4.DRIVER IC: SSD1315 5.Resolution: 128x64 6.INTERFACE: SPI 7.VOLTAGE:3.3V

8.OPERATING TEMP: -40°C~70°C



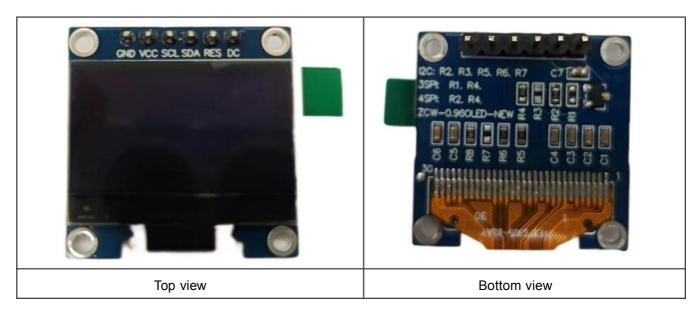
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1.5 Pin Definition

PIN No.	Symbol	Description
1	GND	Ground of Logic Circuit
!	GND	This is a ground pin. It must be connected to ground
		Power Supply for OLED
2	VCC	This is a voltage supply pin. It must be connected to source
3	SCL	The serial clock input SCL
4	SDA	⊤he serial data input SDA
		Power Reset for Controller and Driver
		This pin is reset signal input. When the pin is low, initialization of the chip is
5	RES	executed. Keep this pin pull high during normal operation
		Data/Command Control
		This pin is Data/Command control pin. When the pin is pulled high, the
6	DC	input at SDA is treated as display data. When the pin is pulled low, the input
		at SDA will be transferred to the command register.

1.6 product picture

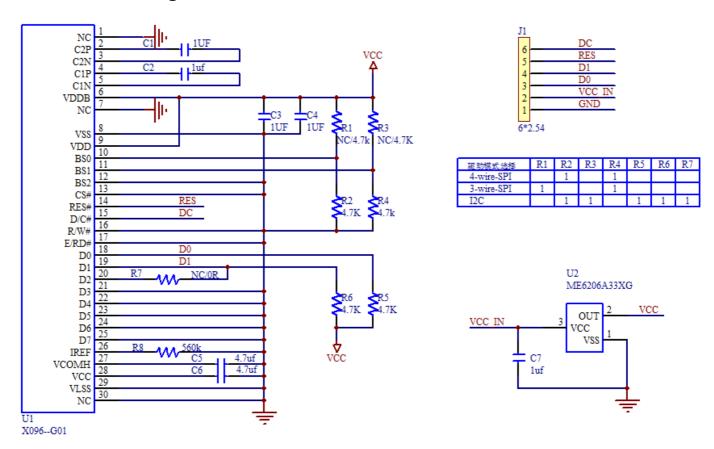




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1.7 Schematic Diagram



SPI Switch to IIC:

- 1. Change R4 to R3. R7=0 Ω
- 2. Ground DC pin and CS pin.
- 3. Use IIC program.



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2. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Display	VCC	3	5	V	1, 2
Supply Voltage for Logic	SCL/SDA	1.65	3.3	V	
Operating Temperature	T _{OP}	-40	80	. C	
Storage Temperature	T _{STG}	-40	85	. C	3
Life Time (120 cd/m²)		10,000	-	hour	4
Life Time (80 cd/m²)		30,000	-	hour	4
Life Time (60 cd/m²)		50,000	-	hour	4

Note 1: All the above voltages are on the basis of "GND = 0V".

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 3. "Optics & Electrical Characteristics". If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

Note 3: The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80. C.

Note 4: VCC = 3.3 V, $T_a = 25^{\circ}\text{C}$, 50% Checkerboard.

Software configuration follows Section 4.4 Initialization.

End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.



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3. Optics & Electrical Characteristics

3.1 Optics Characteristics

Characteristics	Symbol	Conditions	Min	Тур	Max	Unit
Brightness (VCC Supplied Externally)	Lbr	Note 5	90	-	-	cd/m2
Brightness (VCC Generated Internal DC/ DC)	Lbr	Note 6	80	100	-	cd/m2
C.I.E.	(x) (y)	C.I.E. 1931	0.28 0.27	0.32 0.31	0.36 0.35	
Dark Room Contrast	CR		-	2000:1	-	
Viewing Angle			-	Free	-	degree

^{*}Optical measurement taken at VDD = 2.8V, VCC = 9 &7.25V. Software configuration follows Section 4.2 Initialization.

3.2 DC Characteristics

Characteristics	Symbol	Symbol Conditions		Тур	Max	Unit
Supply Voltage for Logic	VDD		1.65	2.8	3.3	V
Supply Voltage for Display (Supplied Externally)	VCC	Note 5 (Internal DC/ DC Disable)	-	12	-	V
Supply Voltage for DC/ DC	VBAT	Internal DC/ DC Enable	3.5	-	4.2	V
Supply Voltage for Display (Generated by Internal DC/ DC)	VCC	Note 6 (Internal DC/ DC Enable)	7	-	7.5	V
High Level Input	VIH	IOUT = 100μA, 3.3MHz	0.8xVD D	_	VDD	V
Low Level Input	VIL	IOUT = 100μA, 3.3MHz	0	_	0.2xVD D	V
High Level Output	VOH	IOUT = 100μA, 3.3MHz	0.9xVD D	-	VDD	V
Low Level Output	VOL	IOUT = 100μA, 3.3MHz	0	_	0.1xVD D	V
Operating Current for VDD	IDD		-	180	300	uA
Operating Current for VCC (VCC Supplied Externally)	ICC	Note 7	-	9	15	mA



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Operating Current for VBAT (VCC Generated by Internal DC/ DC)	IBAT	Note 8	-	25.6	32	mA
Sleep Mode Current for VDD			-	1	5	uA
Sleep Mode Current for VCC			-	2	10	uA

Note 5 & 6: Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer's request.

Note 7: VDD = 2.8V, VCC = 12V, 100% Display Area Turn on.

Note 8: VDD = 2.8V, VCC = 7.25V, 100% Display Area Turn on.

3.3 AC Characteristics

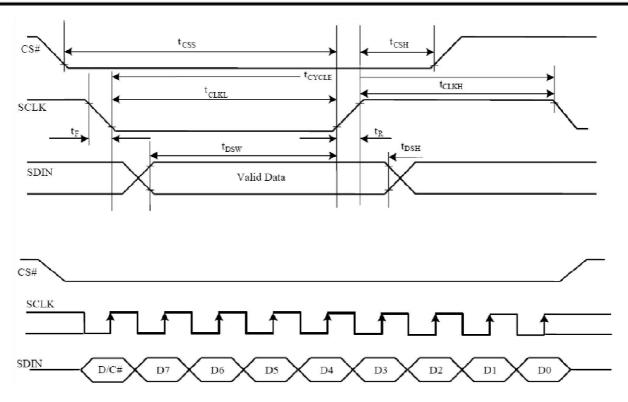
3.3.1 SPI Interface Timing Characteristics:

Symbol	Description	Min	Max	Unit
tcycle	Clock Cycle Time	100	-	ns
tCSS	Chip Select Setup Time	20	-	ns
tCSH	Chip Select Hold Time	10	-	ns
tDSW	Write Data Setup Time	15	-	ns
tDHW	Write Data Hold Time	15	-	ns
tCLKL	Clock Low Time	20	-	ns
tCLKH	Clock High Time	20	-	ns
tR	Rise Time	-	40	ns
tF	Fall Time	-	40	ns

^{*} Software configuration follows Section 4.2 Initialization.



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4 Functional Specification

4.1 Commands

	COIII	1114	iii u s										
D/C	Hex	D	D	D	D	D3	D2	D	D0	Command	Description		
0	81	1	0	0	0	0	0	0	1		Double byte command to select 1 out		
0	A[7:0]	A7	A6	A5	A4	A3	A2	A1	A0	Set Contrast	256 contrast steps. Contrast increases as		
										Control	the value increases.		
											(RESET = 7Fh)		
0	DB	1	1	0	1	1	0	1	1		A[6:4] Hex V COMH deselect level		
0	A[7:0]	0	A6	A5	A4	0	0	0	0		cod		
										Set VCOMH	000b 00h ~ 0.65 x VCC		
										Deselect Level	010b 20h ~ 0.77 x VCC (RESET)		
											011b 30h ~ 0.83 x VCC		
0	A6/A7	1	0	1	0	0	1	1	X0		A6h, X[0]=0b: Normal display (RESET) 0		
										Set	in RAM: OFF in display panel 1		
										Normal/Inverse	in RAM: ON in display panel		
										Display	A7h, X[0]=1b: Inverse display		
											0 in RAM: ON in display panel		
0	AE	1	0	1	0	1	1	1	X0	Set Display	AEh,X[0]1=0b:DAispla OFFsleelp mode)l		
	AF									ON/OFF	(RESET)		
										010011	AFh X[0]=1b:Display ON in normal mode		
0	A0/A1	1	0	1	0	0	0	0	X0		A0h, X[0]=0b: column address 0 is		
										Set Segment	mapped to SEG0 (RESET)		
										Re -map	A1h, X[0]=1b: column address 127 is		
											mapped to SEG0		
0	C0/C8	1	1	0	0	X3	0	0	0		C0h, X[3]=0b: normal mode (RESET)		
										Set COM	Scan from COM0 to COM[N – 1]		
										Output Scan	C8h, X[3]=1b: remapped mode.		
										Direction	Scan from COM[N-1] to		
											СОМО		

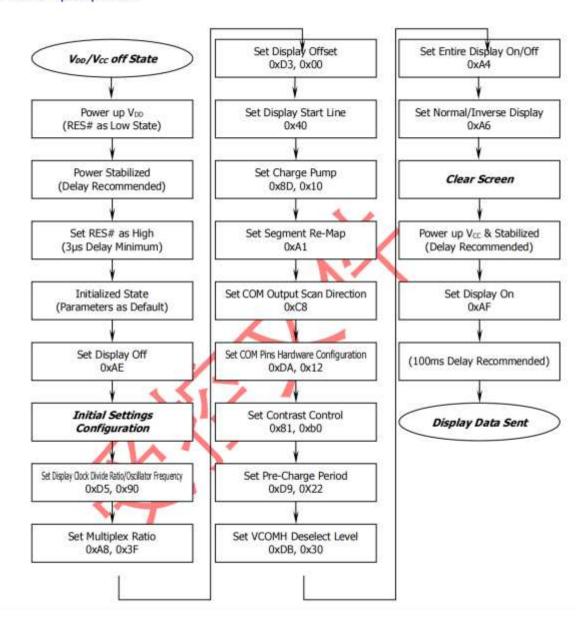


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4.2 VCC Generated by Internal DC/DC Circuit

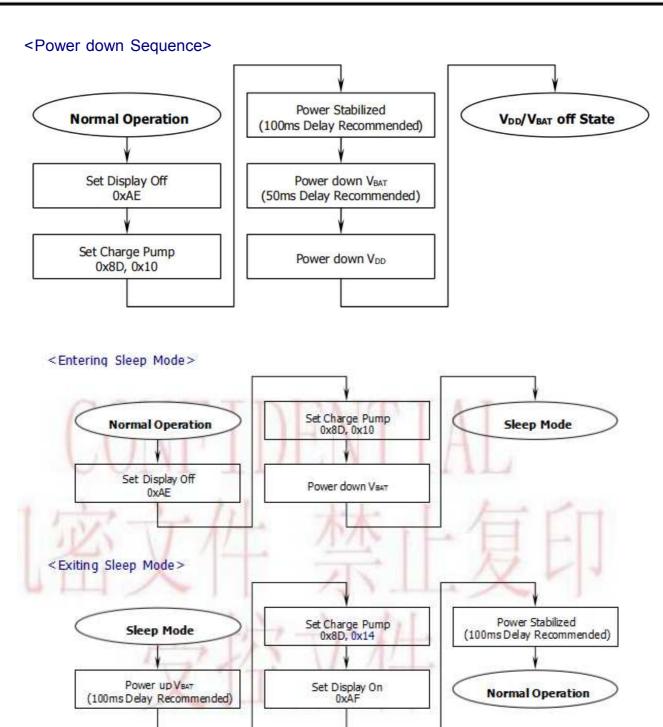
<Power up Sequence>



If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.



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```
{
//OLED 复位
OLED RES C Ir();//RES 置 0
delay ms(200);//延时 200ms
OLED_RES_Set();//RES 置 1
//OLED 初始化
OLED_WR_Byte(0xAE,OLED_CMD);//--turn off oled panel
OLED WR Byte(0x00,OLED CMD);//---set low column address
OLED WR Byte(0x10,OLED CMD);//---set high column address
OLED WR Byte(0x40,OLED CMD);//--set start line address
OLED WR Byte(0x81,OLED CMD);//--set contrast control register
OLED WR Byte(0xCF,OLED CMD);// Set SEG Output Current Brightness
OLED_WR_Byte(0xA1,OLED_CMD);//--Set SEG/Column Mapping
                                                               0xa0 左右反置 0xa1 正常
                                                              0xc0 上下反置 0xc8 正常
OLED_WR_Byte(0xC8,OLED_CMD);//Set COM/Row Scan Direction
OLED WR Byte(0xA6,OLED CMD);//--set normal display
OLED WR Byte(0xA8,OLED CMD);//--set multiplex ratio(1 to 64)
OLED WR Byte(0x3f,OLED CMD);//--1/64 duty
OLED_WR_Byte(0xD3,OLED_CMD);//-set display offset Shift Mapping RAM Counter (0x00~0x3F)
OLED_WR_Byte(0x00,OLED_CMD);//-not offset
OLED WR Byte(0xd5,OLED CMD);//--set display clock divide ratio/oscillator frequency
OLED_WR_Byte(0x80,OLED_CMD);//--set divide ratio, Set Clock as 100 Frames/Sec
OLED_WR_Byte(0xD9,OLED_CMD);//--set pre-charge period
OLED_WR_Byte(0xF1,OLED_CMD);//Set Pre-Charge as 15 Clocks & Discharge as 1 Clock
OLED WR Byte(0xDA,OLED CMD);//--set com pins hardware configuration
OLED WR Byte(0x12,OLED CMD);
OLED WR Byte(0xDB,OLED CMD);//--set vcomh
OLED_WR_Byte(0x30,OLED_CMD);//Set VCOM Deselect Level
OLED WR Byte(0x20,OLED CMD);//-Set Page Addressing Mode (0x00/0x01/0x02)
OLED WR Byte(0x02,OLED CMD);//
OLED WR Byte(0x8D,OLED CMD);//--set Charge Pump enable/disable
OLED WR Byte(0x14,OLED CMD);//--set(0x10) disable
OLED_Clear();
OLED WR Byte(0xAF,OLED CMD);
}
```



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```
#define OLED_CMD
                      0 //write command
#define OLED_DATA
                       1 //write data
void OLED_WR_Byte(u8 dat,u8 cmd)
{
  u8 i;
  if(cmd)
    OLED_DC_Set();
    OLED_DC_C Ir();
    OLED_CS_C Ir();
 for(i=0;i<8;i++)
 {
    OLED_SCL_C Ir();
  if(dat&0x80)
    OLED_SDA_Set();
  else
    OLED_SDA_C Ir();
    OLED_SCL_Set();
  dat<<=1;
  OLED_CS_Set();
  OLED_DC_Set();
}
```



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5. Reliability

5.1 Contents of Reliability Tests

Item	Conditions	Criteria
High Temperature Operation	70。C, 240 hrs	
Low Temperature Operation	-40。C, 240 hrs	
High Temperature Storage	85。C, 240 hrs	The operational
Low Temperature Storage	-40。C, 240 hrs	functions work.
High Temperature/Humidity Operation	60。C, 90% RH, 120 hrs	
Thermal Shock	-40 ° C ⇔ 85 ° C, 24 cycles 60 mins dwell	

used for the above tests do not include polarizer.

5.2 Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23 \pm 5. C; 55 \pm 15% RH.

6.Outgoing Quality Control Specifications

6.1 Environment Required

Customer's test & measurement are required to be conducted under the following conditions:

Temperature: 23 \pm 5. C Humidity: 55 \pm 15% RH

Fluorescent Lamp: 30W
Distance between the Panel & Lamp: ≥ 50cm
Distance between the Panel & Eyes of the Inspector: ≥ 30cm
Finger glove (or finger cover) must be worn by the inspector.

Inspection table or jig must be anti-electrostatic.

^{*} No moisture condensation is observed during tests.



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6.2 Sampling Plan

Level II, Normal Inspection, Single Sampling, MIL-STD-105E

6.3 Criteria & Acceptable Quality Level

Partition	AQL	Definition	
Major	0.65	Defects in Pattern Check (Display On)	
Minor	1.0	Defects in Cosmetic Check (Display Off)	

6.3.1 Cosmetic Check (Display Off) in Non-Active Area

Check Item	Classification	Criteria
Panel General Chipping	Minor	X > 6 mm (Along with Edge) Y > 1 mm (Perpendicular to edge)

6.3.2 Cosmetic Check (Display Off) in Active Area

It is recommended to execute in clear room environment (class 10k) if actual in necessary.

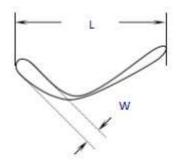
Check Item	Classification	Criteria
Any Dirt & Scratch on Polarizer's	Acceptable	Ignore for not Affect the Polarizer
Protective Film		

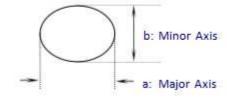


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Scratches, Fiber, Line-Shape Defect (On Polarizer)	Minor	W ≤ 0.1 W > 0.1 L ≤ 2 L > 2	lgnore n ≤ 1 n = 0
Dirt, Black Spot, Foreign Material, (On Polarizer)	Minor	Φ ≤ 0.1 0.1 < Φ ≤ 0.25 0.25 < Φ	Ignore n ≤ 1 n = 0
Dent, Bubbles, White spot (Any Transparent Spot on Polarizer)	Minor	$\Phi \le 0.5$ \Rightarrow Ignore if no $0.5 < \Phi$ $n = 0$	Influence on Display
Fingerprint, Flow Mark (On Polarizer)	Minor	Not .	Allowable

- * Protective film should not be tear off when cosmetic check.
- ** Definition of W & L & Φ (Unit: mm): Φ = (a + b) / 2







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6.3.3 Pattern Check (Display On) in Active Area

Check Item	Classification	Criteria
No Display	Major	
Missing Line	Мајог	
Pixel Short	Major	
Darker Pixel	Major	
Wrong Display	Major	
Un-uniform	Major	



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7. Precautions When Using These OEL Display Modules

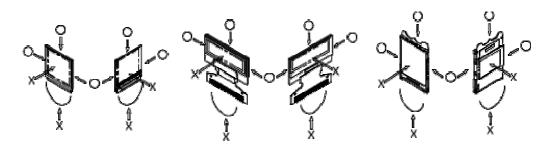
4. 1 Handling Precautions

- 1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
- 2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- 3) If pressure is applied to the display surface or its neighborhood of the OEL display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- 4) The polarizer covering the surface of the OEL display module is soft and easily scratched. Please be careful when handling the OEL display module.
- 5) When the surface of the polarizer of the OEL display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
- * Scotch Mending Tape No. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent such as ethyl alcohol, since the surface of the polarizer will become cloudy.

Also, pay attention that the following liquid and solvent may spoil the polarizer:

- * Water
- * Ketone
- * Aromatic Solvents
- 6) Hold OEL display module very carefully when placing OEL display module into the system housing. Do not apply excessive stress or pressure to OEL display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



- 7) Do not apply stress to the driver IC and the surrounding molded sections.
- 8) Do not disassemble nor modify the OEL display module.



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- 9) Do not apply input signals while the logic power is off.
- 10) Pay sufficient attention to the working environments when handing OEL display modules to prevent occurrence of element breakage accidents by static electricity.
- * Be sure to make human body grounding when handling OEL display modules.
- * Be sure to ground tools to use or assembly such as soldering irons.
- * To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
- * Protective film is being applied to the surface of the display panel of the OEL display module. Be careful since static electricity may be generated when exfoliating the protective film.
- 11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OEL display module has been stored for a long period of time, residue adhesive material of the protection film may remain on the surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5).
- 12) If electric current is applied when the OEL display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

7.2 Storage Precautions

- 1) When storing OEL display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. and, also, avoiding high temperature and high humidity environment or low temperature (less than 0 °C) environments. (We recommend you to store these modules in the packaged state when they were shipped from wanghong technology Co., Ltd.)
- At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
- 2) If electric current is applied when water drops are adhering to the surface of the OEL display module, when the OEL display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

7.3 Designing Precautions

- 1) The absolute maximum ratings are the ratings which cannot be exceeded for OEL display module, and if these values are exceeded, panel damage may be happen.
- 2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
- 3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)
- 4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- 5) As for EMI, take necessary measures on the equipment side basically.
- 6) When fastening the OEL display module, fasten the external plastic housing section.



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- 7) If power supply to the OEL display module is forcibly shut down by such errors as taking out the main battery while the OEL display panel is in operation, we cannot guarantee the quality of this OEL display module.
- 8) The electric potential to be connected to the rear face of the IC chip should be as follows: SSD1315
- * Connection (contact) to any other potential than the above may lead to rupture of the IC.

7.4 Precautions when disposing of the OEL display modules

1) Request the qualified companies to handle industrial wastes when disposing of the OEL display modules. Or, when burning them, be sure to observe the environmental and hygienic laws and regulations.

7.5 Other Precautions

- 1) When an OEL display module is operated for a long of time with fixed pattern may remain as an after image or slight contrast deviation may occur.
 - Nonetheless, if the operation is interrupted and left unused for a while, normal state can be restored. Also, there will be no problem in the reliability of the module.
- 2) To protect OEL display modules from performance drops by static electricity rapture, etc., do not touch the following sections whenever possible while handling the OEL display modules.
 - * Pins and electrodes
 - * Pattern layouts such as the FPC
- 3) With this OEL display module, the OEL driver is being exposed. Generally speaking, semiconductor elements change their characteristics when light is radiated according to the principle of the solar battery. Consequently, if this OEL driver is exposed to light, malfunctioning may occur.
 - * Design the product and installation method so that the OEL driver may be shielded from light in actual usage.
 - * Design the product and installation method so that the OEL driver may be shielded from light during the inspection processes.
- 4) Although this OEL display module stores the operation state data by the commands and the indicationdata, when excessive external noise, etc. enters into the module, the internal status may be changed. It therefore is necessary to take appropriate measures to suppress noise generation or to protect from influences of noise on the system design.
- 5) We recommend you to construct its software to make periodical refreshment of the operation statuses (re-setting of the commands and re-transference of the display data) to cope with catastrophic noise.



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Warranty:

The warranty period shall last twelve (12) months from the date of delivery. Buyer shall be completed to assemble all the processes within the effective twelve (12) months. wanghong technology Co.,Ltd. shall be liable for replacing any products which contain defective material or process which do not conform to the product specification, applicable drawings and specifications during the warranty period. All products must be preserved, handled and appearance to permit efficient handling during warranty period. The warranty coverage would be exclusive while the returned goods are out of the terms above.

Notice:

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