

Analogue Electronics

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<https://hxp.plus/>

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Chapter 1

Basics of Circuits

1.1 The direction of current and voltage

In complex problems, we can not always know the direction of currents or voltage. The usual solution is to assume a direction, and use it to solve problems. If the solution of current or voltage, is positive, then the position is just what we assumed, and vice versa.

1.2 How to determine whether a component is consuming or providing energy

For resistors, resistors are always consuming energy.

For power sources, if the direction of current is from the positive electrode to the negative electrode, then the power source is consuming energy, and vice versa.

1.3 Eletronic Components

1.3.1 Resistors

$$U = -IR$$

1.3.2 Power Sources

(Controlled) Voltage Source

$$P = UI$$

The resistance of an ideal voltage source is : 0

Note that an ideal voltage source must not be short-circuited.

(Controlled) Current Source

The current through a current source is only decided by the source itself.

The resistance of an ideal current source is : ∞

Note that an ideal current source must not be open-circuited.

1.4 Kirchhoff's Laws

- branch
- node
- loop
- mesh

1.4.1 Kirchhoff's Current Law

For each node in the circuit, as the node can not accumulate charges, the sum of current is zero.

$$\sum I = 0$$

1.4.2 Kirchhoff's Voltage Law

For each loop in circuit, the sum of the voltage in of all branches is zero.

$$\sum U = 0$$

1.5 Gain of an Amplifier Circuit

	Voltage	Current	Transresistance	Transconductance	Power
Gain	$A_v = \frac{v_o}{v_i}$	$A_i = \frac{i_o}{i_i}$	$A_r = \frac{v_o}{i_i}$	$A_g = \frac{i_o}{v_i}$	$A_p = \frac{P_o}{P_i}$
Gain in dB	$20 \lg A_v $	$20 \lg A_i $	$20 \lg A_r $	$20 \lg A_g $	$10 \lg A_p$

Chapter 2

Operational Amplifier

2.1 Operational Amplifier

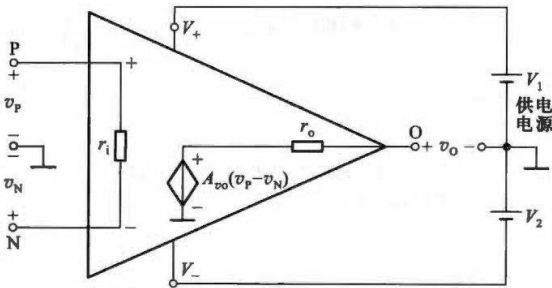
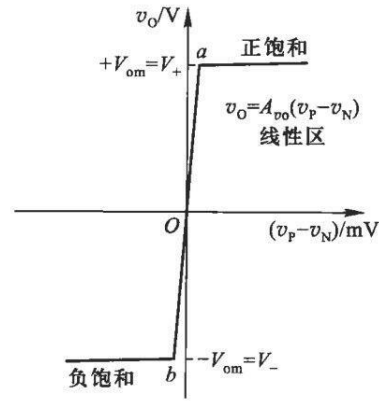


图 2.1.3 运算放大器的电路简化模型

(a) the model of an operational amplifier

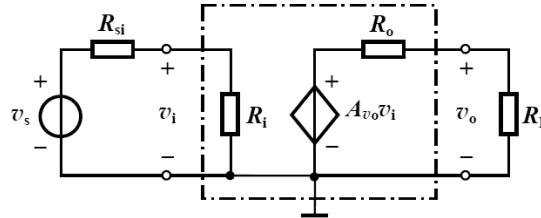


(b) How the output voltage varies

We define r_i as the input impedance, r_o as the output impedance. v_p as the non-inverting input, v_n the inverting input.

Usually, we have $v_p \approx v_n$, $r_i \approx \infty$, $r_o \approx 0$.

Note that the output voltage of a operational amplifier has limits, called **Bandwidth**. When the input voltage exceeds the limits, it outputs the maximum or minimum value.



$$A_v = \frac{v_o}{v_i} = A_{vo} \cdot \frac{R_L}{R_o + R_L}$$

The output resistance may infect the gain of amplifier. The larger R_L is, the more A_v approaches A_{vo} , while the ideal case is when $R_o = 0$

2.2 Ideal Operational Amplifier

For ideal operational amplifier:

- $v_p = v_n$, $i_i = 0$, $r_i = \infty$
- $r_o = 0$, $v_o = A_{vo}(v_p - v_n)$
- $bandwidth = \infty$

Here is a model which shows all the feature of an ideal operational comparator:

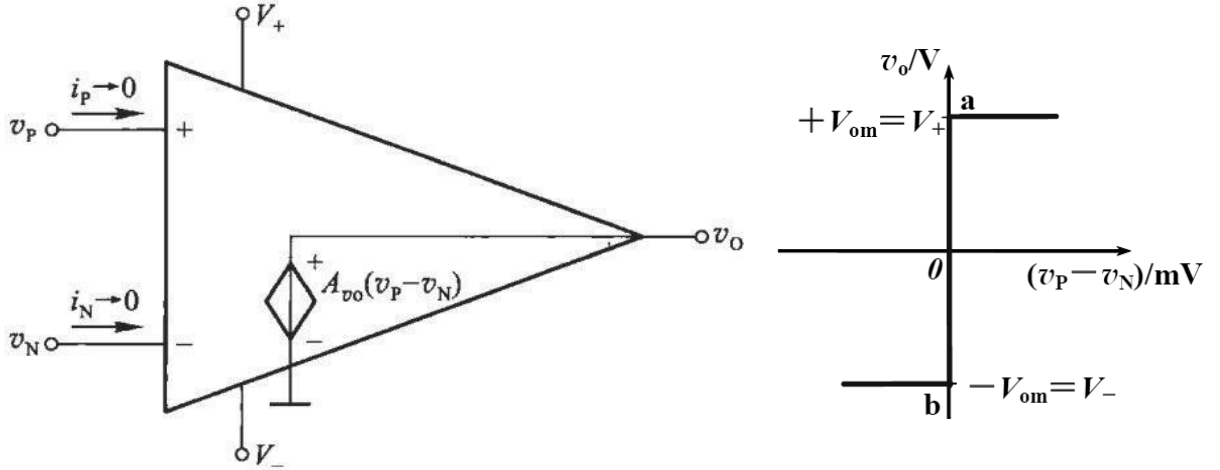


Figure 2.2: An ideal operational amplifier

2.3 Closed-loop Amplifier

Usually operational amplifiers are used with vegetative feedback to ensure its stability. We apply a portion of output voltage to input, reducing the gain of a circuit.

2.3.1 Non-inverting Operational Amplifier

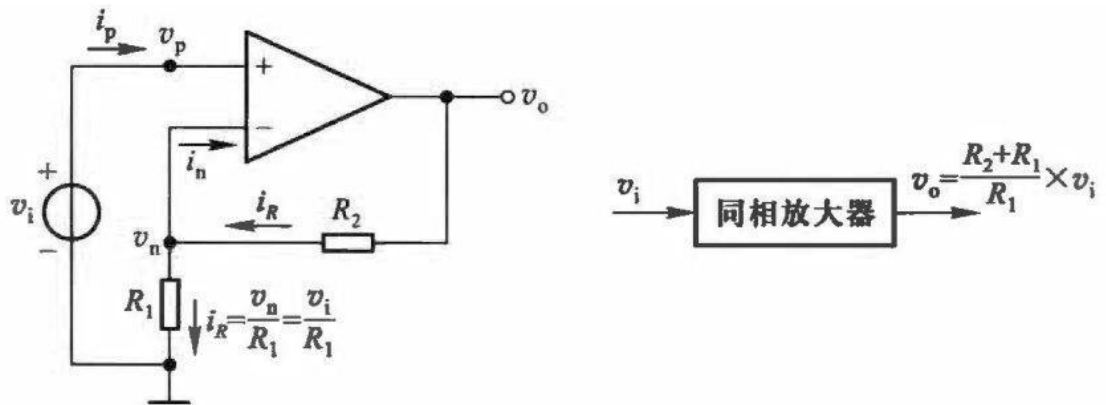


Figure 2.3: An non-inverting amplifier

$$A_{vo} = \frac{R_2 + R_1}{R_1} = 1 + \frac{R_2}{R_1}$$

Note that when $R_2 \ll R_1$, $A_{vo} = 1$, $v_i = v_o$.

2.3.2 Inverting Operational Amplifier

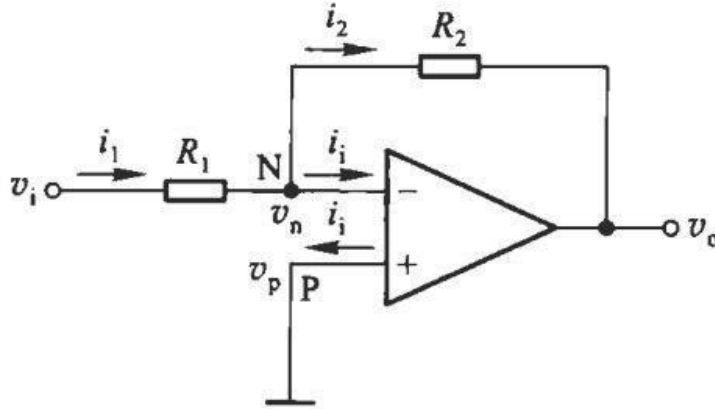


Figure 2.4: An inverting amplifier

$$A_{vo} = -\frac{R_2}{R_1}$$

2.4 Applications of operational amplifiers

2.4.1 Subtraction Circuit

An subtraction circuit can calculate the difference of inverting input and non-inverting input.

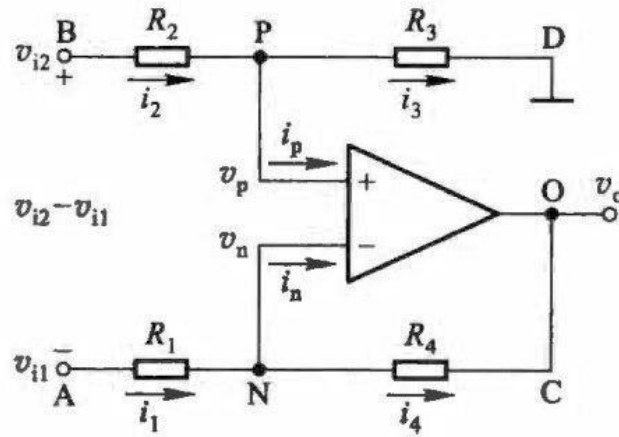


Figure 2.5: A subtraction circuit

$$\begin{cases} \frac{v_{i2} - v_p}{R_2} = \frac{v_p}{R_3} \\ \frac{v_{i1} - v_n}{R_1} = \frac{v_n - v_o}{R_4} \end{cases}$$

$$\begin{cases} R_3(v_{i2} - v_p) = R_2 v_p & \Rightarrow v_p = \frac{R_3}{R_2 + R_3} v_{i2} \\ R_4(v_{i1} - v_n) = R_1(v_n - v_o) & \Rightarrow v_n = \frac{R_4 v_{i1} + R_1 v_o}{R_4 + R_1} \end{cases}$$

$$\frac{R_3}{R_2 + R_3} v_{i2} = \frac{R_4 v_{i1} + R_1 v_o}{R_4 + R_1}$$

$$\frac{R_3 (R_4 + R_1)}{R_2 + R_3} v_{i2} = R_4 v_{i1} + R_1 v_o$$

\Rightarrow

$$v_o = \frac{\frac{R_4}{R_1} + 1}{\frac{R_2}{R_3} + 1} v_{i2} - \frac{R_4}{R_1} v_{i1} = \left(1 + \frac{R_4}{R_1}\right) \frac{\frac{R_3}{R_2}}{1 + \frac{R_3}{R_2}} v_{i2} - \frac{R_4}{R_1} v_{i1}$$

If $R_4/R_1 = R_3/R_2 = r$, then

$$v_o = (1 + r) \frac{r}{1 + r} v_{i2} - r v_{i1} = r (v_{i2} - v_{i1})$$

$$A_v = r = \frac{R_4}{R_1} = \frac{R_3}{R_2}$$

2.4.2 Sum Circuit

An sum circuit adds the inverting input and the non-inverting input.

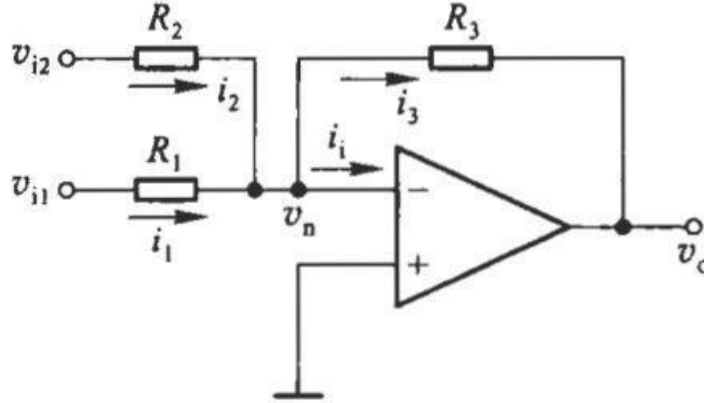


Figure 2.6: Sum Circuit

Similarly, we have

$$\begin{cases} \frac{v_{i1} - v_n}{R_1} + \frac{v_{i2} - v_n}{R_2} = \frac{v_n - v_o}{R_3} \\ v_n = v_p = 0 \end{cases}$$

\Rightarrow

$$\frac{v_{i1}}{R_1} + \frac{v_{i2}}{R_2} = \frac{-v_o}{R_3}$$

$$v_o = - \left(\frac{R_3}{R_1} v_{i1} + \frac{R_3}{R_2} v_{i2} \right)$$

When we set

$$R_1 = R_2 = R_3$$

We have

$$v_o = -(v_{i1} + v_{i2})$$

2.4.3 Integrating Circuit

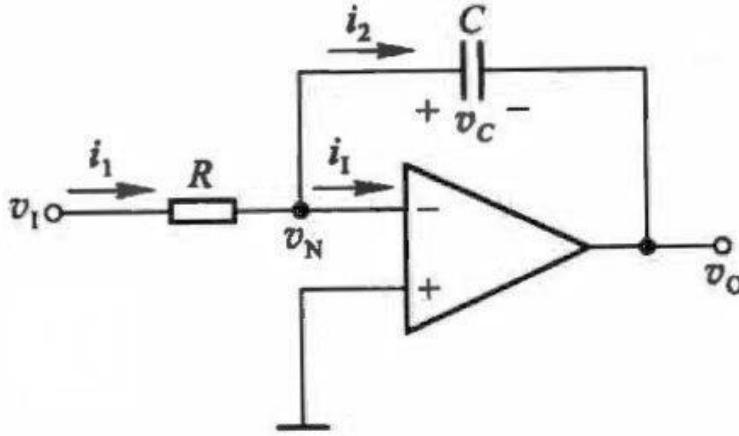


Figure 2.7: integrating circuit

$$C = \frac{Q}{U} \Rightarrow U = \frac{Q}{C} = \frac{1}{C} \int I dt = \frac{1}{C} \int \frac{v_i}{R} dt = \frac{1}{RC} \int v_i dt$$

$$0 - v_o = U$$

\Rightarrow

$$v_o = -\frac{1}{RC} \int v_i dt$$

We define

$$\tau = RC$$

Then

$$-v_o = \frac{1}{\tau} \int v_1 dt$$

2.4.4 Differential Circuit

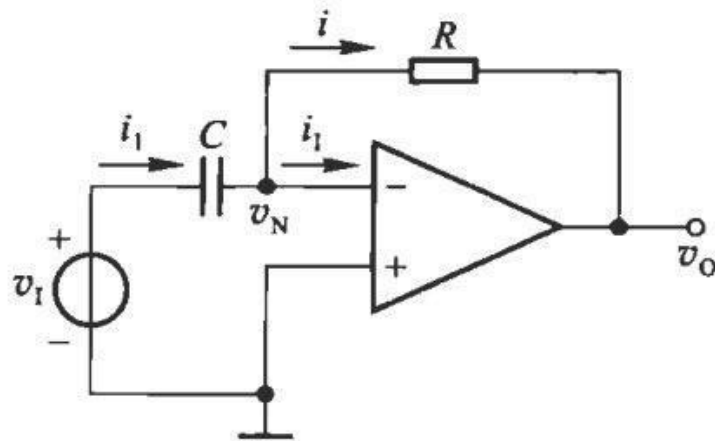


Figure 2.8: differential circuit

$$\begin{cases} i_i = \frac{dQ}{dt} = C \frac{dv_i}{dt} \\ i = \frac{v_o}{R} \\ i_i = i \end{cases}$$

 \Rightarrow

$$-v_o = RC \frac{dv_i}{dt} = \tau \frac{dv_i}{dt}$$

Chapter 3

Diodes

3.1 Semiconductors

3.1.1 Intrinsic Semiconductor

An intrinsic semiconductor, also called as undoped semiconductor, is a pure semiconductor without and significant dopant species present. Two factors are responsible to the current pass through it:

- Excited electrons
- Holes

However, we seldom use intrinsic semiconductors.

3.1.2 Extrinsic Semiconductor

An extrinsic semiconductor is a semiconductor that has been doped, which has more features and provides more charge carriers.

P-type semiconductor

P-type semiconductors are created by doping some electron acceptor elements during manufacture. It has more holes, holes are major carriers of the current.

N-type semiconductor

N-type semiconductors are created by doping some electron donor elements during manufacture. It has more electrons, electrons are major carriers of the current.

3.2 P-N Junction and Diode

When we combine the 2 types of extrinsic semiconductor together, we found some interesting features. As p-type semiconductors use holes to transmit currents, n-type semiconductors use electrons to transmit currents, and, to make life easier, we take holes as positive charges. When the two types of semiconductors are put together, at the contact surface, diffusion phenomenon occur.

Some holes traveled into the n-type semiconductor, some electrons traveled into the p-type semiconductor. And after that, an inner electric field formed, which hinders the p-n junction from carrying currents.

To ease the effect above, we need to add some positive voltage at p-type semiconductor, and also add some negative voltage at n-type semiconductor. And if we add negative voltage at n-type semiconductor, positive voltage at p-type semiconductor, the effect will be intensified.

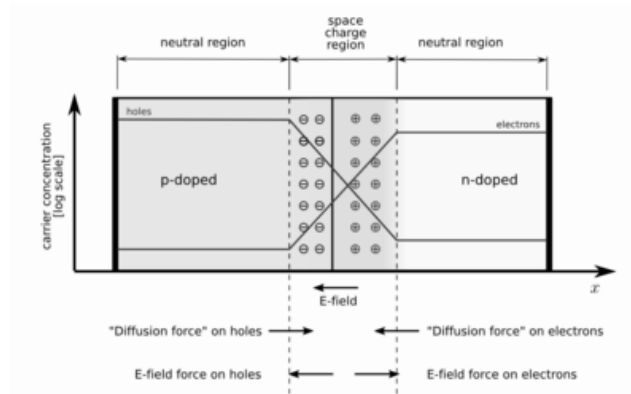


Figure 3.1: PN Junction at equilibrium state

In conclusion, if we add forward voltage (from p to n), the diode acts as a short circuit. If we add reversed voltage, the diode acts like an open circuit. And if the reversed voltage is big enough, it will cause the diode broken-through, and the current flow through it will increase tremendously.

3.2.1 Breakdown of P-N Junction

There are two types of breakdown

- electricity breakdown, which is invertible
- heat breakdown, which cause permanent damage

3.3 Diode modeling

3.3.1 Mathematically idealized diode

Firstly, consider a mathematically idealized diode. In such an ideal diode, if the diode is reverse biased, the current flowing through it is zero. This ideal diode starts conducting at 0 V and for any positive voltage an infinite current flows and the diode acts like a short circuit. The I-V characteristics of an ideal diode are shown below:

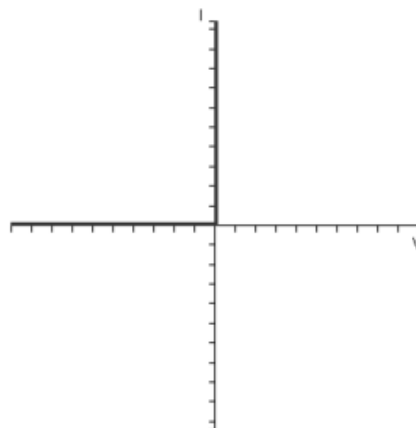
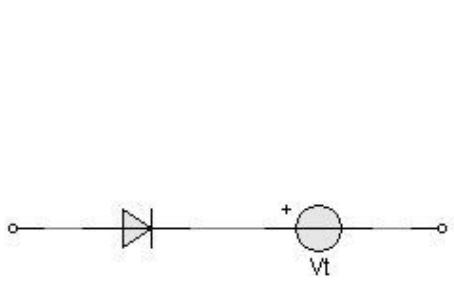


Figure 3.2: I-V characteristic of an ideal diode.

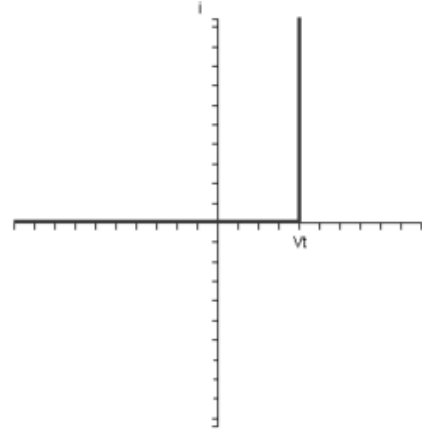
3.3.2 Ideal diode in series with voltage source

Now consider the case when we add a voltage source in series with the diode in the form shown below:

When forward biased, the ideal diode is simply a short circuit and when reverse biased, an open circuit.



(a) Ideal diode with a series voltage source.

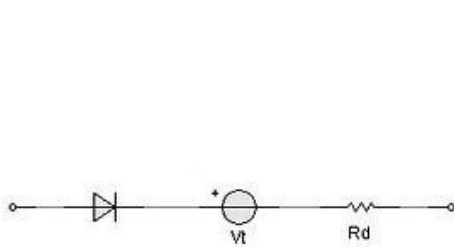


(b) I-V characteristic of an ideal diode with a series voltage source.

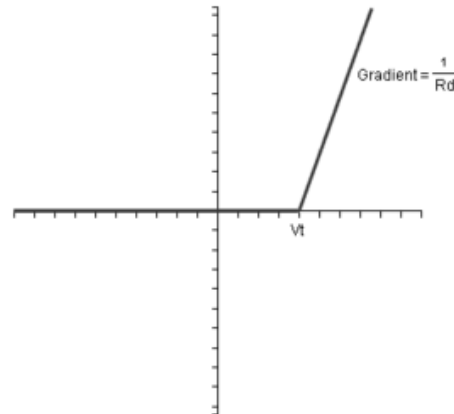
Figure 3.3: Ideal diode in series with voltage source

3.3.3 Diode with voltage source and current-limiting resistor

The last thing needed is a resistor to limit the current, as shown below:



(a) Ideal diode with a series voltage source and resistor.



(b) I-V characteristic of an ideal diode with a series voltage source and resistor.

Figure 3.4: Diode with voltage source and current-limiting resistor

The real diode now can be replaced with the combined ideal diode, voltage source and resistor and the circuit then is modelled using just linear elements. If the sloped-line segment is tangent to the real diode curve at the Q-point, this approximate circuit has the same small-signal circuit at the Q-point as the real diode.

3.3.4 Diode in Small Signal Circuits

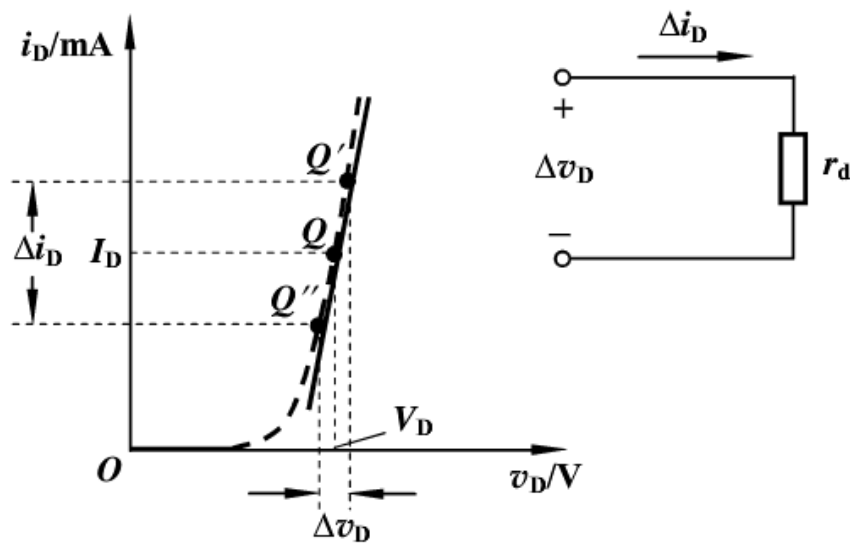


Figure 3.5: I-V Characteristic of Diode in Small Signal

$$r_d = \frac{1}{g_d} = \frac{V_T}{I_{DQ}}$$

Where, in the condition of $T = 300$ K,

$$V_T = 26 \text{ mV}$$

3.4 Applications of Diodes

3.4.1 Rectifier Circuit

The Half-wave Rectifier

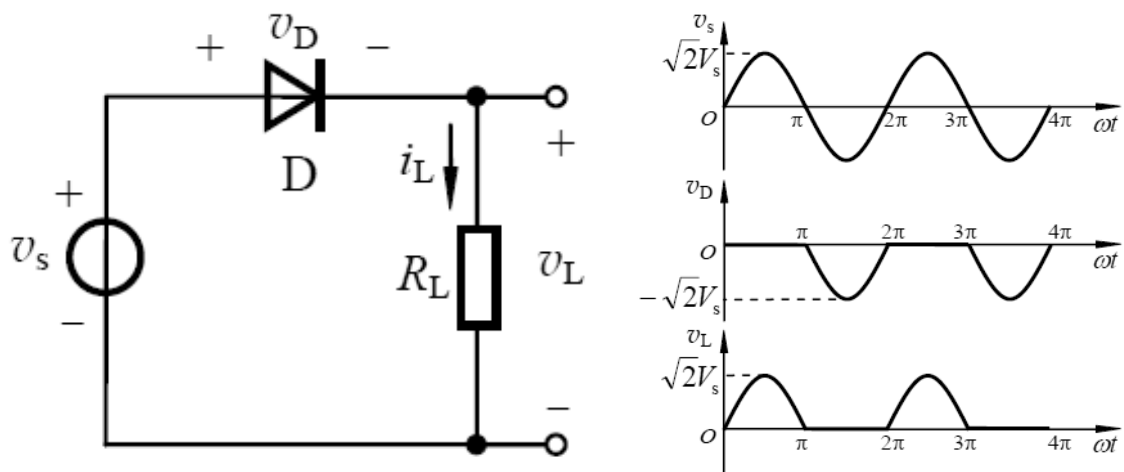


Figure 3.6: Half-wave Rectifier, using the model of ideal diode in series with voltage source

The Bridge Rectifier

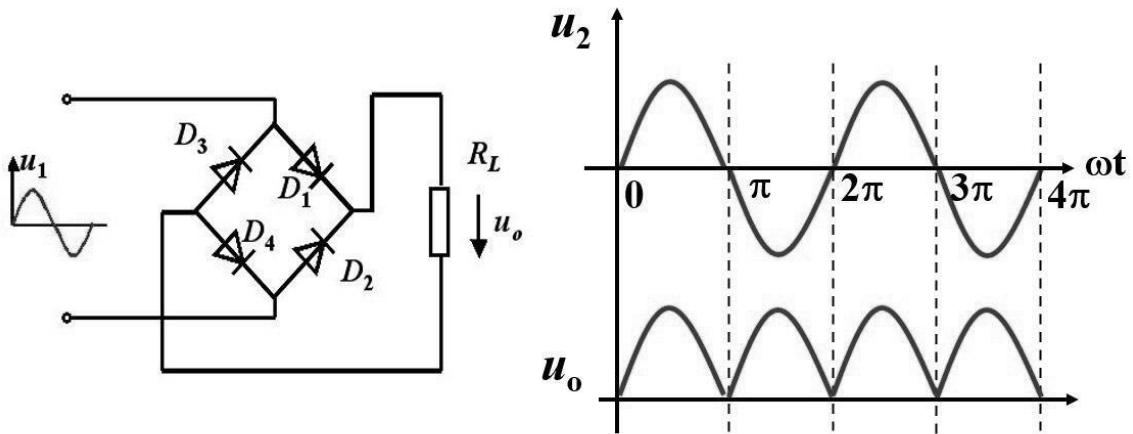


Figure 3.7: A Bridged Rectifier Circuit

$$V_L \approx 0.9V_S$$

$$I_{D1} = I_{D2} = I_{D3} = I_{D4} = \frac{0.45V_S}{R_L}$$

3.4.2 Limiting Circuit

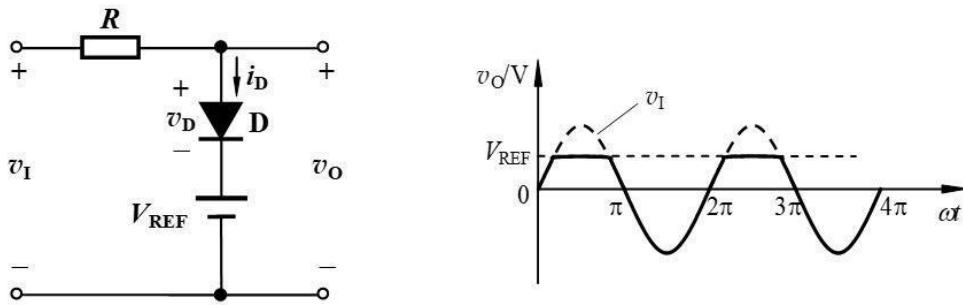
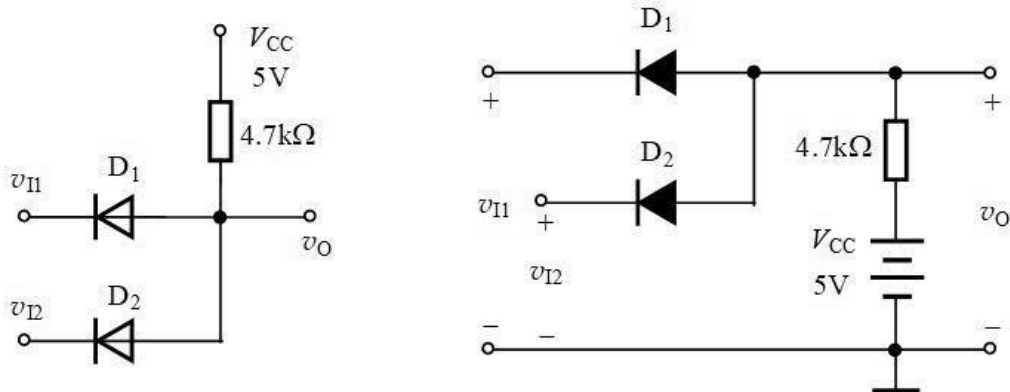


Figure 3.8: A Limiting Circuit

3.4.3 Switching Circuit

Figure 3.9: A Switching Circuit, $v_o = 5\text{ V}$ holds only if all the input voltage is 5 V

3.5 Diodes for Special Usage

3.5.1 Zener diode

A Zener diode is manufactured to be broken-through. It is used to stabilize voltages. As we can know from the I-V characteristic of an diode, when the diode is broken-through, change in current only cause little change in voltage.

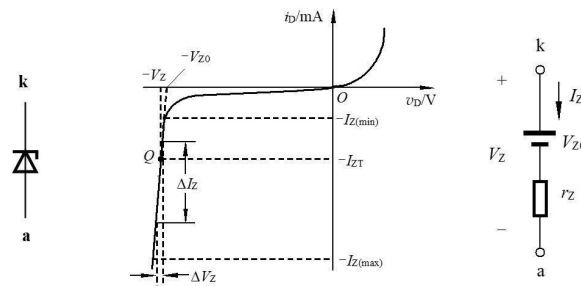


Figure 3.10: Zener Diode's electronic symbol and I-V characteristic

3.5.2 Photodiode

A photodiode is a semiconductor device that converts light into an electrical current. The current is generated when photons are absorbed in the photodiode.

3.5.3 Light-emitting diode

A light-emitting diode (LED) is a semiconductor light source that emits light when current flows through it.

3.5.4 Schottky diode

The Schottky diode (named after the German physicist Walter H. Schottky), also known as Schottky barrier diode or hot-carrier diode, is a semiconductor diode formed by the junction of a semiconductor with a metal. It has a low forward voltage drop and a very fast switching action.

Chapter 4

MOSFET and Amplifying Circuit

4.1 Classification of MOSFET

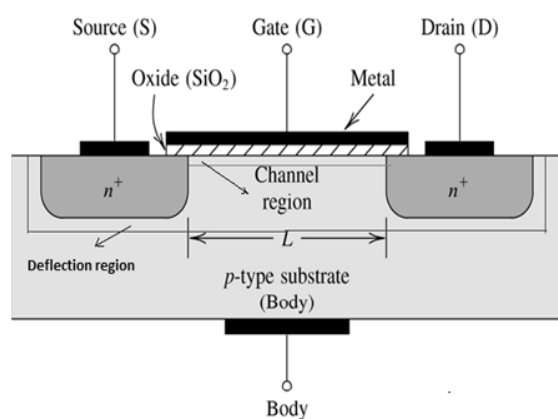
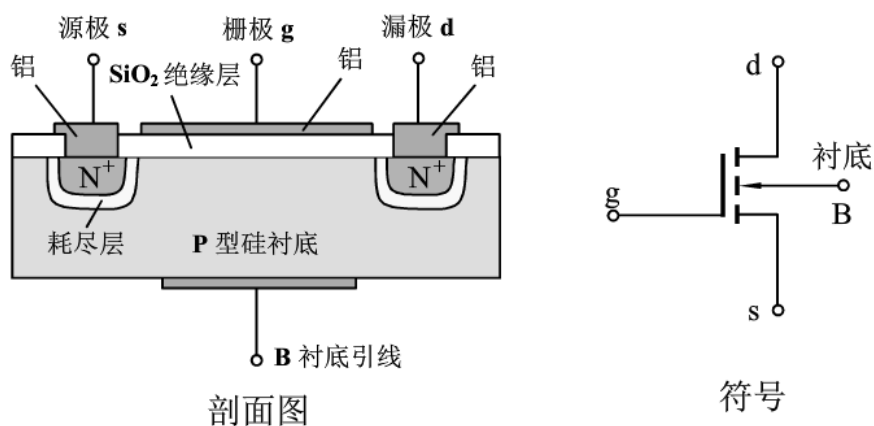


Figure 4.1: MOSFET Block diagram

4.1.1 N-type Enhancement-mode MOSFET



Only when $V_{GS} > V_{TN} > 0$, N-Channel will be formed, MOSFET is conductive. V_{TN} is called the Threshold Voltage.

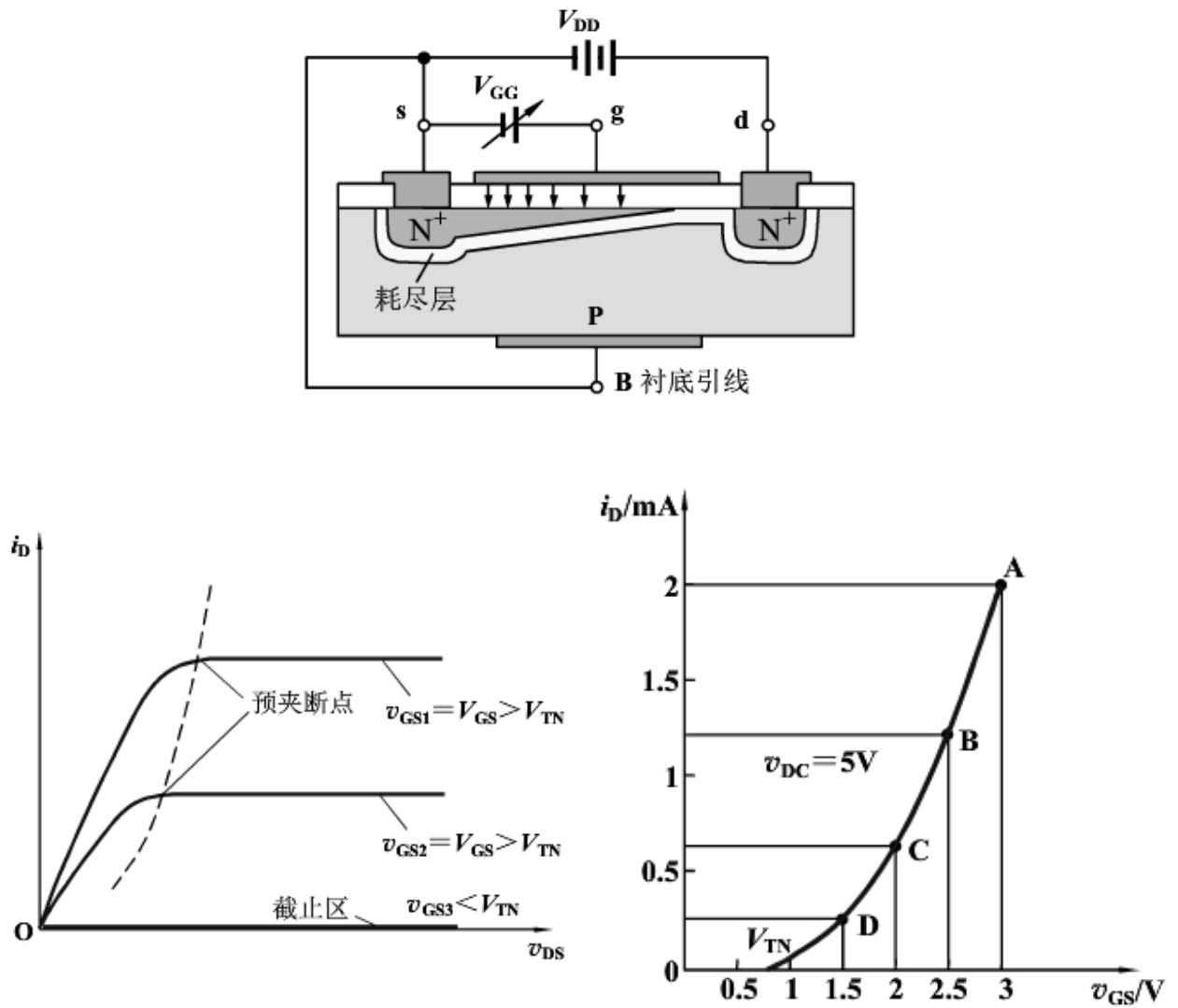
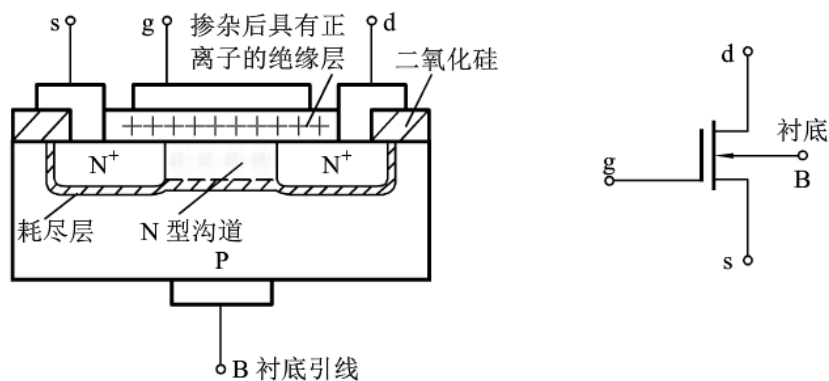


Figure 4.2: The I-V Characteristic of N-type Enhancement-mode MOSFET

4.1.2 N-type Depletion-mode MOSFET

The only difference between Enhancement-mode and Depletion-mode is the charges in oxide, which made $V_{TN} < 0$



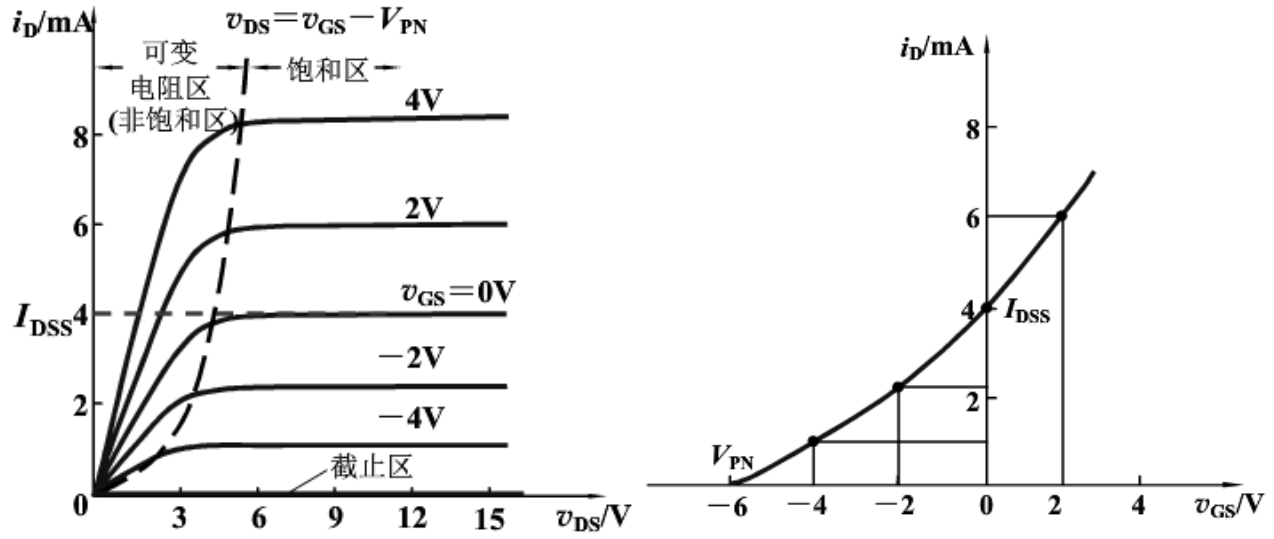


Figure 4.3: The I-V Characteristic of N-type Depletion-mode MOSFET

4.1.3 P-type MOSFET

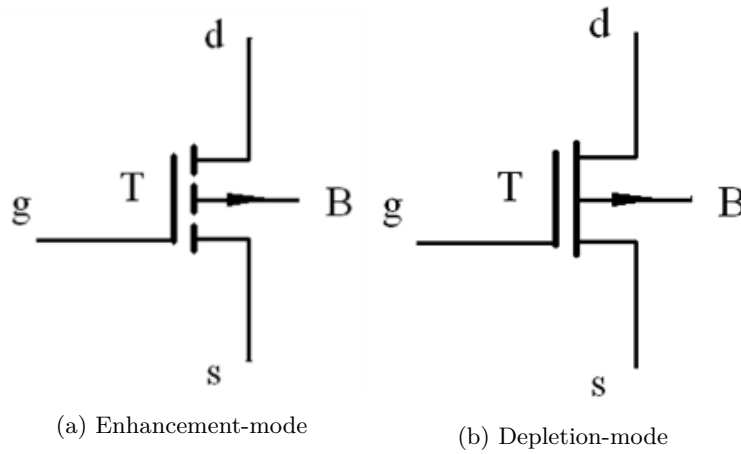


Figure 4.4: The Electronic Symbol of P-type MOSFET

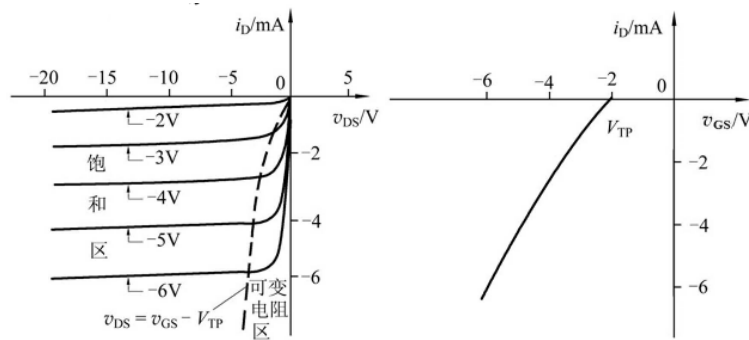
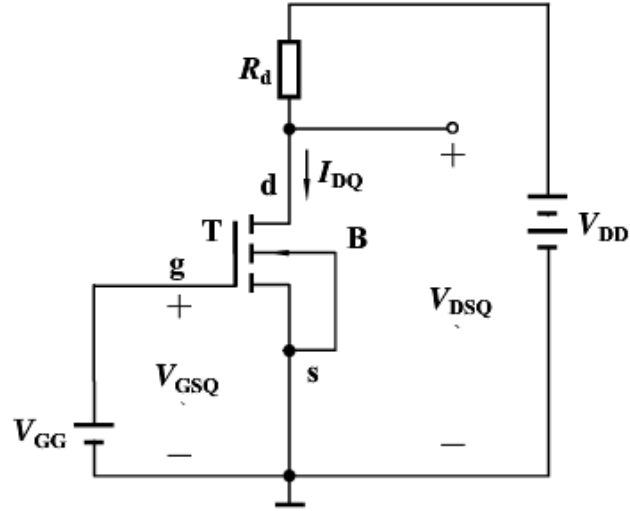


Figure 4.5: The I-V Characteristic of N-type Depletion-mode MOSFET

4.2 Static Working Point



To calculate the static working point of a MOSFET

$$\begin{cases} V_{GSQ} = V_{GG} \\ I_{DQ} = K_n (V_{GSQ} - V_{TN})^2 \\ V_{DSQ} = V_{DD} - I_{DQ} R_d \end{cases}$$

Note that $V_{DSQ} > V_{GSQ} - V_{TN}$ must be verified to ensure that MOSFET is working in active mode. Then for small AC signal, the current at drain is

$$i_D = 2K_n (V_{GSQ} - V_{TN}) v_{GS} = g_m v_{GS}$$

4.3 Early Effect

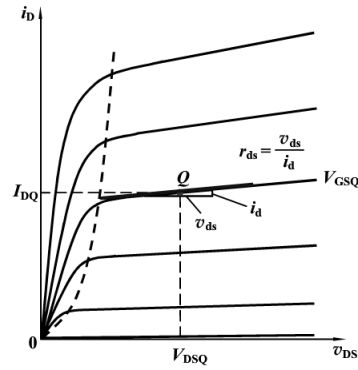


Figure 4.6: MOSFET-Early-Effect

$$i_D = K_n (v_{GS} - V_{TN})^2 (1 + \lambda v_{DS})$$

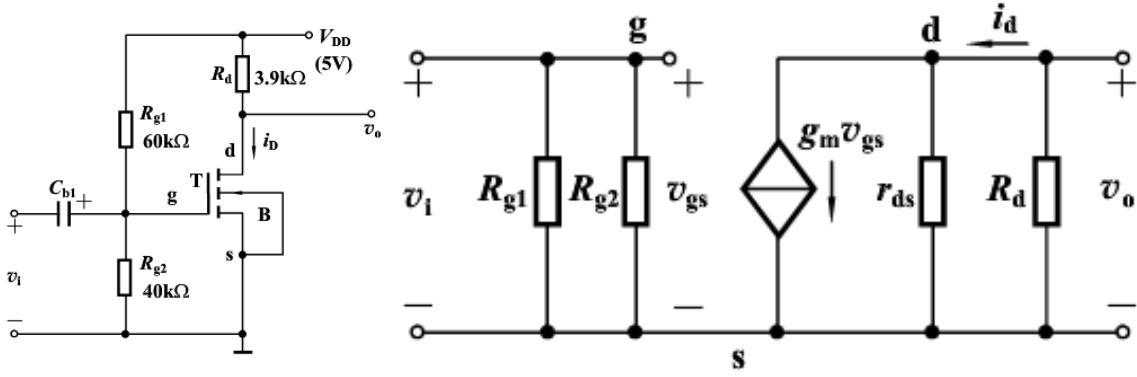
$$r_{ds} = \left. \frac{\partial v_{DS}}{\partial i_D} \right|_{V_{GSQ}} = \frac{1}{\lambda K_n (V_{GSQ} - V_{TN})^2} \approx \frac{1}{\lambda I_{DQ}} = \frac{V_A}{I_{DQ}}$$

Where V_A is called the Early Voltage

$$V_A = \frac{1}{\lambda}$$

4.4 Three types of Amplifier Circuit

4.4.1 Common Source Amplifier Circuit



$$\begin{cases} V_{GSQ} = \left(\frac{R_{g2}}{R_{g1} + R_{g2}} \right) V_{DD} \\ I_{DQ} = K_n (V_{GSQ} - V_{TN})^2 \\ V_{DSQ} = V_{DD} - I_{DQ} R_d \end{cases}$$

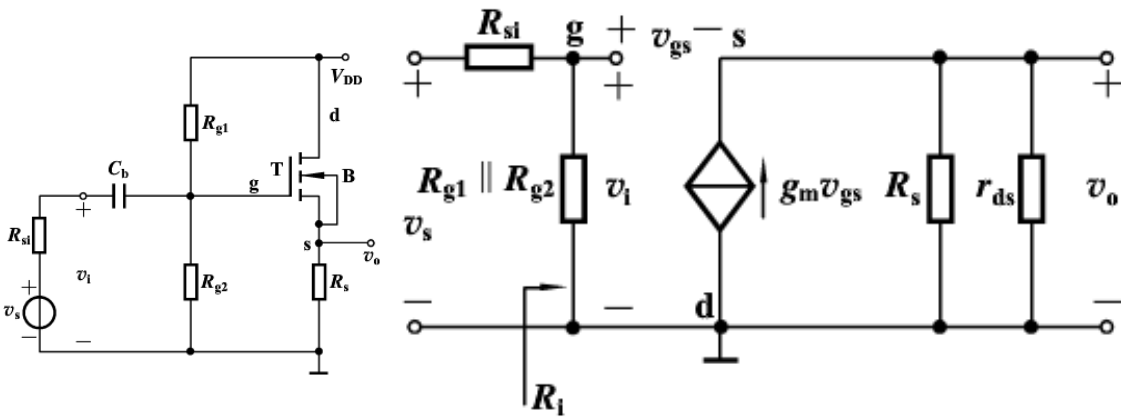
$$g_m = 2K_n (V_{GSQ} - V_{TN})$$

$$A_v = -g_m (r_{ds} \parallel R_d)$$

$$R_i = R_{g1} \parallel R_{g2}$$

$$R_o = R_d \parallel R_d \approx R_d$$

4.4.2 Common Drain Amplifier Circuit



$$\begin{cases} V_{GSQ} = \frac{R_{g2}}{R_{g1} + R_{g2}} \cdot V_{DD} - I_{DQ} R_s \\ I_{DQ} = K_n (V_{GSQ} - V_{TN})^2 \\ V_{DSQ} = V_{DD} - I_{DQ} R_s \end{cases}$$

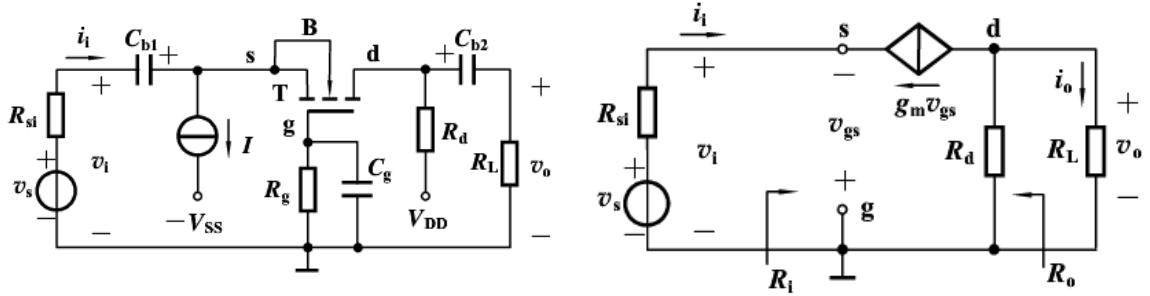
$$g_m = 2K_n (V_{GSQ} - V_{TN})$$

$$A_v = \frac{g_m (R_s \parallel r_{ds})}{1 + g_m (R_s \parallel r_{ds})} \approx 1$$

$$R_i = R_{g1} \parallel R_{g2}$$

$$R_o = R_s \parallel r_{ds} \parallel \frac{1}{g_m}$$

4.4.3 Common Gate Amplifier Circuit



$$\begin{cases} I_{DQ} = K_n (V_{GSQ} - V_{TN})^2 = I \\ V_{DSQ} = V_{DD} - I_{DQ} R_d + V_{GSQ} \end{cases}$$

$$g_m = 2K_n (V_{GSQ} - V_{TN})$$

$$A_v = g_m (R_d \parallel R_L)$$

$$R_i \approx \frac{1}{g_m}$$

$$R_o \approx R_d$$

4.5 Summary

4.5.1 Features of Three Types of MOSFET Amplifying Circuits

Type	A_v	A_i	R_i	R_o
Common Source	high, inverse	-	high	-
Common Drain	1	-	high	low
Common Gate	high, non-inverse	1	low	-

4.5.2 Small Signal Model of MOSFET

$$I_{DQ} = K_n (V_{GSQ} - V_{TN})^2 \quad g_m = 2K_n (V_{GSQ} - V_{TN}) \quad r_{ds} = \frac{1}{\lambda K_n (V_{GSQ} - V_{TN})^2}$$

4.5.3 Saturation Mode of MOSFET

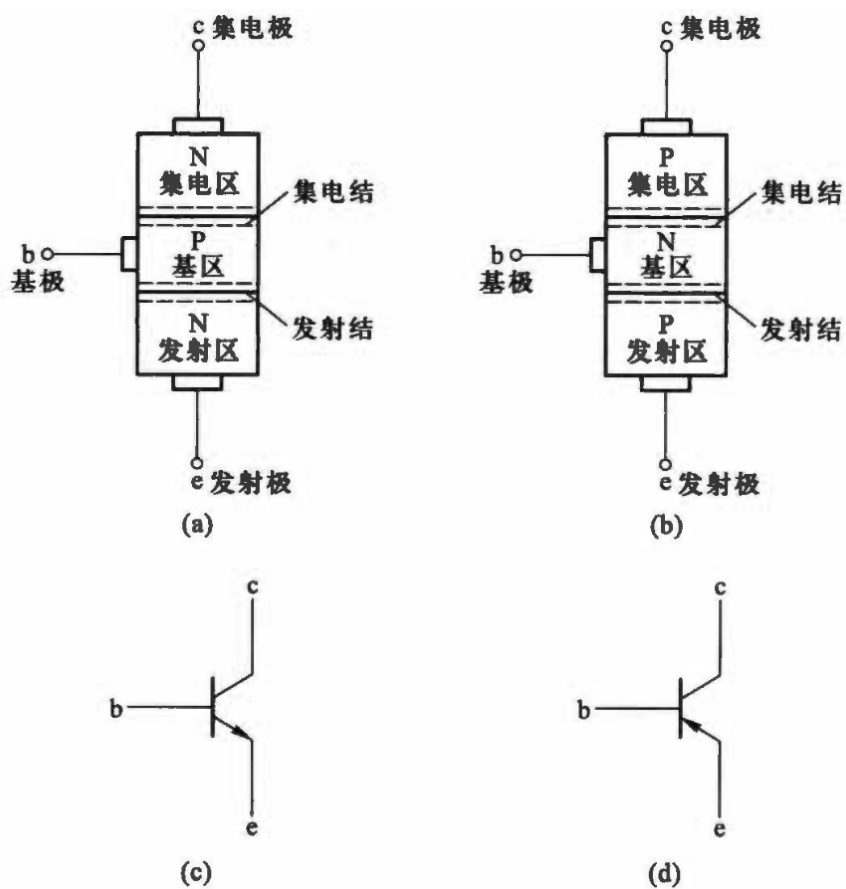
MOSFET Type	V_{GS}	V_{DS}
N-type enhancement	$V_{GS} > V_{TN}$	$V_{DS} > V_{GS} - V_{TN}$
N-type depletion	$V_{GS} > V_{PN}$	$V_{DS} > V_{GS} - V_{PN}$
P-type enhancement	$V_{GS} < V_{TP}$	$V_{DS} < V_{GS} - V_{TP}$
P-type depletion	$V_{GS} < V_{PP}$	$V_{DS} < V_{GS} - V_{PP}$

Chapter 5

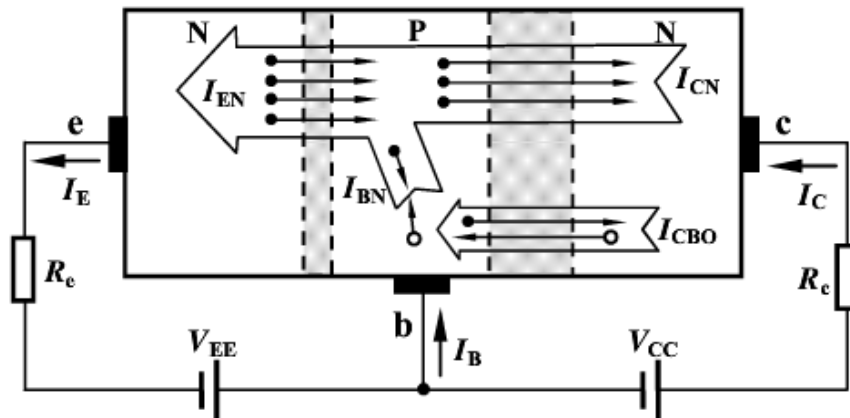
Bipolar Junction Transistor

5.1 Electronic Symbol

The arrow represents the direction of current.



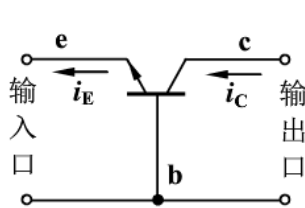
5.2 Control Principle



$$\alpha = \frac{I_c}{I_e} \quad \beta = \frac{I_c}{I_b} \quad \alpha = \frac{\beta}{1 + \beta} \quad I_e = (1 + \beta) I_b$$

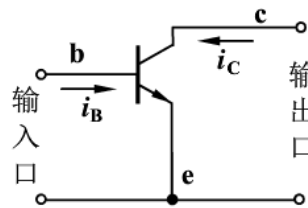
$$I_E = I_{ES} \exp(V_{BE}/V_T) \quad V_T = 26 \text{ mV}$$

5.3 Three Types of Amplifier Circuit



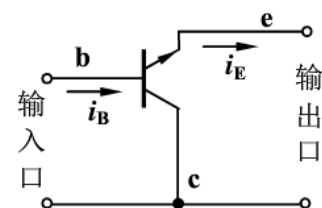
(a) 共基极

$$i_C = \alpha i_E$$



(b) 共发射极

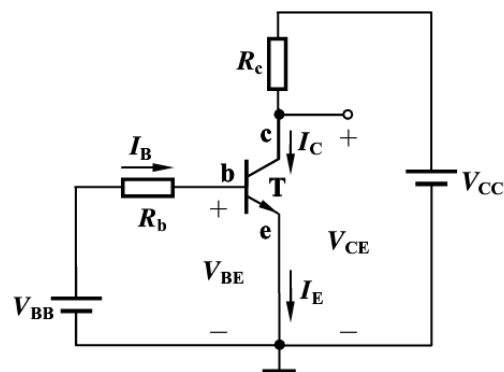
$$i_C = \beta i_B$$



(c) 共集电极

$$i_E = (1 + \beta) i_B$$

5.4 Static Working Point



$$I_{BQ} = \frac{V_{BB} - V_{BEQ}}{R_b}$$

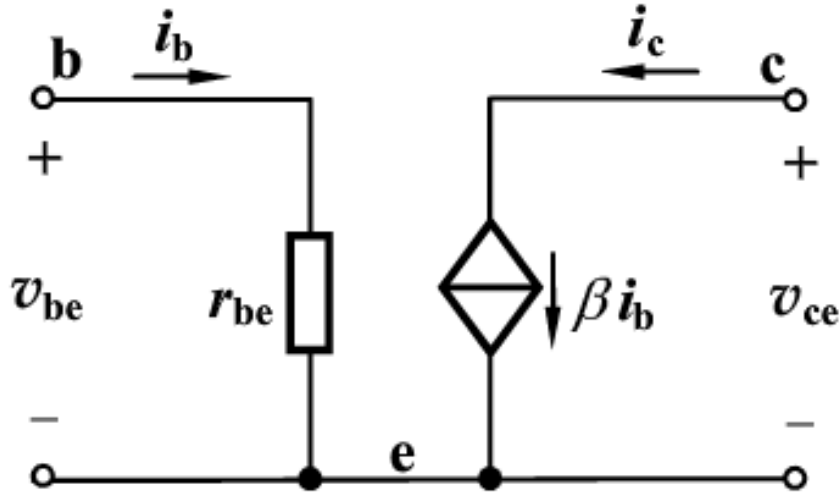
$$V_{BEQ} = 0.7 \text{ V}$$

$$I_{CQ} = \beta I_{BQ}$$

$$V_{CEQ} = V_{CC} - I_{CQ} R_C$$

Note that the static working point is not associated with small signals discussed below.

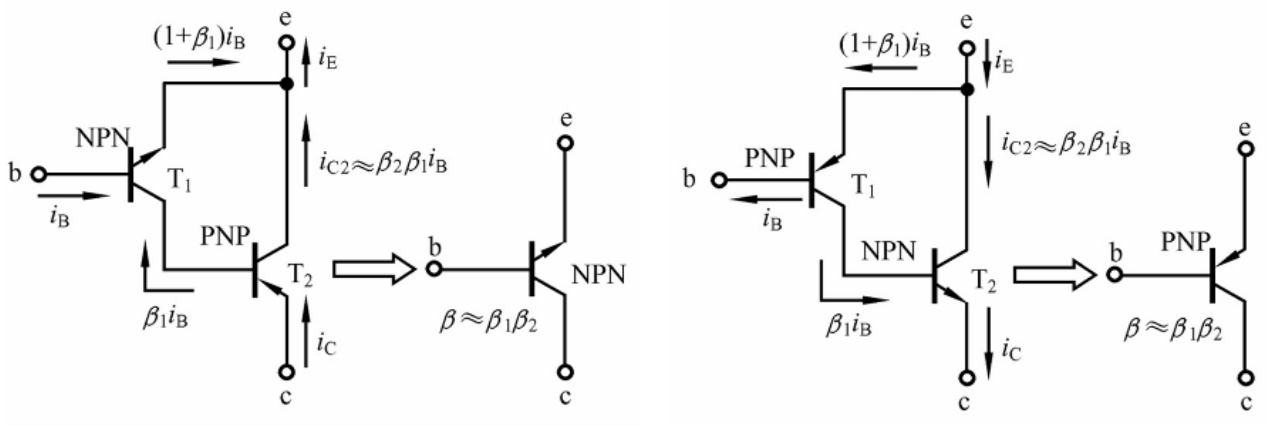
5.5 Model of Small Signal



When $T = 300 \text{ K}$

$$r_{be} = 200 \, \Omega + (1 + \beta) \frac{26 \text{ mV}}{I_{EQ} (\text{mA})}$$

5.6 Compound Transistor

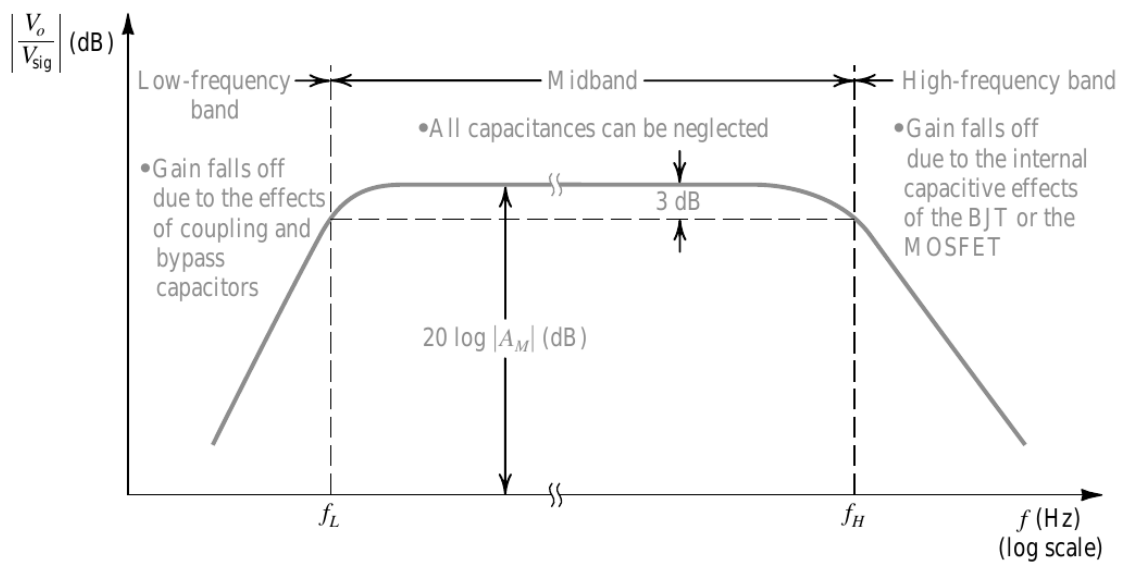


Noted that the emitter and base of compound transistor is exactly the same as the first transistor's.

Chapter 6

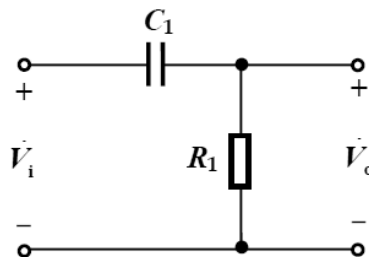
Frequency Response

6.1 Sketch of Gain vs Frequency of All Circuits



6.2 R-C Series Circuit

6.2.1 Low-Frequency Response of R-C High Pass Filter



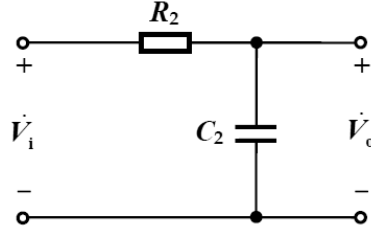
$$f_L = \frac{1}{2\pi R_1 C_1} \quad |A_{vL}| = \frac{1}{\sqrt{1 + (f_L/f)^2}} \quad \varphi_L = \arctan(f_L/f)$$

6.2.2 Mid-Frequency

In mid-frequency, capacitors are equal to open circuit while only resistors remain in the circuit. Where

$$A_{vM} = 1 \quad \varphi = 0$$

6.2.3 High-Frequency Response of R-C Low Pass Filter

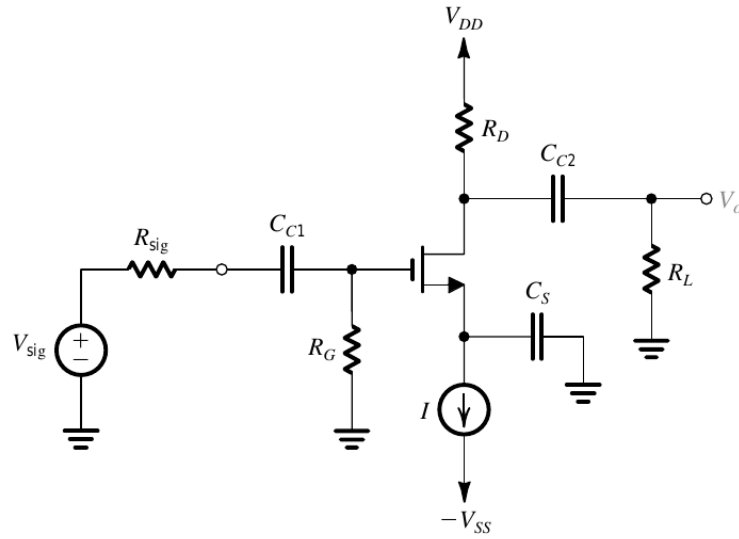


$$f_H = \frac{1}{2\pi R_1 C_1} \quad |A_{vH}| = \frac{1}{\sqrt{1 + (f_H/f)^2}} \quad \varphi_H = -\arctan(f_H/f)$$

6.3 Frequency Response of Amplifiers

6.3.1 Low-Frequency

Common-Source Amplifier



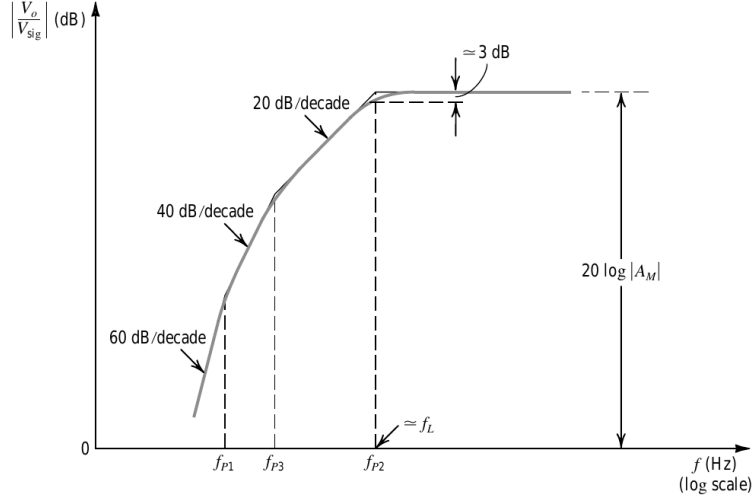
$$A_M = -\frac{R_G}{R_G + R_{sig}} [g_m (R_D \parallel R_L)]$$

$$\omega_{p1} = \frac{1}{C_{C1} (R_G + R_{sig})}$$

$$\omega_{p2} = \frac{g_m}{C_S}$$

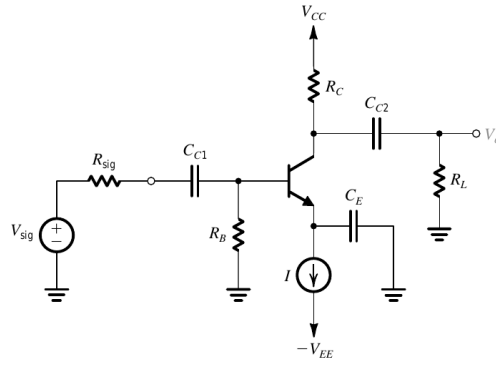
$$\omega_{p3} = \frac{1}{C_{C2} (R_D + R_L)}$$

$$\frac{V_o}{V_{sig}} = A_M \left(\frac{s}{s + \omega_{p1}} \right) \left(\frac{s}{s + \omega_{p2}} \right) \left(\frac{s}{s + \omega_{p3}} \right)$$



$$f_L \approx f_{p2} = \frac{\omega_{p2}}{2\pi} = \frac{1}{2\pi(C_S/g_m)}$$

Common-Emitter Amplifier

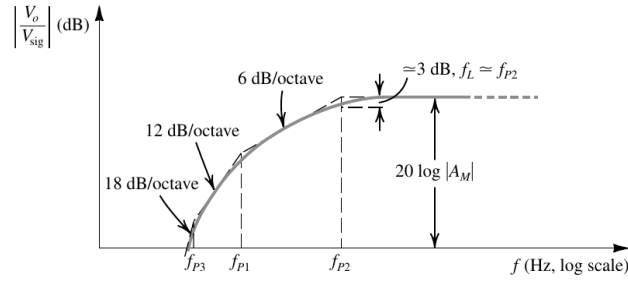


$$A_M = -\frac{(R_B \parallel r_\pi)}{(R_B \parallel r_\pi) R_{sig}} g_m (R_C \parallel R_L)$$

$$\omega_{p1} = \frac{1}{C_{C1} [(R_B \parallel r_\pi) + R_{sig}]}$$

$$\omega_{p2} = \frac{1}{C_E \left[r_e + \frac{R_B \parallel R_{sig}}{\beta + 1} \right]}$$

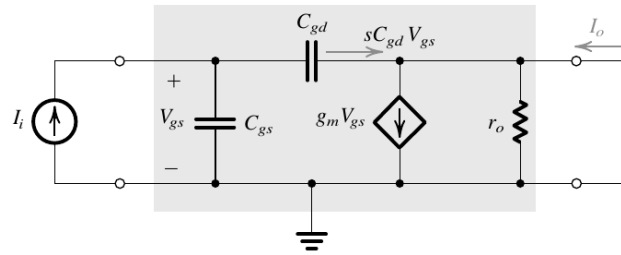
$$\omega_{p3} = \frac{1}{C_{C2} (R_C + R_L)}$$



$$f_L \approx f_{p2}$$

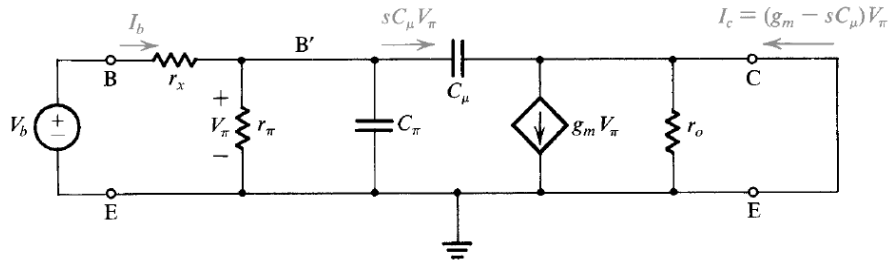
6.3.2 High-Frequency

Unity-Gain Frequency



MOSFET

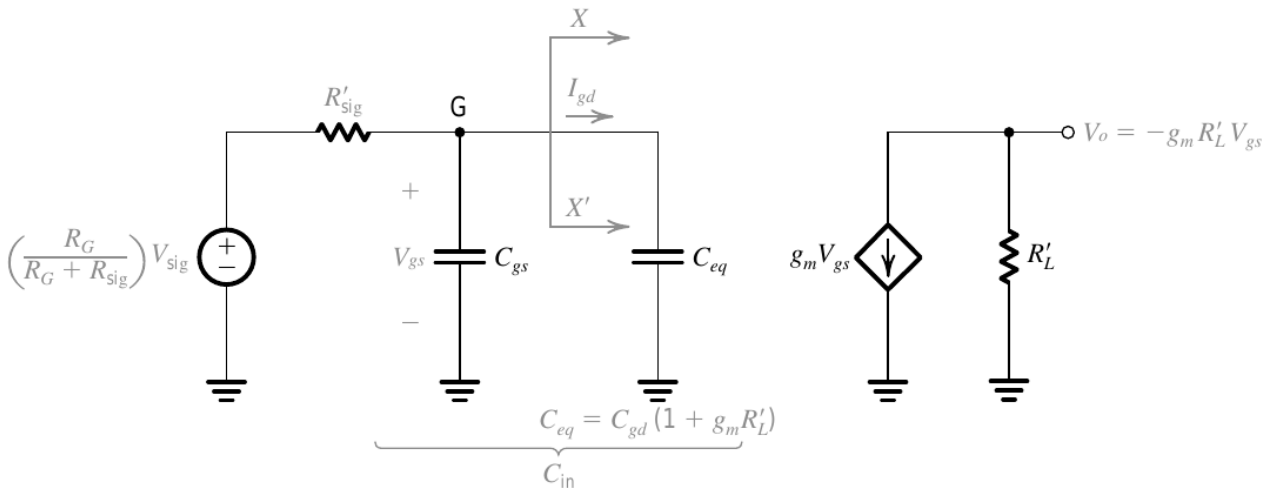
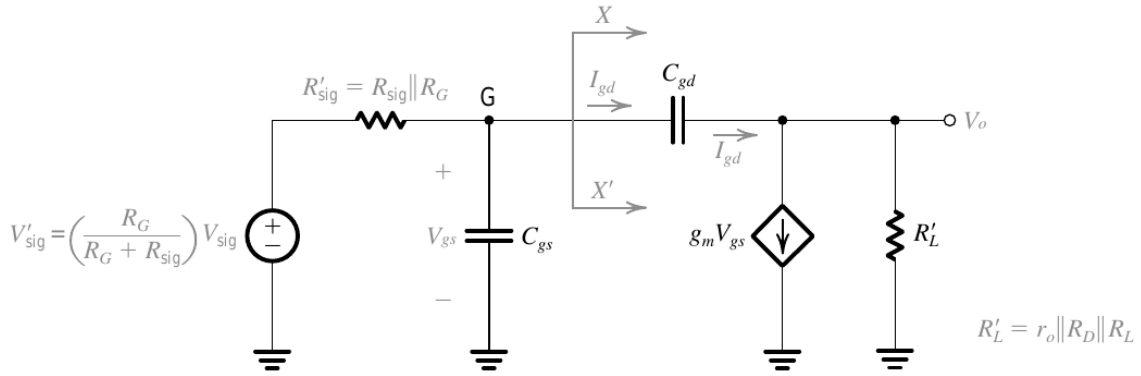
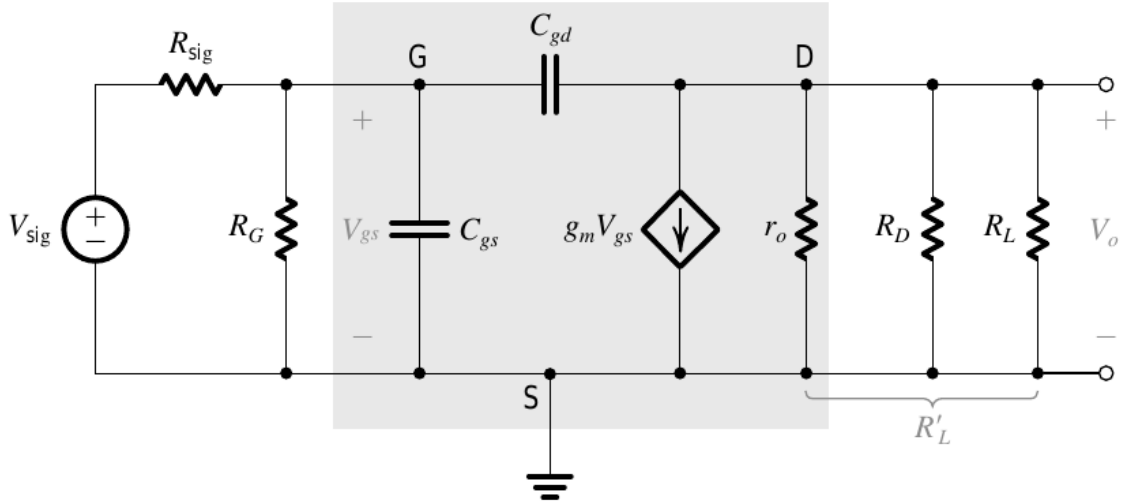
$$\frac{I_o}{I_i} = \frac{g_m}{s(C_{gs} + C_{gd})} \quad \omega_T = \frac{g_m}{(C_{gs} + C_{gd})} \quad f_T = \frac{g_m}{2\pi(C_{gs} + C_{gd})}$$



BJT

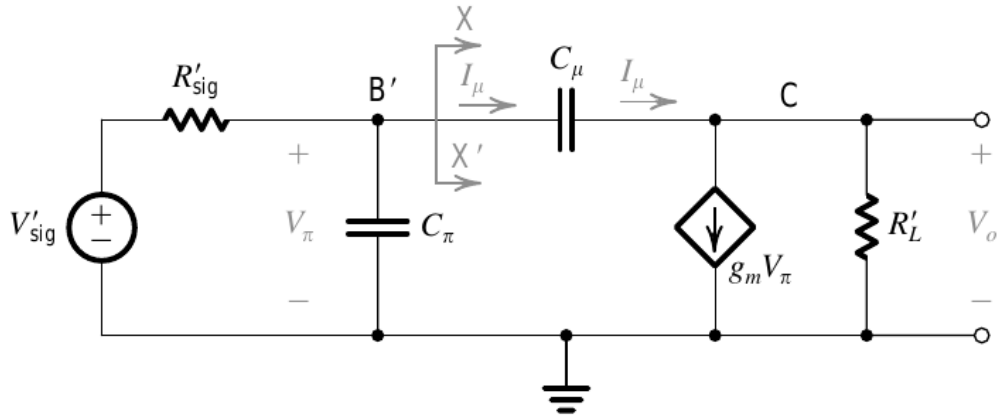
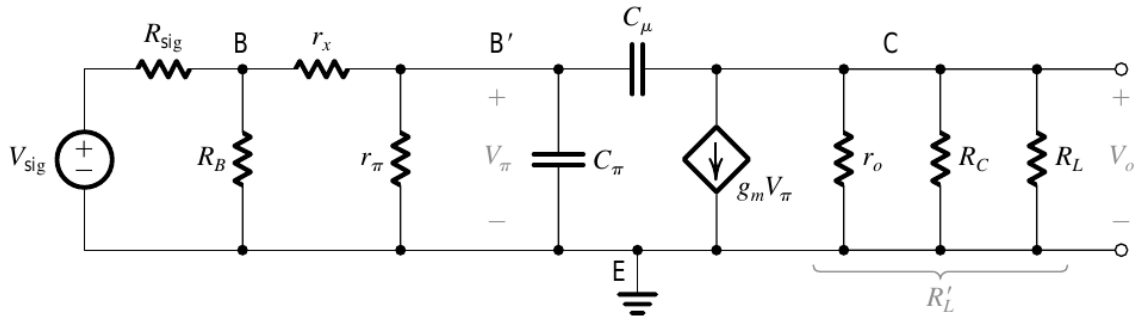
$$\frac{I_c}{I_b} = \frac{g_m r_\pi}{1 + s(C_\pi + C_\mu) r_\pi} = \frac{\beta_0}{1 + s(C_\pi + C_\mu) r_\pi} \quad \omega_\beta = \frac{1}{(C_\pi + C_\mu) r_\pi} \quad \omega_T = \beta_0 \omega_\beta = \frac{g_m}{C_\pi + C_\mu}$$

Common-Source Amplifier



$$\omega_H = \frac{1}{C_{in} R'_{sig}} \quad A_M = -\frac{R_G}{R_G + R_{sig}} g_m R'_L \quad f_H = \frac{\omega_H}{2\pi} = \frac{1}{2\pi C_{in} R'_{sig}}$$

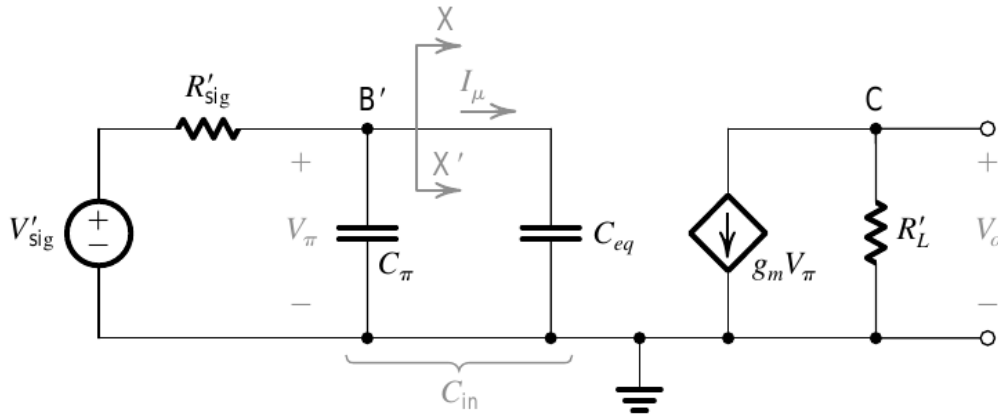
Common-Emitter Amplifier



$$V'_{\text{sig}} = V_{\text{sig}} \frac{R_B}{R_B + R_{\text{sig}}} \frac{r_{\pi}}{r_{\pi} + r_x + (R_{\text{sig}} \parallel R_B)}$$

$$R'_L = r_o \parallel R_C \parallel R_L$$

$$R'_{\text{sig}} = r_{\pi} \parallel [r_x + (R_B \parallel R_{\text{sig}})]$$



$$C_{\text{in}} = C_{\pi} + C_{eq}$$

$$= C_{\pi} + C_{\mu}(1 + g_m R'_L)$$

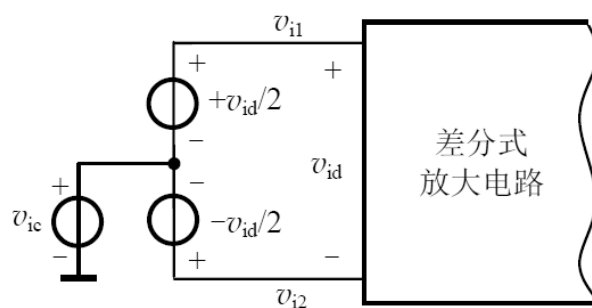
$$V_o = -g_m R'_L V_{\pi}$$

$$A_M = -\frac{R_B}{R_B + R_{\text{sig}}} \frac{r_{\pi}}{r_{\pi} + r_x + (R_{\text{sig}} \parallel R_B)} (g_m R'_L) \quad f_H = \frac{\omega_H}{2\pi} = \frac{1}{2\pi C_{\text{in}} R'_{\text{sig}}}$$

Chapter 7

Analogue Integrated Circuits

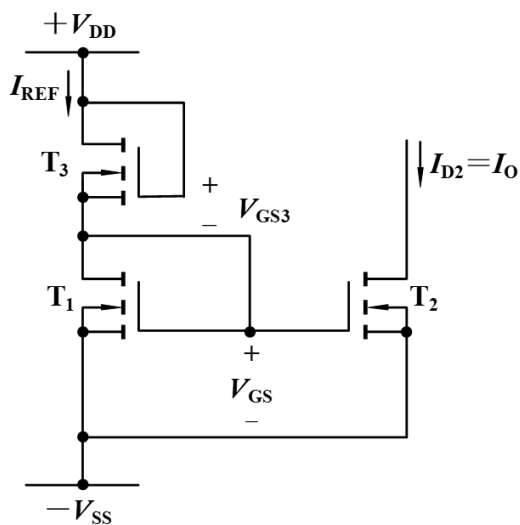
7.1 Differential Amplifier Circuit



$$v_{i1} = v_{ic} + \frac{v_{id}}{2} \quad v_{i2} = v_{ic} - \frac{v_{id}}{2}$$

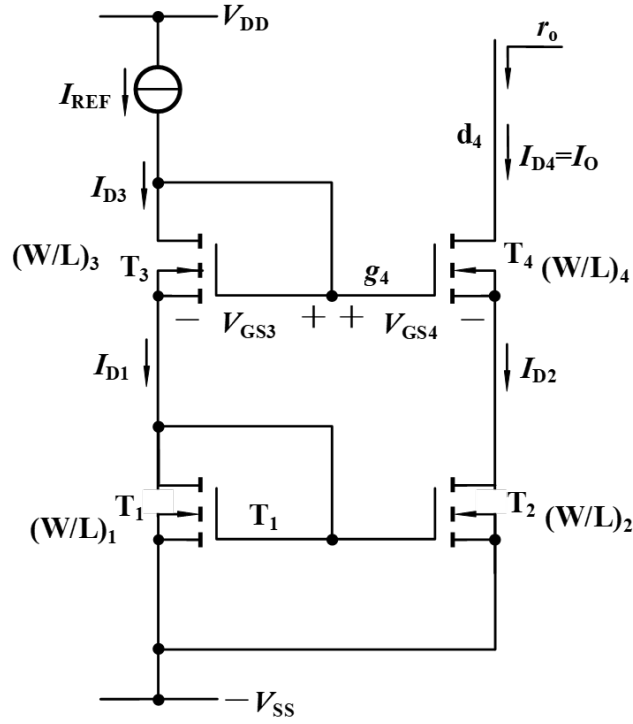
7.2 MOSFET Current Source

7.2.1 MOSFET Current Mirror



$$I_o = I_{D2} = K_n (V_{GS} - V_{TN})^2$$

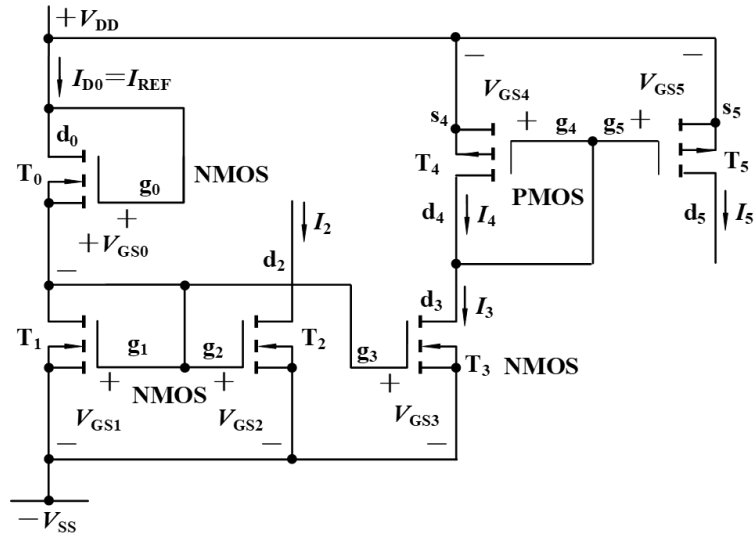
7.2.2 Cascade Current Mirror



The larger output resistance, the more stability of output current.

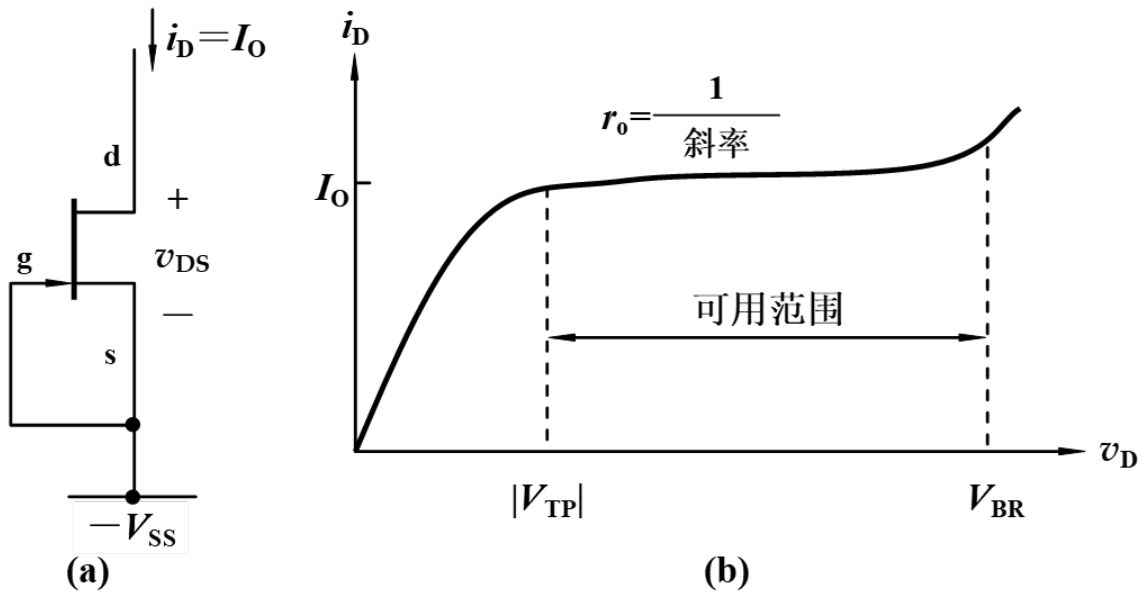
$$r_o = r_{ds4} + r_{ds2} (1 + g_m r_{ds4}) \approx g_m r_{ds4} r_{ds2}$$

7.2.3 Combined Current Mirror



$$I_2 = \frac{(W/L)_2}{(W/L)_1} I_{REF} \quad I_3 = \frac{(W/L)_3}{(W/L)_1} I_{REF} \quad I_4 = I_3 \quad I_5 = \frac{(W/L)_5}{(W/L)_4} I_4$$

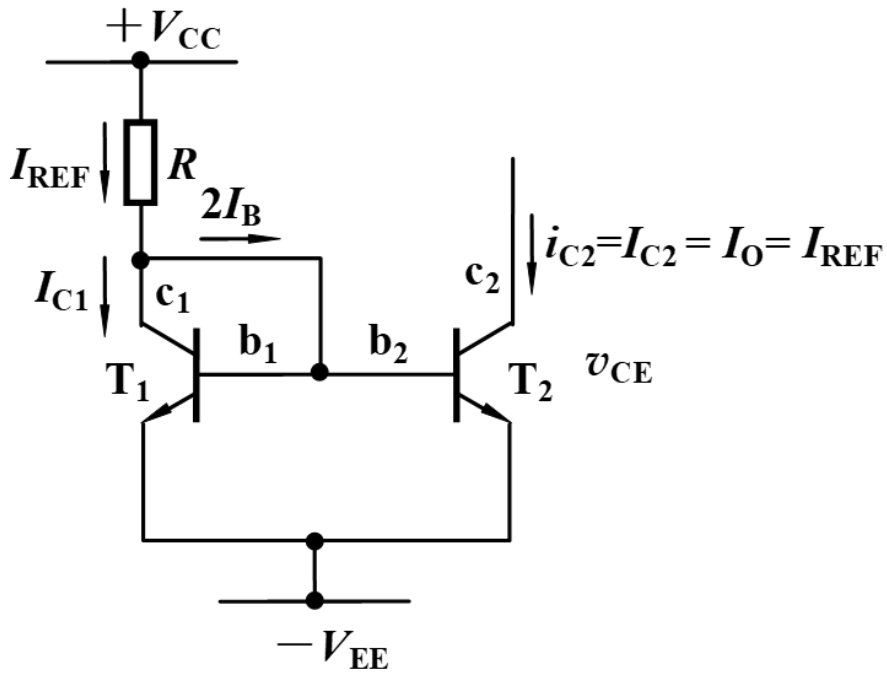
7.2.4 JFET Current Mirror



$$i_D = I_o = I_{DSS} (1 + \lambda v_{DS}) \quad r_o = \frac{1}{\lambda I_{DSS}}$$

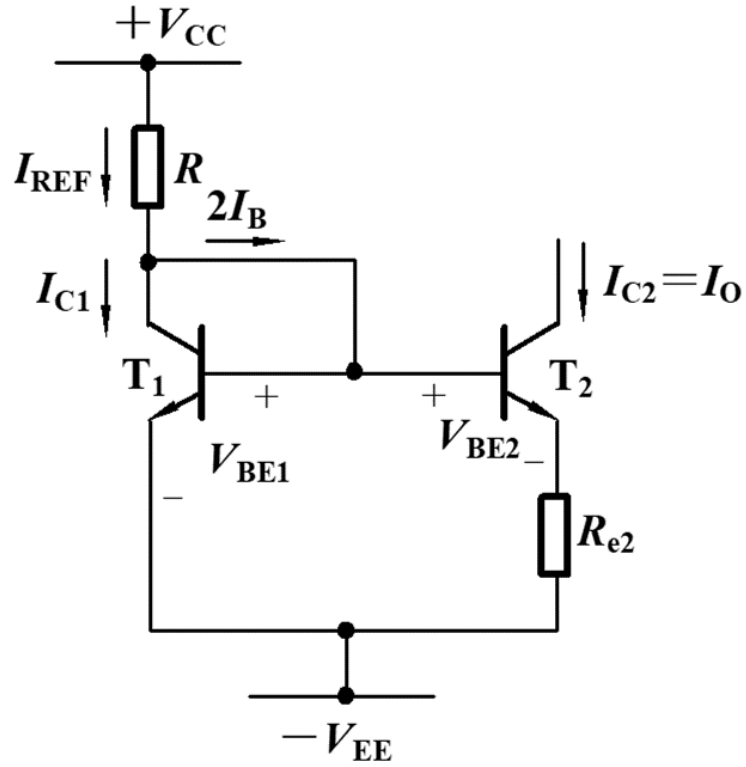
7.3 BJT Current Source

7.3.1 BJT Current Mirror



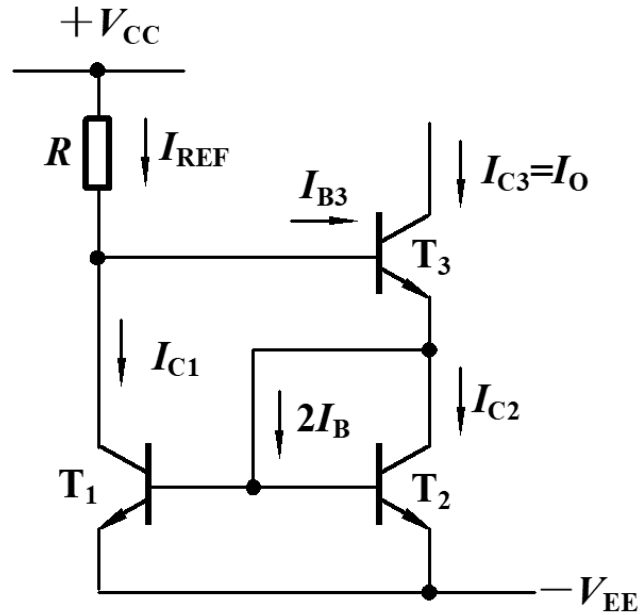
$$I_{C2} = I_{C1} \approx I_{REF} \approx \frac{V_{CC}}{R} \quad r_o = r_{ce}$$

7.3.2 Micro Current Source



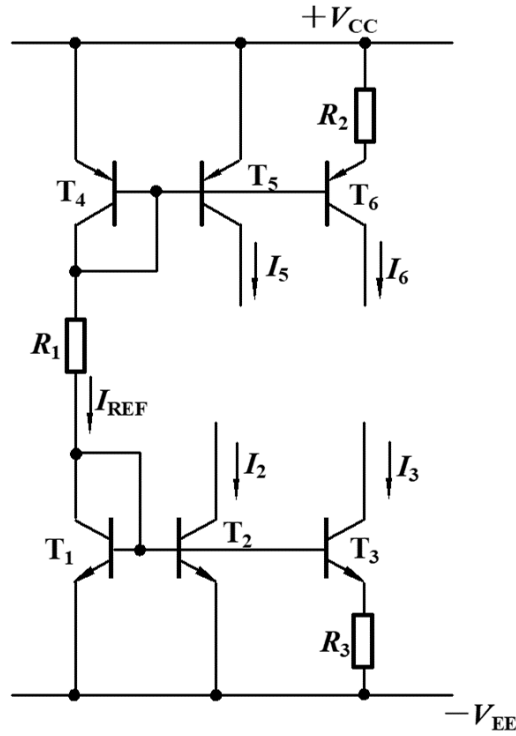
$$I_o = \frac{\Delta V_{BE}}{R_{e2}} \quad r_o \approx r_{ce2} + \left(1 + \frac{\beta R_{e2}}{r_{be2} + R_{e2}}\right)$$

7.3.3 Current Source with High Output Resistance



$$I_{REF} = \frac{V_{CC} - V_{BE3} - V_{BE2} + V_{EE}}{R} \quad I_o \approx I_{C2} = \frac{A_3}{A_1} \cdot I_{REF}$$

7.3.4 Combined Current Source

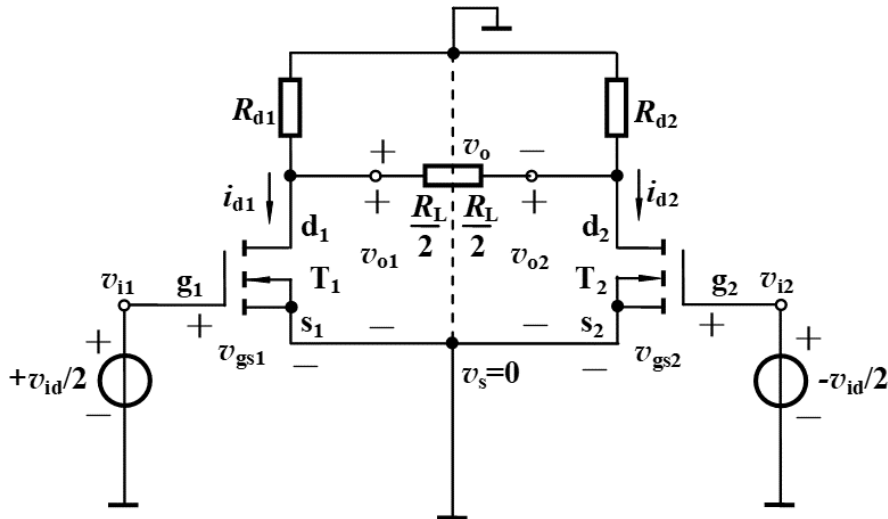


$$I_{REF} = \frac{V_{CC} + V_{EE} - V_{BE1} + V_{EB4}}{R_1}$$

7.4 Differential Amplifier

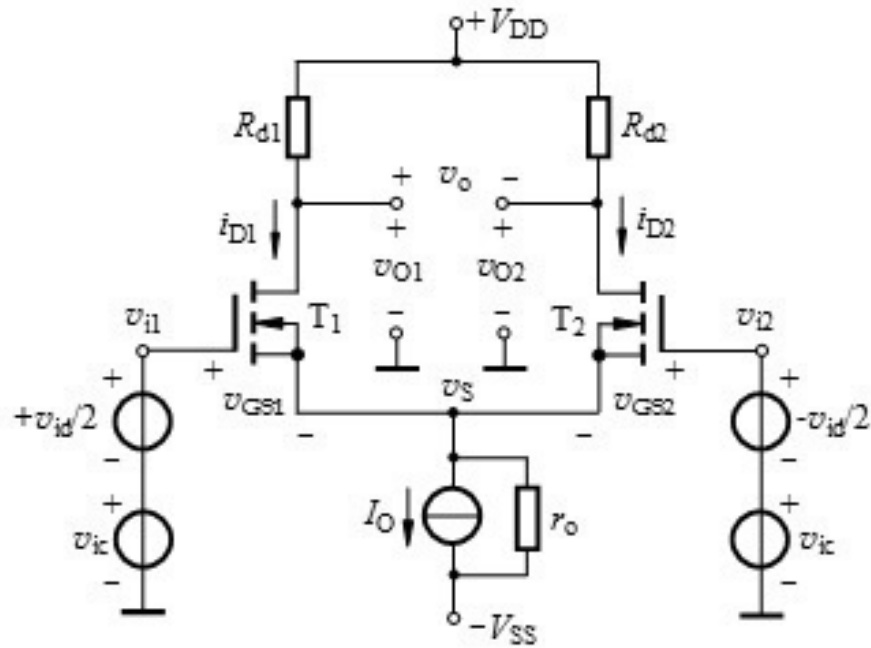
7.4.1 MOSFET

Two output terminals



$$i_{d1} = i_{d2} = I_D = \frac{1}{2}I_o \quad g_m = \sqrt{2K_n I_o} \quad A_{vd} = -g_m \left(r_{ds} \parallel R_d \parallel \frac{R_L}{2} \right) \quad K_{CMR} = \infty \quad R_o = 2R_d$$

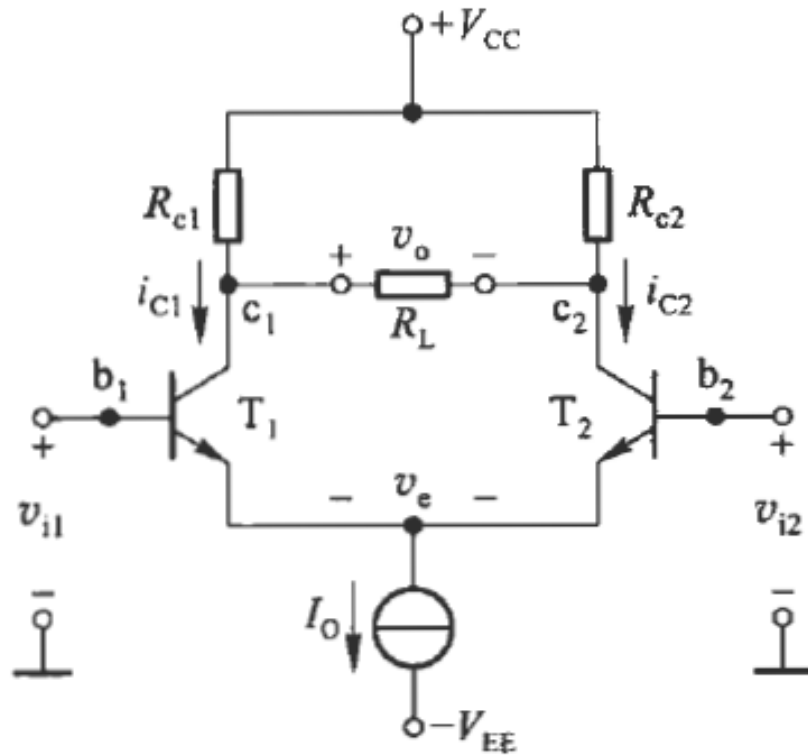
One output terminal



$$g_m = \sqrt{2K_n I_o} \quad A_{vd1} = -A_{vd2} = -\frac{g_m (r_{ds} \parallel R_d \parallel R_L)}{2} \quad K_{CMR} \approx g_m r_o \quad R_o = R_d$$

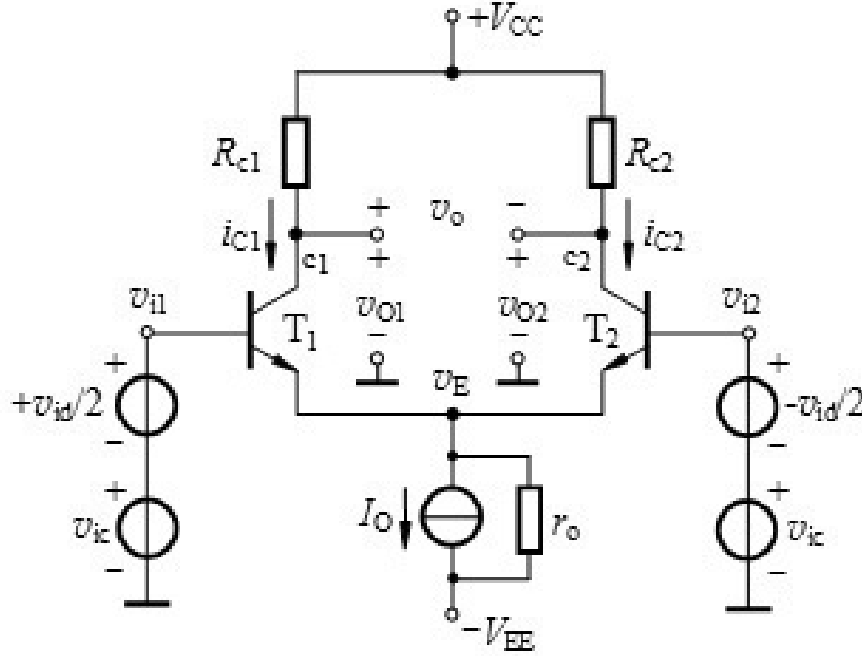
7.4.2 BJT

Two output terminals



$$A_{vd} = -\frac{\beta \left(R_c \parallel \frac{R_L}{2} \right)}{r_{be}} \quad K_{CMR} = \infty \quad R_{id} = 2r_{be} \quad R_{ic} = \frac{1}{2} [r_{be} + (1 + \beta) 2r_o] \quad R_o = 2R_c$$

One Output Terminal

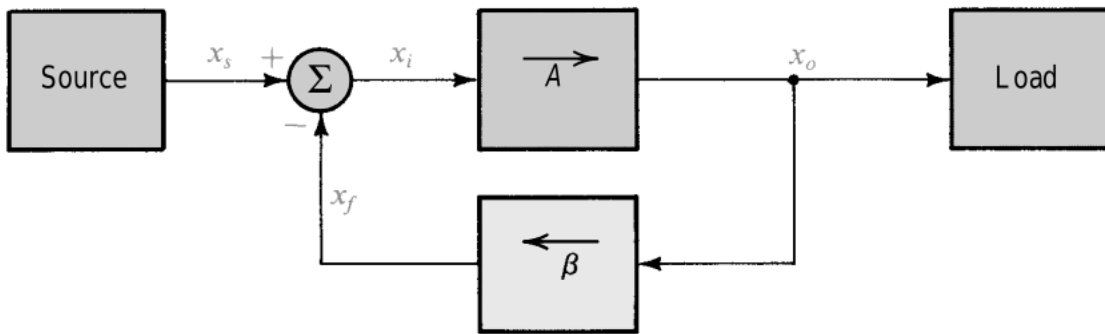


$$A_{vd1} = -A_{vd2} = -\frac{\beta (R_c \parallel R_L)}{2r_{be}} \quad K_{CMR} \approx \frac{\beta r_o}{r_{be}} \quad R_{id} = 2r_{be} \quad R_{ic} = \frac{1}{2} [r_{be} + (1 + \beta) 2r_o] \quad R_o = R_c$$

Chapter 8

Feedback

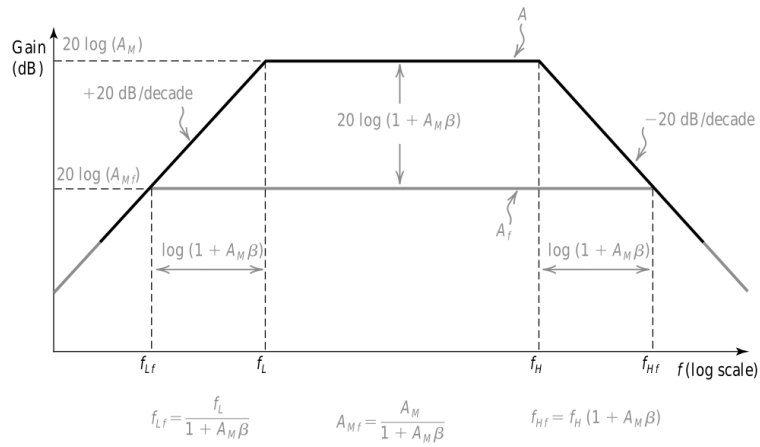
8.1 Basic Feedback Structure



$$x_o = Ax_i \quad x_f = \beta x_o \quad x_i = x_s - x_f$$

$$A_f = \frac{x_o}{x_s} = \frac{Ax_i}{x_i + \beta Ax_i} = \frac{A}{1 + A\beta} \approx \frac{1}{\beta} \quad (A\beta \gg 1)$$

8.2 Bandwidth and Gain



$$\omega_{Hf} = \omega_H \cdot (1 + A_M\beta) \quad \omega_{Lf} = \omega_L / (1 + A_M\beta)$$

8.3 Four Basic Feedback Topologies

Feedback Type	Usage	Gain	Input Resistance	Output Resistance
Series-Shunt	Voltage \rightarrow Voltage	$A_f = \frac{A}{1 + A\beta}$	$R_{if} = (1 + A\beta) R_i$	$R_{of} = \frac{R_o}{1 + A\beta}$
Shunt-Series	Current \rightarrow Current	$A_f = \frac{A}{1 + A\beta}$	$R_{if} = \frac{R_i}{1 + A\beta}$	$R_{of} = (1 + A\beta) R_o$
Series-Series	Voltage \rightarrow Current	$A_f = \frac{A}{1 + A\beta}$	$R_{if} = (1 + A\beta) R_i$	$R_{of} = (1 + A\beta) R_o$
Shunt-Shunt	Current \rightarrow Voltage	$A_f = \frac{A}{1 + A\beta}$	$R_{if} = \frac{R_i}{1 + A\beta} R_i$	$R_{of} = \frac{R_o}{1 + A\beta}$

8.4 Positive or Negative

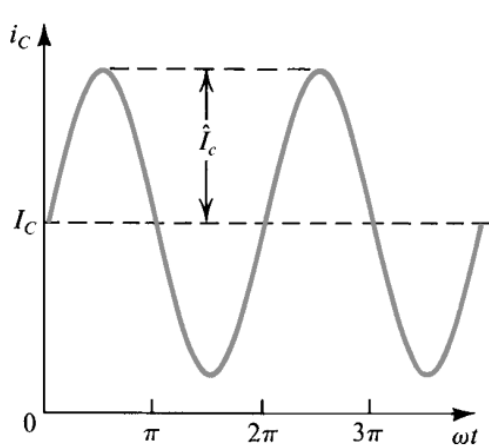
Amplifier Type	Common S	Common G	Common D	Common E	Common B	Common C
Sign of A_v	—	+	+	—	+	—

Chapter 9

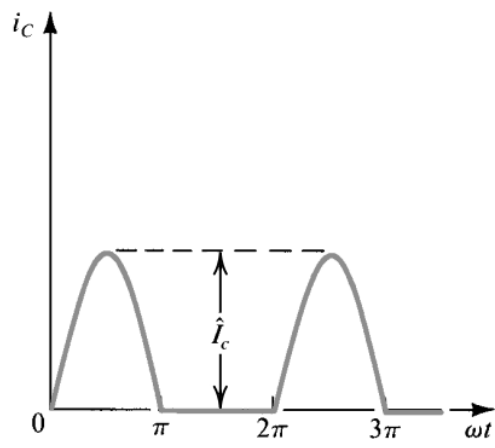
Power Amplifiers

9.1 Classification of Power Amplifiers

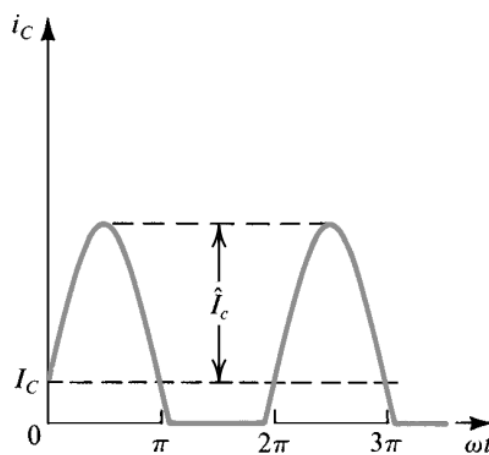
Output stages are classified according to the collector current waveform that results when an input signal is applied.



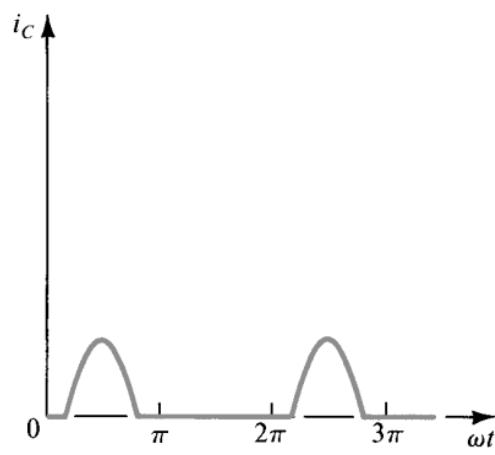
(a) Class A



(b) Class B

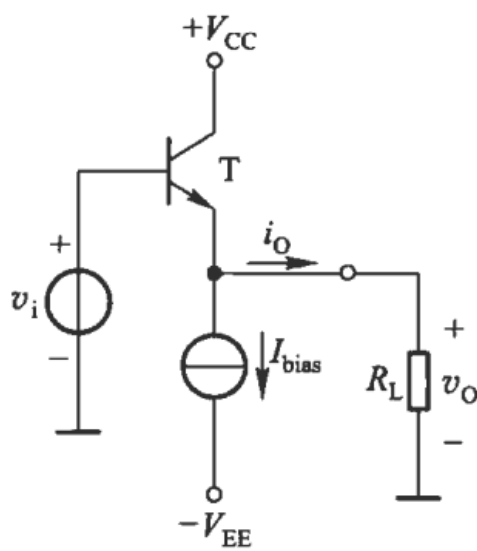


(a) Class AB

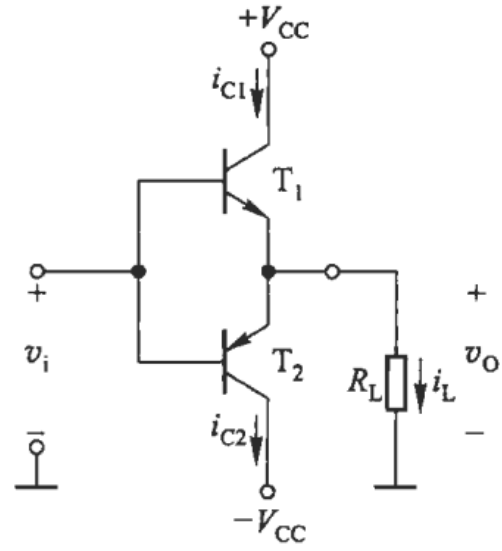


(b) Class C

9.2 Some Example Circuits



(a) Class A Output Stage



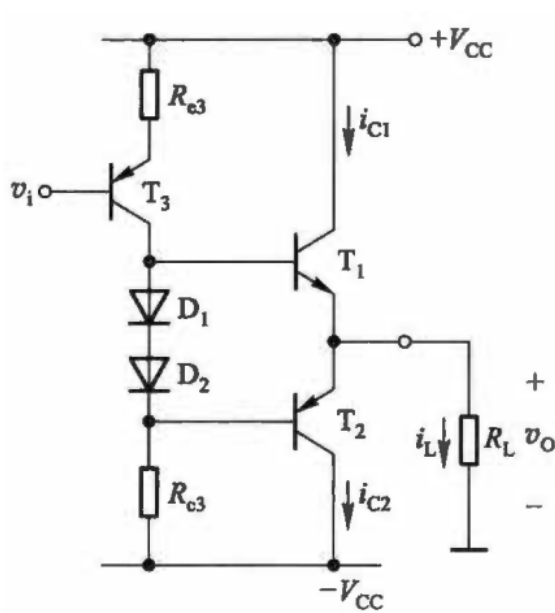
(b) Class B Output Stage

9.3 Power Output

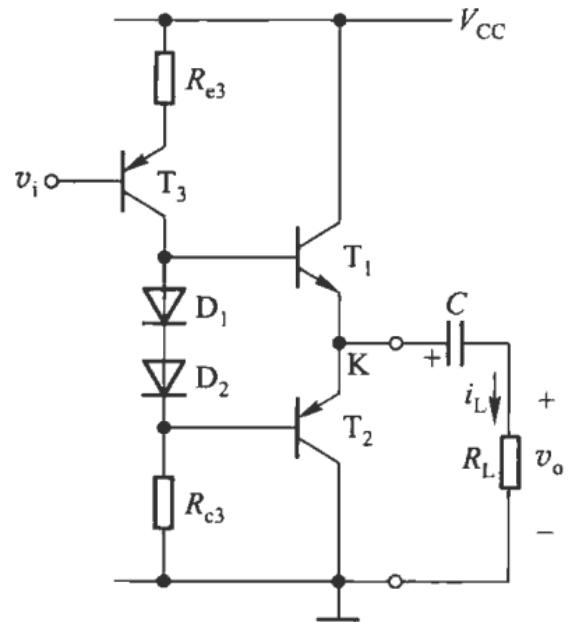
$$V_{om} = \frac{V_{om+} + V_{om-}}{2}$$

$$P_{om} = \left(\frac{V_{om}}{\sqrt{2}} \right)^2 \cdot \frac{1}{R_L}$$

9.4 Crossover Distortion Avoiding



(a) Class AB Output Stage with Double Source

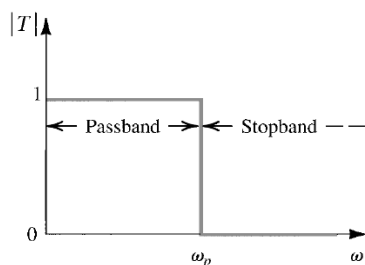


(b) Class AB Output Stage with Single Source

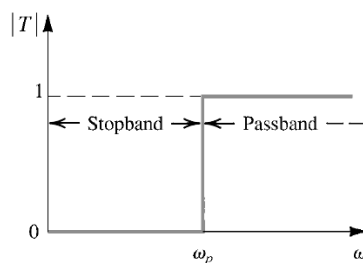
Chapter 10

Filters and Signal Generators

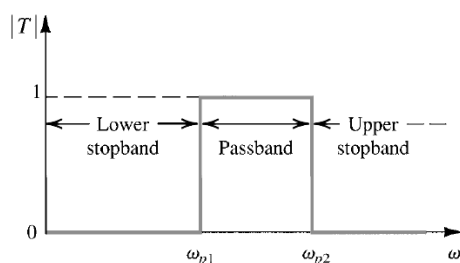
10.1 Four Types of Filters



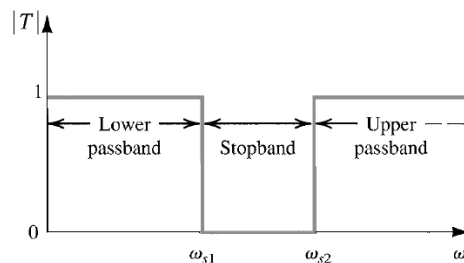
(a) Low-pass (LP)



(b) High-pass (HP)

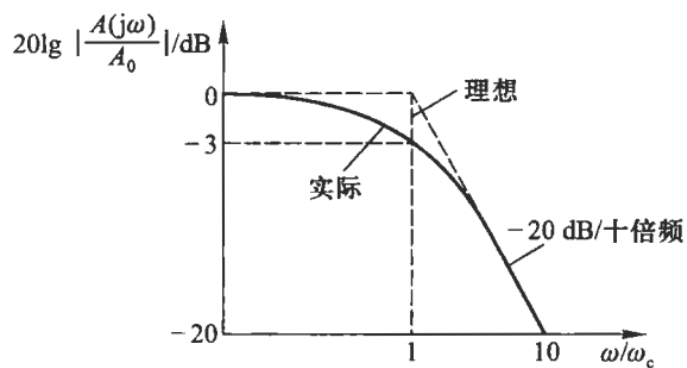
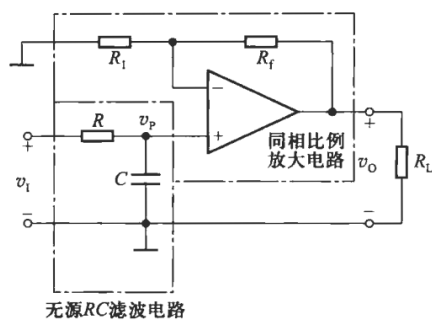


(c) Bandpass (BP)

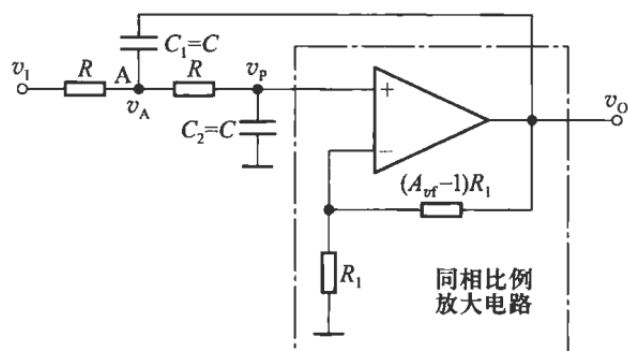


(d) Bandstop (BS)

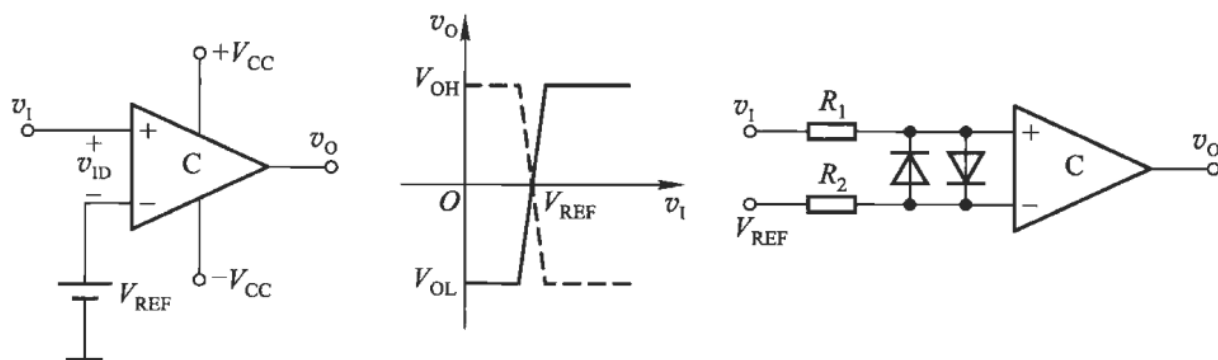
10.2 Filter with source



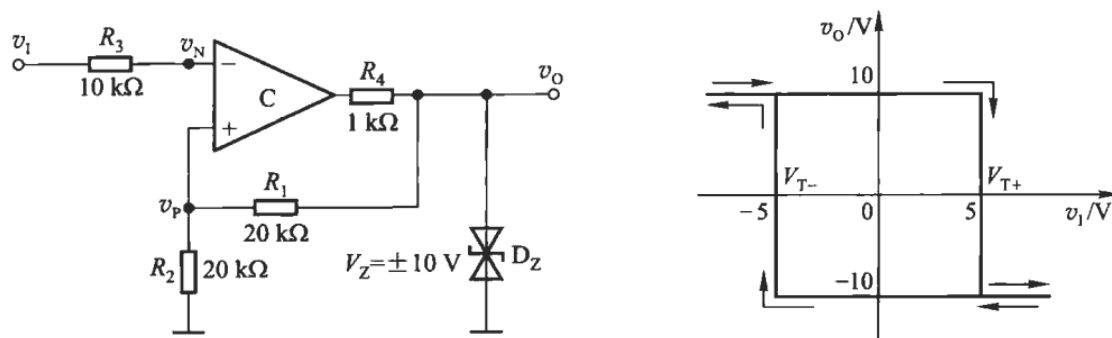
10.3 Sallen-Key Filtering Circuit



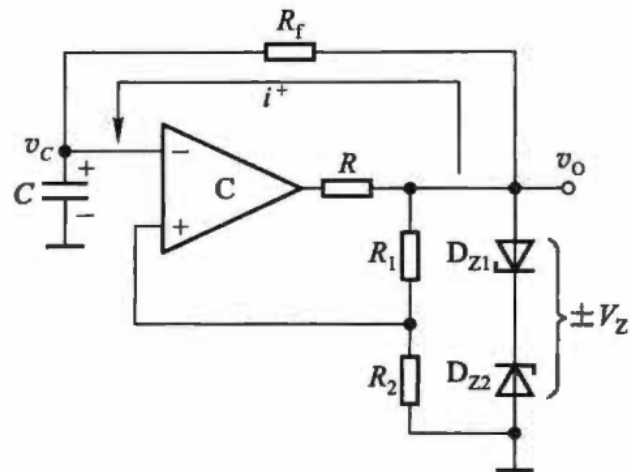
10.4 Voltage Comparator



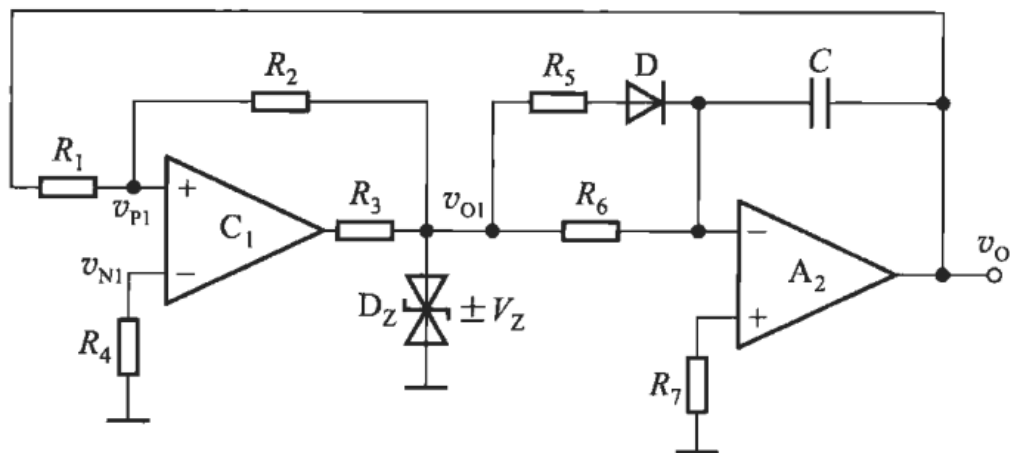
10.5 Trigger



10.6 Generation of Square Waveforms



10.7 Generation of Triangle Waveforms

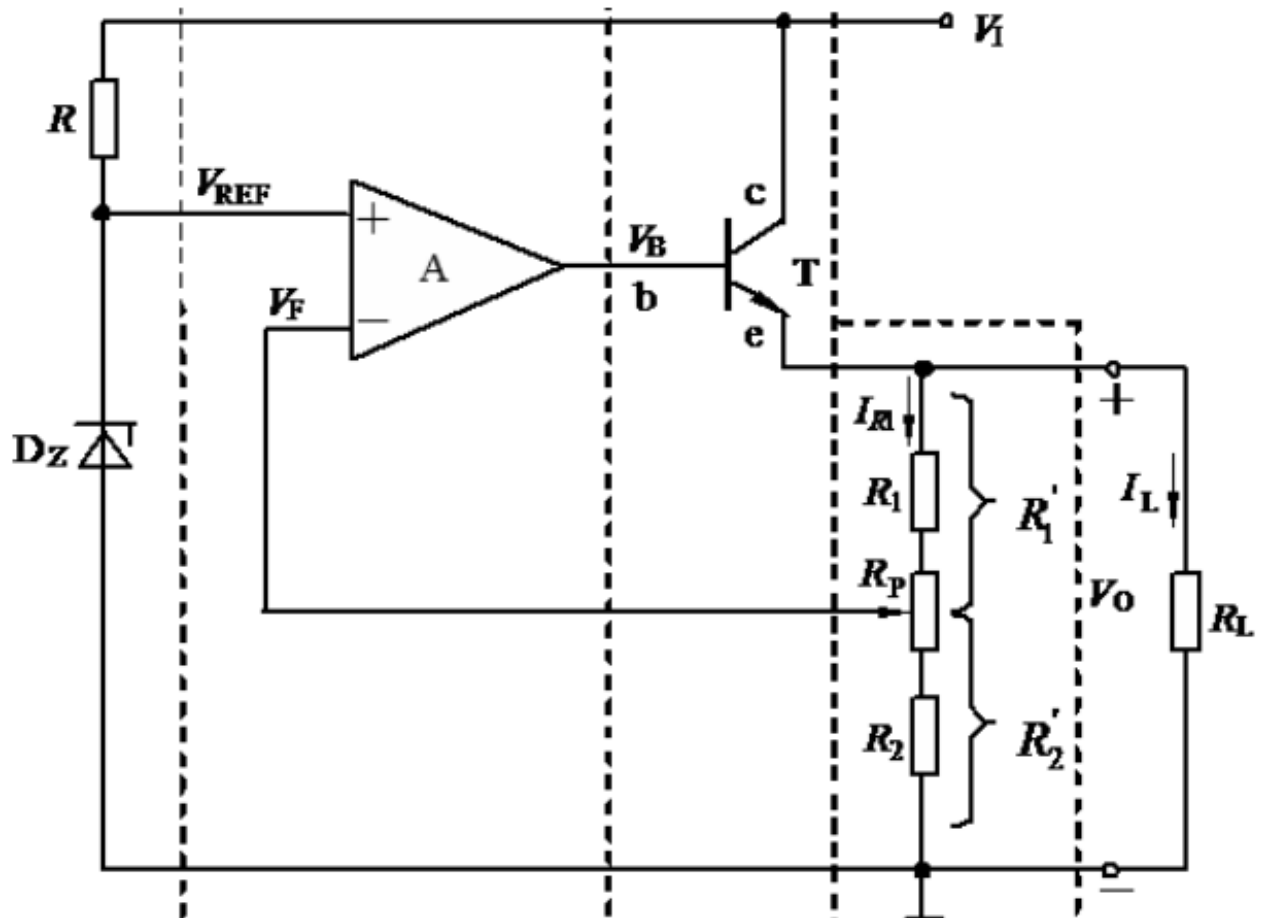


Stability Problem of DC Source

$$V_L \approx 0.9V_S \qquad I_{DI} = I_{D2} = I_{D3} = I_{D4} = \frac{0.45V_S}{R_L}$$

Figure 10-1 consists of two parts. The left part is a circuit diagram of a bridge rectifier. It features a transformer labeled 'Tr' with primary voltage $\sim 220\text{V}$ and frequency 50Hz . The secondary voltage is v_1 . The secondary terminals are connected to a bridge rectifier consisting of four diodes D_1, D_2, D_3, D_4 . The output terminals are labeled 'a' and 'b', with output voltage v_2 . The output is connected to a load resistor R_L in series with a switch S . A capacitor C is connected in parallel with the load. The output voltage across the load is v_L . The current through the capacitor is $i_{C充}$ (charging) and $i_{C放}$ (discharging). The current through the load is i_L . The right part is a graph of the output voltage v_L and capacitor voltage v_C versus angular frequency ωt . The input voltage is $v_2 = \sqrt{2} V_2 \sin \omega t$. The graph shows the capacitor charging (C 充电) and discharging (C 放电) cycles. The output voltage v_L is a full-wave rectified sine wave. The capacitor voltage v_C is a series of peaks and troughs. The peaks are labeled 'a', 'c', 'e' and the troughs are labeled 'b', 'd'. The troughs occur at $\pi, 2\pi, 3\pi, 4\pi$ on the ωt axis. The diodes are labeled as conducting during each half-cycle: D_1, D_3 for the first half-cycle, D_2, D_4 for the second half-cycle, and so on.

11.3 Series Feedback Voltage Regulator



$$V_O = V_{REF} \left(1 + \frac{R_1}{R_2} \right)$$

11.4 Integrated Voltage Regulator

