

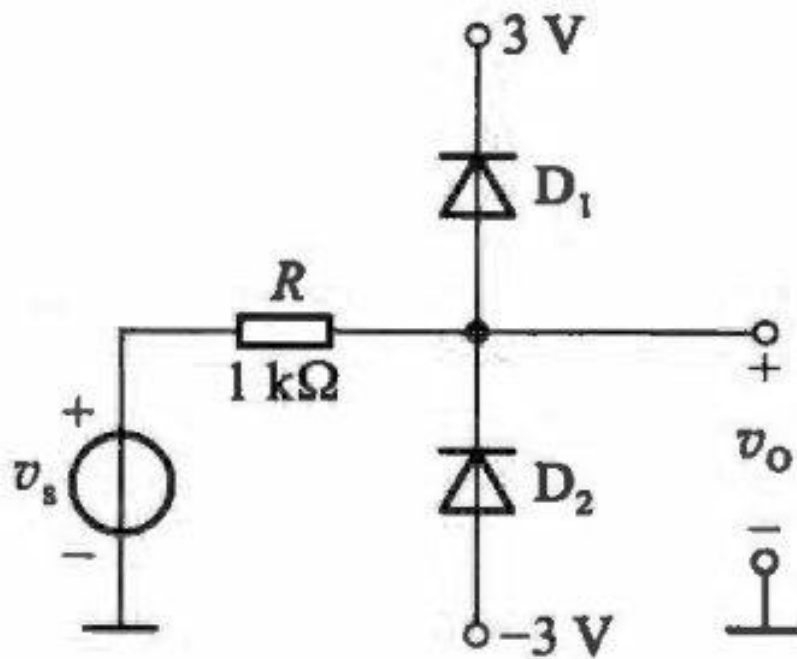
Homework for Chapter 3

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3.4.7 电路如图题 3.4.7 所示, D_1 、 D_2 为硅二极管, 当 $v_s = 6\sin \omega t$ V 时, 试用恒压降模型分析电路, 绘出输出电压 v_o 的波形。



图题 3.4.7

From the circuit we shall know, if the forward voltage of the 2 diodes are 0.7 V :

- A current flows in D_1 whenever $v_s > 3.7\text{ V}$
- A current flows in D_2 whenever $v_s < -3.7\text{ V}$

So that the voltage is being restricted between $\pm 3.7\text{ V}$

