EECS240 – Spring 2010

Lecture 14: Design Example



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Design Example

- What are the specs?
 - And where do they come from?
- Example: 50MS/s, ~11-bit ADC
 - Accuracy:
 - Settling time:
 - Dynamic range:
 - Closed-loop gain:
 - Power:

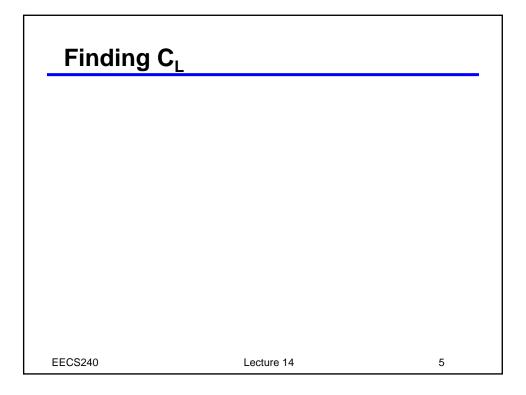
EECS240 Lecture 14 2

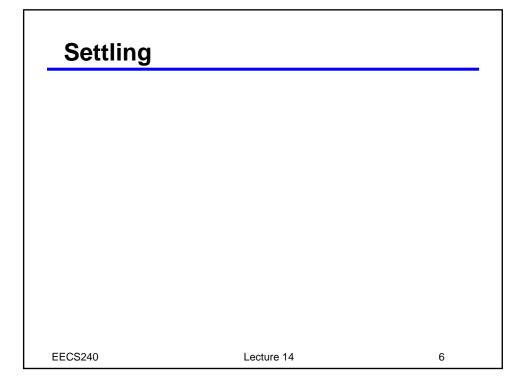
Design Procedure

Design Procedure Cont'd

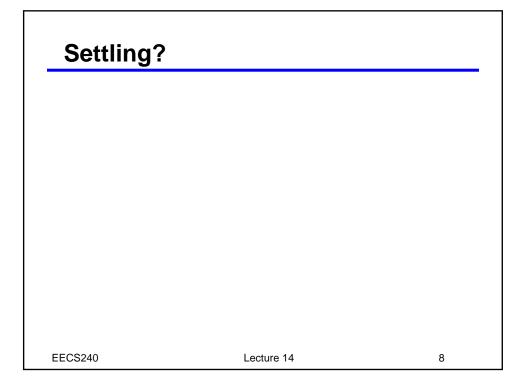
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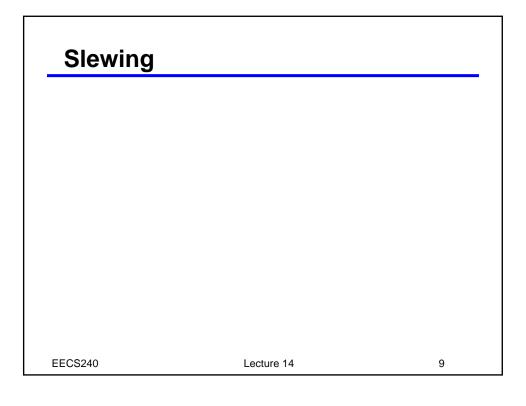
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Device Sizing EECS240 Lecture 14 7





Single-ended vs. Differential