

EECS240 – Spring 2010

Lecture 14: Design Example



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Design Example

- **What are the specs?**
 - And where do they come from?
- **Example: 50MS/s, ~11-bit ADC**
 - Accuracy:
 - Settling time:
 - Dynamic range:
 - Closed-loop gain:
 - Power:

Design Procedure

Design Procedure Cont'd

Finding C_L

Settling

Device Sizing

Settling?

Slewing

Single-ended vs. Differential
