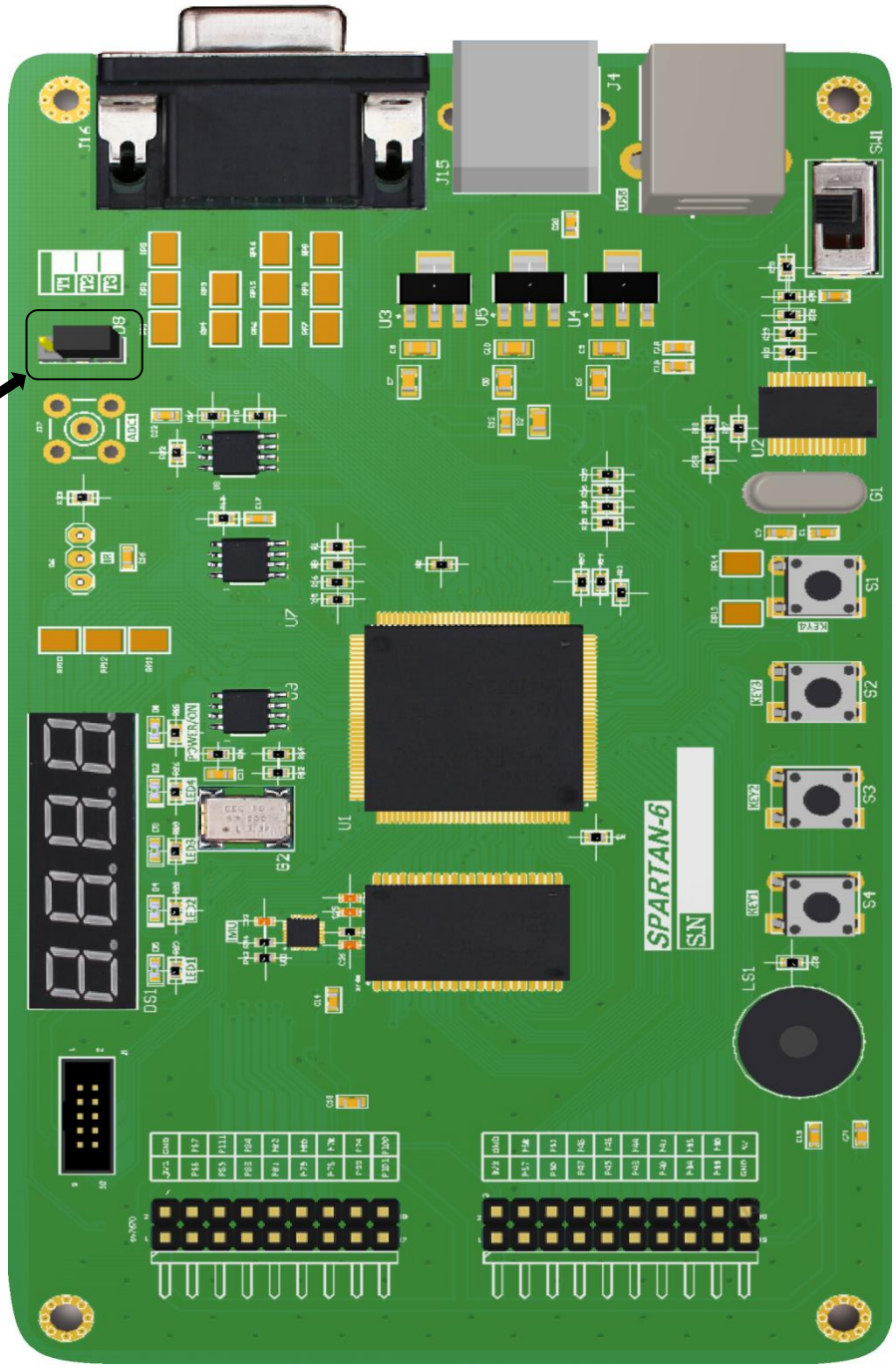
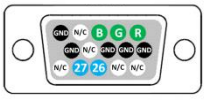
HDLab - FPGA Development Board



SRAM

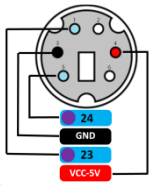
A0	102	A15	121
A1	104	A16	120
A2	105	A17	119
A3	10	A18	118
A4	9	D0	7
A5	143	D1	6
A6	142	D2	5
A7	141	D3	2
A8	140	D4	131
A9	139	D5	127
A10	138	D6	126
A11	137	D7	124
A12	134	CS	8
A13	133	WE	1
A14	132	OE	123

VGA

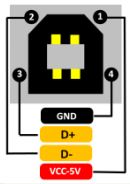


46	47	48	50	51	R	
35	40	41	43	44	45	G
29	30	32	33	34	B	

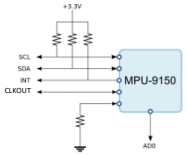
PS2



USB

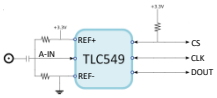


IMU



SCL	114
SDA	112
CLKOUT	115
INT	116
AD0	117

ADC



A-IN	SMA
CS	61
CLK	55
DOUT	59

POWER ON/OFF

Jumper

GND	A-IN	G-VGA

UART

TX	14
RX	12

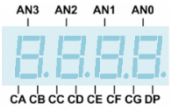
IR

IR	62

Reset B

RESET BUT	21

7 Segment



CA	80	CG	84
CB	79	DP	85
CC	83	AN0	66
CD	82	AN1	74
CE	81	AN2	67
CF	78	AN3	75

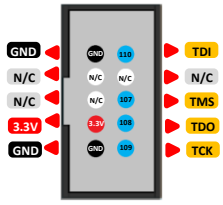
Buttons

BUTTON 1	15
BUTTON 2	16
BUTTON 3	17
BUTTON 4	21

LEDs

LED 1	94
LED 2	95
LED 3	97
LED 4	98

JTAG PORT

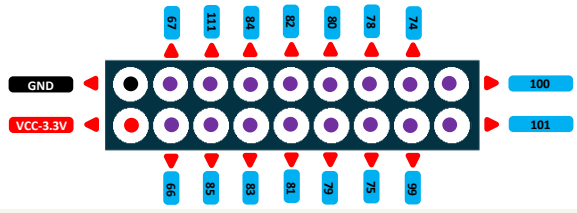


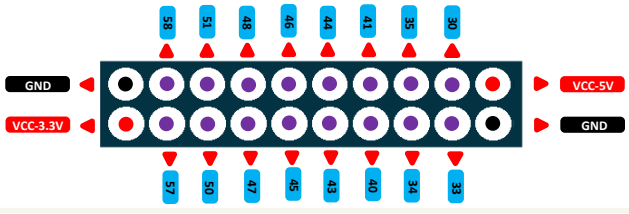
Buzzer

BUZZER	11

CLK

50M-CLK	56





Power | GND | Control | Physical Pin | Analog Pin | Pin Name | Digital Pin