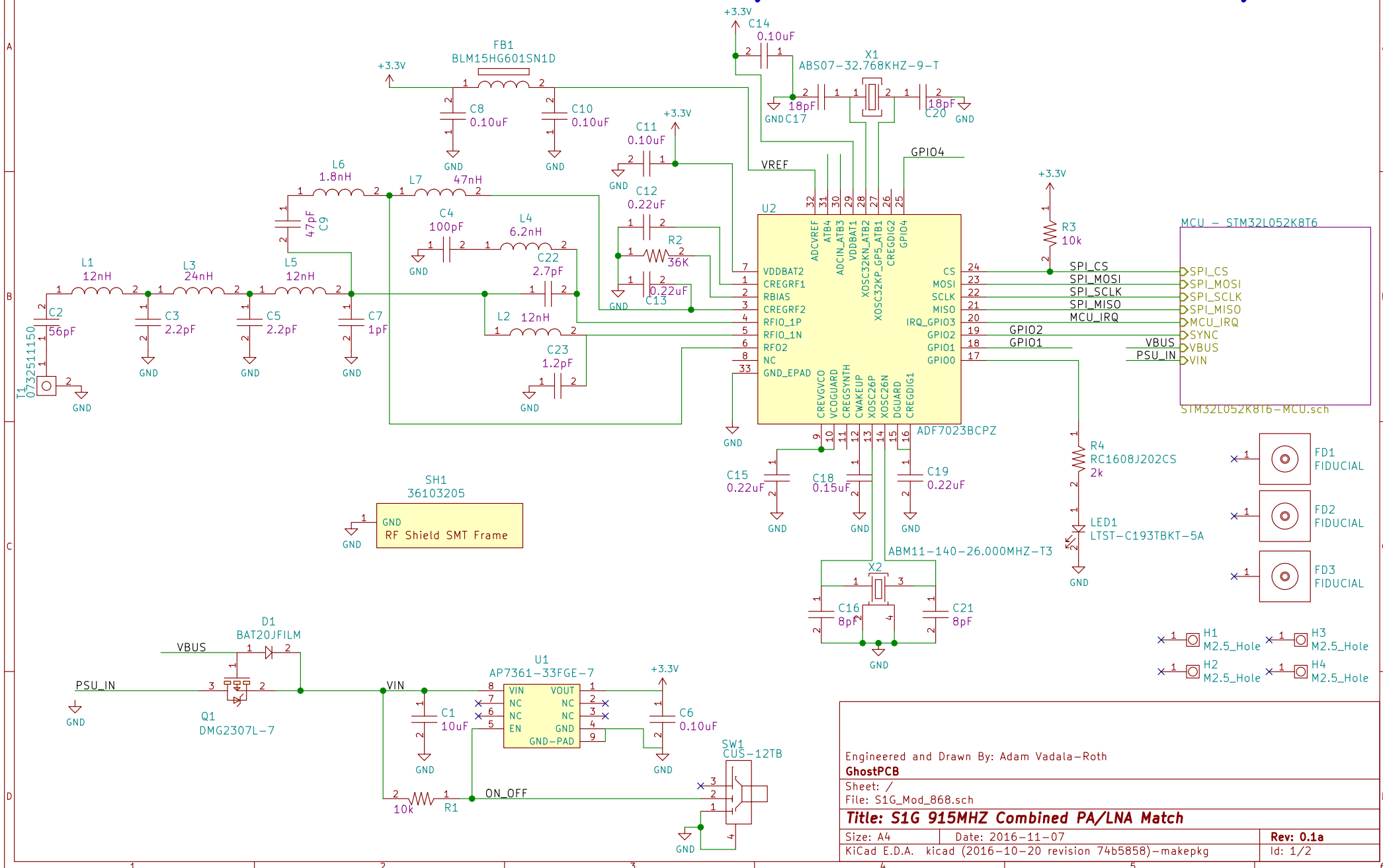# S1G-Mod 915MHZ Sub 1GHZ RF RX/TX Module Combined PA/LNA

Engineered and Drawn By: Adam Vadala-Roth

GhostPCB

Sheet: /
File: S1G_Mod_868.sch

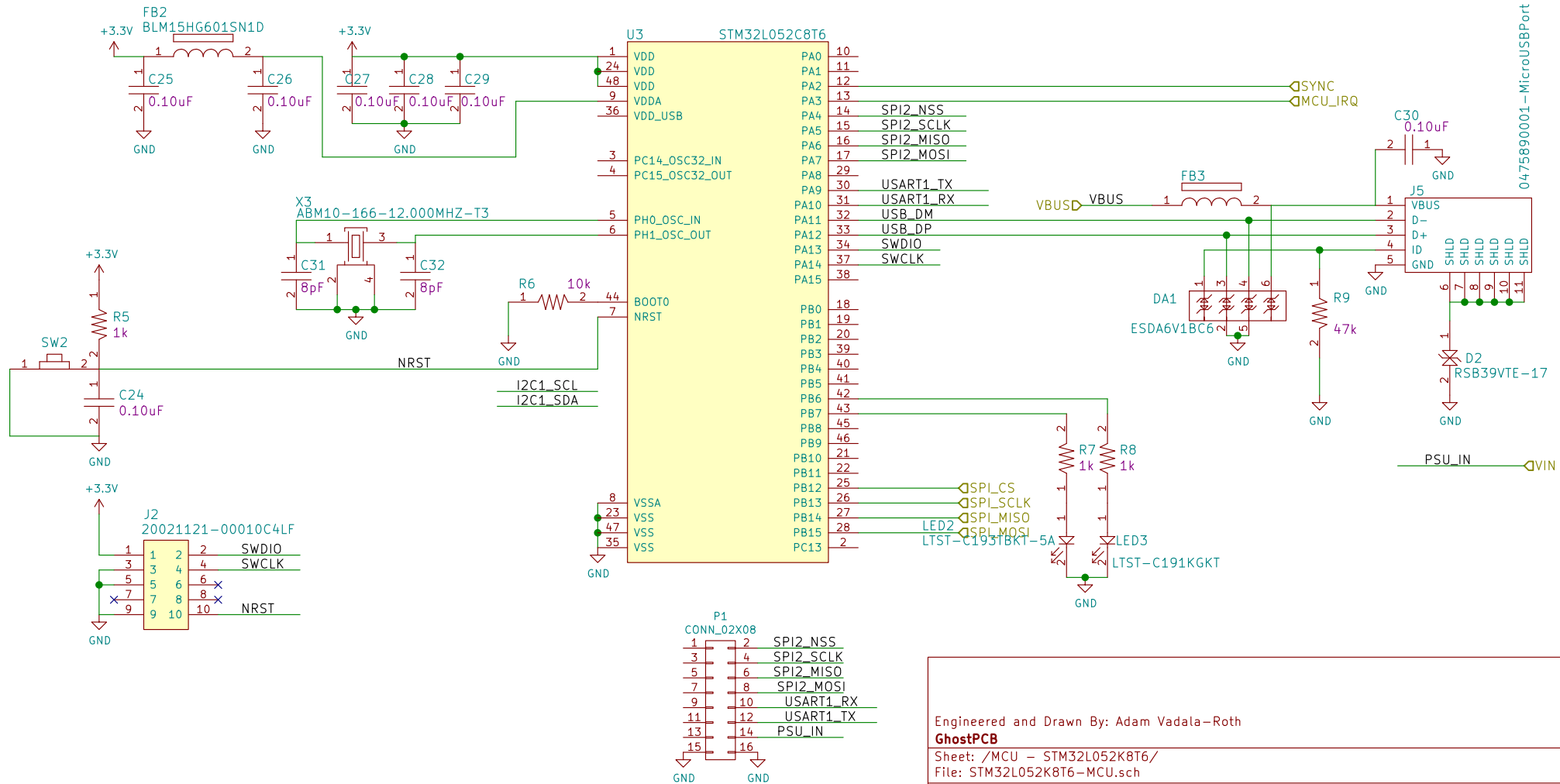*Title: S1G 915MHZ Combined PA/LNA Match*

Size: A4	Date: 2016-11-07	Rev: 0.1a
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STM32L052C8T6 – MCU for COMs and Configuration

U3 — STM32L052C8T6

FB2 BLM15HG601SN1D
+3.3V
C25 0.10uF
C26 0.10uF
GND
+3.3V
C27 0.10uF C28 0.10uF C29 0.10uF
GND

X3 ABM10-166-12.000MHZ-T3
C31 8pF C32 8pF
GND

+3.3V
R5 1k
SW2
C24 0.10uF
GND
NRST
GND

R6 10k
GND

+3.3V
J2 20021121-00010C4LF
SWDIO
SWCLK
NRST
GND

P1 CONN_02X08
SPI2_NSS
SPI2_SCLK
SPI2_MISO
SPI2_MOSI
USART1_RX
USART1_TX
PSU_IN
GND

VDD 1
VDD 24
VDD 48
VDDA 9
VDD_USB 36
PC14_OSC32_IN 3
PC15_OSC32_OUT 4
PH0_OSC_IN 5
PH1_OSC_OUT 6
BOOT0 44
NRST 7
VSSA 8
VSS 23
VSS 47
VSS 35
GND

PA0 10
PA1 11
PA2 12
PA3 13
PA4 14 SPI2_NSS
PA5 15 SPI2_SCLK
PA6 16 SPI2_MISO
PA7 17 SPI2_MOSI
PA8 29
PA9 30 USART1_TX
PA10 31 USART1_RX
PA11 32 USB_DM
PA12 33 USB_DP
PA13 34 SWDIO
PA14 37 SWCLK
PA15 38
PB0 18
PB1 19
PB2 20
PB3 39
PB4 40
PB5 41
PB6 42
PB7 43
PB8 45
PB9 46
PB10 21
PB11 22
PB12 25 SPI_CS
PB13 26 SPI_SCLK
PB14 27 SPI_MISO
PB15 28 SPI_MOSI
PC13 2

SYNC
MCU_IRQ

I2C1_SCL
I2C1_SDA

LED2 LTST-C193TBKT-5A
R7 1k R8 1k
LED3 LTST-C191KGKT
GND

FB3
VBUS VBUS
DA1 ESDA6V1BC6
GND
R9 47k
GND

C30 0.10uF
GND
J5
VBUS
D-
D+
ID
GND
SHLD SHLD SHLD SHLD SHLD
0475890001-MicroUSBPort
GND
D2 RSB39VTE-17
GND

PSU_IN VIN

Engineered and Drawn By: Adam Vadala-Roth
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Sheet: /MCU – STM32L052K8T6/
File: STM32L052K8T6-MCU.sch
Title: STM32L052C8T6 – MCU for COMS/Config
Size: A4 | Date: 2016-11-07 | **Rev: 0.1a**
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