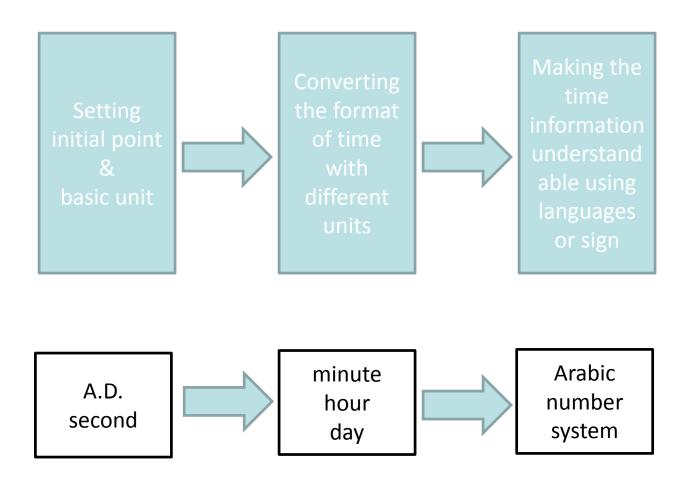
EEE116 Experimental, Computer Skills and Sustainability

Week 3 Digital clock pre-lab tutorial



Processing of Time Information





Digital clock project



To design a digital clock which can be started, stopped and reset. It should be able to count up to a certain number, such as, 59 seconds (i.e. from 0,1,2,3....57, 58, 59, 0, 1, 2, 3....) and output the seconds by a pair of 7-segment LED displays.

This is a design project, there is no step by step instruction for you to follow.

Milestones



Functional blocks

To start on the design of your clock, break the whole system down into functional blocks such as the timing generator, the decoder, the display, the set logic, etc.

Complete design

Specify how the functional blocks interface with each other. At this point you can have a completed design with a full schematic in Multisim or online simulation tools. (Optional)

https://circuits.io

Milestones



Building and testing

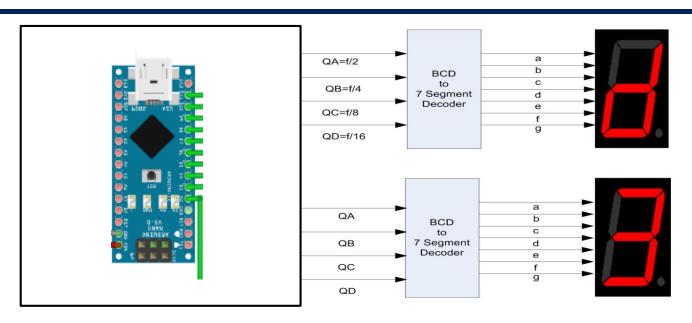
A good approach is to build each of your functional blocks from milestone M1 and test them independently. For example, use a function generator as input signal for a block and look at the output of the block using the oscilloscope.

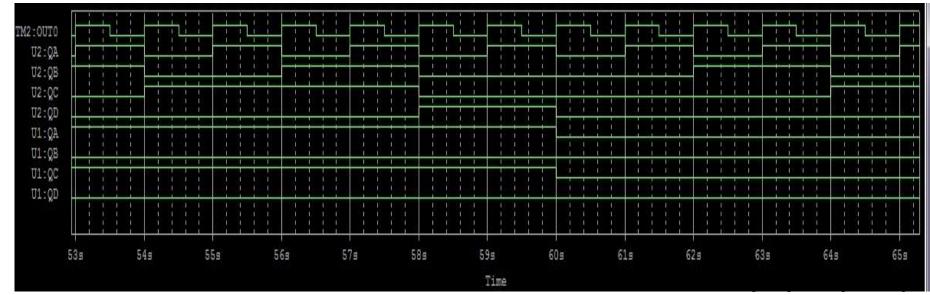
Demonstration of Working Clock.

You must demonstrate your working clock to your TA before the end of the lab. Take a short video clip for your work (as backup).

Basic Structure







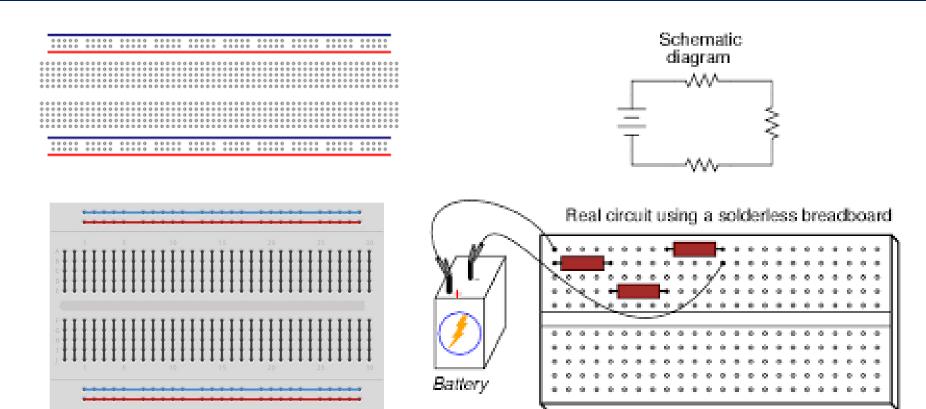
Generate time signal from Arduino 高交利物浦大學

```
_ D X
oo SingleDig_0_to_10_v1 | Arduino 1.8.1
File Edit Sketch Tools Help
        Ø
  SingleDig_0_to_10_v1 §
 int inputs[4] = {8,9,10,11}; // A,B,C,D inputs
byte BCD[16][4] ={\{0,0,0,0,0\},
{1,0,0,0},
 {0,1,0,0},
{1,1,0,0},
 {0,0,1,0},
{1,0,1,0},
 {0,1,1,0},
{1,1,1,0},
 {0,0,0,1},
 {1,0,0,1},
 {0,1,0,1},
 {1,1,0,1},
 {0,0,1,1},
 {1,0,1,1},
 {0,1,1,1},
{1,1,1,1}}; //BCD code
void setup() {
for (int a = 0; a < 4; a++) {
pinMode(inputs[a], OUTPUT);} //set outputs
void loop() {
static int num = 0;
for (int c = 0; c < 4; c++) {
digitalWrite(inputs[c], BCD[num][c]);
  num++:
  num = num % 10;
  delay(1000);
```

```
💿 digitalNum | Arduino 1.8.1
File Edit Sketch Tools Help
  digitalNum
void setup() {
  // put your setup code here, to run once:
  pinMode(9, OUTPUT);
  pinMode (10, OUTPUT);
  pinMode (11, OUTPUT);
  pinMode (12, OUTPUT);
void loop() {
  static int num = 0;
  // put your main code here, to run repeatedly:
  digitalWrite(9, !!(num \in (0x01 << 0)));
  digitalWrite(10, !!(num & (0x01<<1)));
  digitalWrite(11, !!(num & (0x01<<2)));
  digitalWrite(12, !!(num & (0x01<<3)));
  num = num % 10;
  delay(1000);
```

Breadboard



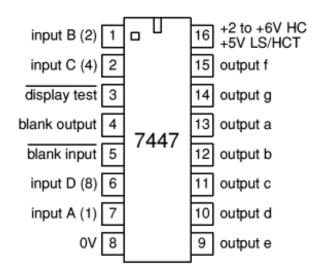


Use DC power supply on your bench to power the circuit and arduino, don't use Arduino as power source. (to protect your Arduino and your laptop)

Connect 5V to the red line and Ground to the blue line.

BCD-7 Segment Decoder





BCD--Binary Coded Decimal

- Before signals go to LED, a 330ohms resistor is needed for each output of 7447 (To limit the current, protect the LED light and your eyes)
- Every IC need to be powered. Connect Vcc (Pin 16) to 5V, and GND (Pin 8) to ground.

Read the datasheet



SN5446A, '47A, '48, SN54LS47, 'LS48, 'LS49 SN7446A, '47A, '48, SN74LS47, 'LS48, 'LS49 BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

'46A, '47A, 'LS47 feature

- Open-Collector Outputs **Drive Indicators Directly**
- Lamp-Test Provision
- Leading/Trailing Zero Suppression

'48, 'LS48 feature

- Internal Pull-Ups Eliminate **Need for External Resistors**
- Lamp-Test Provision
- · Leading/Trailing Zero Suppression

'LS49 feature

- . Open-Collector Outputs
- Blanking Input

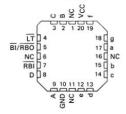
SN5446A, SN5447A, SN54LS47, SN5448, SN54LS48 . . . J PACKAGE SN7446A, SN7447A, SN7448 . . . N PACKAGE SN74LS47, SN74LS48 . . . D OR N PACKAGE (TOP VIEW) VCC 15 f



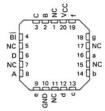
SN54LS49 . . . J OR W PACKAGE SN74LS49 . . . D OR N PACKAGE (TOP VIEW)



SN54LS47, SN54LS48 . . . FK PACKAGE (TOP VIEW)



SN54LS49 . . . FK PACKAGE (TOP VIEW)



NC - No internal connection

SN5446A, '47A, '48, SN54LS47, 'LS48, 'LS49 SN7446A, '47A, '48, SN74LS47, 'LS48, 'LS49 BCD-TO-SEVEN-SEGMENT DECODERS/DRIVERS

description

The '46A, '47A, and 'LS47 feature active-low outputs designed for driving common-anode LEDs or incandescent indicators directly. The '48, 'LS48, and 'LS49 feature active-high outputs for driving lamp buffers or common-cathode LEDs. All of the circuits except 'LS49 have full ripple-blanking input/output controls and a lamp test input. The 'LS49 circuit incorporates a direct blanking input. Segment identification and resultant displays are shown below. Display patterns for BCD input counts above 9 are unique symbols to authenticate input conditions.

The '46A, '47A, '48, 'LS47, and 'LS48 circuits incorporate automatic leading and/or trailing-edge zero-blanking control (RBI and RBO). Lamp test (LT) of these types may be performed at any time when the BI/RBO node is at a high level. All types (including the '49 and 'LS49) contain an overriding blanking input (BI), which can be used to control the lamp intensity by pulsing or to inhibit the outputs. Inputs and outputs are entirely compatible for use with TTL logic

The SN54246/SN74246 and '247 and the SN54LS247/SN74LS247 and 'LS248 compose the 5 and the 9 with tails and were designed to offer the designer a choice between two indicator fonts.



NUMERICAL DESIGNATIONS AND RESULTANT DISPLAYS

SEGMENT

DECIMAL	INPUTS						BI/RBO†	OUTPUTS						NOTE	
FUNCTION	LT	RBI	D	С	В	А	Dirito	а	b	c	d		f	g	
0	Н	н	L	L	L	L	н	ON	ON	ON	ON	ON	ON	OFF	
1	н	×	L	L	L	н	н	OFF	ON	ON	OFF	OFF	OFF	OFF	
2	Н	×	L	L	н	L	н	ON	ON	OFF	ON	ON	OFF	ON	
3	н	X	L	L	н	н	н	ON	ON	ON	ON	OFF	OFF	ON	
4	Н	×	L	н	L	L	н	OFF	ON	ON	OFF	OFF	ON	ON	
5	н	×	L	н	L	н	н	ON	OFF	ON	ON	OFF	ON	ON	
6	н	×	L	н	н	L	н	OFF	OFF	ON	ON	ON	ON	ON	
7	н	X	L	н	H	н	н	ON	ON	ON	OFF	OFF	OFF	OFF	1
8	н	X	н	L	L	L	н	ON	ON	ON	ON	ON	ON	ON	'
9	н	x	н	L	L	н	н	ON	ON	ON	OFF	OFF	ON	ON	1
10	н	×	н	L	н	L	н	OFF	OFF	OFF	ON	ON	OFF	ON	
11	н	×	н	L	н	н	н	OFF	OFF	ON	ON	OFF	OFF	ON	
12	Н	×	н	н	L	L	н	OFF	ON	OFF	OFF	OFF	ON	ON	1
13	н	×	н	H	L	н	н	ON	OFF	OFF	ON	OFF	ON	ON	
14	н	×	н	н	н	L	н	OFF	OFF	OFF	ON	ON	ON	ON	1
15	н	×	н	н	н	н	н	OFF	OFF	OFF	OFF	OFF	OFF	OFF	
81	X	X	X	X	X	×	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	2
RBI	н	L	L	L	L	L	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	3
LT	L	×	×	×	X	×	н	ON	ON	ON	ON	ON	ON	ON	4

H = high level, L = low level, X = irrelevant NOTES: 1. The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The

- 2. When a low logic level is applied directly to the blanking input (BI), all segment outputs are off regardless of the level of any
- 3. When ripple-blanking input (RBI) and inputs A, B, C, and D are at a low level with the lamp test input high, all segment outputs
- go off and the ripple-blanking output (RBO) goes to a low level (response condition).

 4. When the blanking input/ripple blanking output (81/RBO) is open on held high and a low is applied to the lamp-test input, all

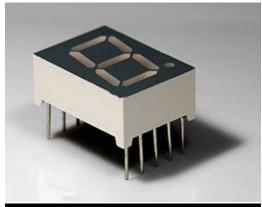
1BI/RBO is wire AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).

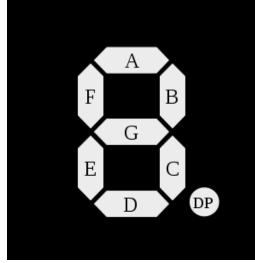


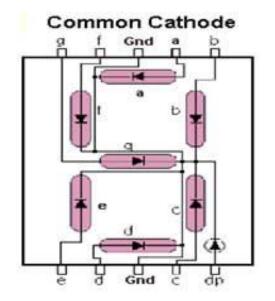
Copyright @ 1988, Texas Instruments Incorporated

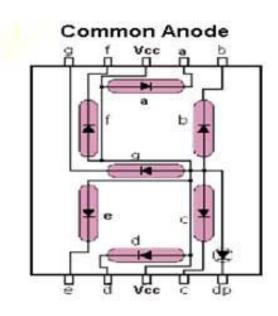
7 Segment Indicator











How to light up number '7' in Common Cathode LED.

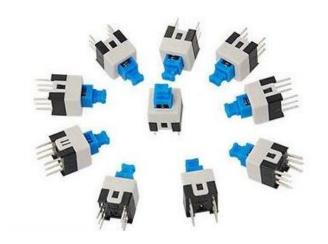
a	b	С	d	е	f	g
1	1	1	0	0	0	0

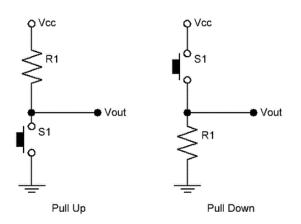
How to light up number '4' in Common Anode LED?

Using switch

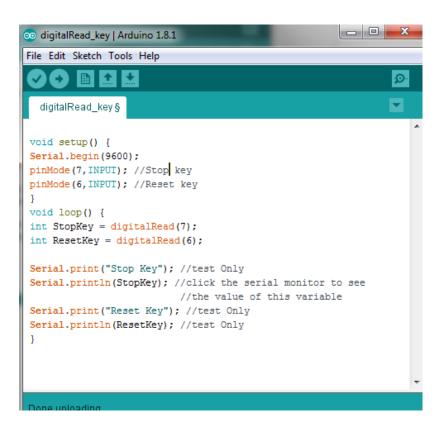


6pin self-locking push button switch









A Pull up or Pull down Resistor MUST (around 10K Ω) be used.

www.TuElectronica.es

Basic requirements



1. Two digit displays:

Your clock has to display two digit numbers. Each digit should roll over at a certain time.

The roll over time is your group number, if you are group number is single digit, then your roll over time is 50 + group number.

For example,

- Your are in group B34, roll over time is 34. Your clock should count 33, 34, 00, 01, 02 ...
- Your are in group A05, roll over time is 50+05=55. Your clock should count 54, 55, 00, 01, 02 ...

2. Pause and reset:

Your clock must can be stopped and reset by press bottoms.

Files to Be Uploaded



On behalf of the whole team, the team leader should upload one video clip to ICE.

MAXIMUM SIZE FOR UPLOADING: 20MB

Schudule



Week	Lab name	Student group	Assessment
Week-8	Digital clock	Group A	Bench inspection for
	Matlab computer lab 2	Group B	Digital clock (Group A)
Mook 0	Digital clock	Group B	Bench inspection for
Week-9	Matlab computer lab 2	Group A	Digital clock (Group B)

Assessment (5%)



Group marks - Bench inspection					
	Two-digit roll over (2%)	Stop and reset (2%)	Circuit layout (1%)	5%	

Bench inspection: demonstration of stopwatch function and circuit layout.