of EEE307

Electronics for Communications

Department of Electrical & Electronic Engineering Xi'an Jiaotong-Liverpool University (XJTLU)

Friday, 29th November 2019

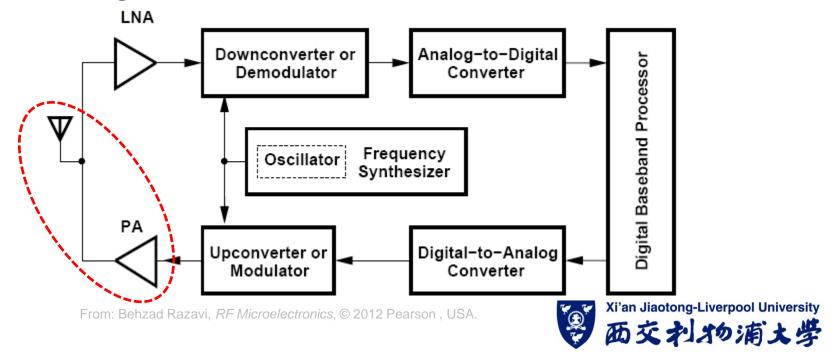
- □ Large-signal Amplification
 - efficiency & linearity consideration
 - > common-source configuration
- □ Classification of Power Amplifiers
 - classes A, B, AB, C linear type
 - → classes E & F switching type



Wireless Transceiver

(antenna driven by transmitter)

□ In using **radio waves** to transmit and receive information, the **transmitter** has to drive the **antenna** with a high power level so that the **electromagnetic** (EM) signal is strong enough to reach certain desirable distances.

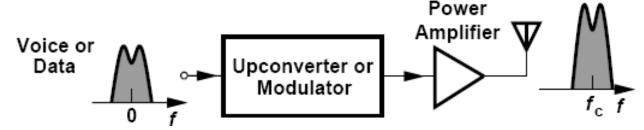


Radio Transmitter

(deliver RF power to antenna)

In radio transmitters, the **power amplifier** (PA) is the key building block to deliver electrical power *efficiently* to the **antenna** for generating EM waves which carry the information.

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□ While the RF PA is also a signal amplification circuit, its design and construction is not simply scaling up the small-signal amplifiers (e.g. LNA) to deliver higher output power.

(power consumption & efficiency)

- □ To deliver high enough power (e.g. 1 W or 30 dBm) to the **antenna**, the RF PA consumes most power in a radio transceiver.
- ☐ High **power consumption** in RF PAs is unavoidable and the key issue is **efficiency**.
- □ Power efficiency is particularly important for portable applications because we want to extend the battery life or reduce the battery weight.
- - \triangleright power loss \Rightarrow heat dissipation.

(50% efficiency in maximum power transfer)

- With the importance of efficiency in RF PAs, the consideration of maximum power transfer (by conjugate matching) in small-signal amplifiers (e.g. LNA) is not quite applicable.
 - ➤ It is also unclear to define impedances in a large-signal, nonlinear system like RF PAs.
- Even with conjugate matching for maximum power transfer, the **efficiency** is only 50% at best.
 - ➤ 50% of the DC power supplied to the RF PA would dissipate as **heat** ⇒ thermal management problems at the chip level or package level スi'an Jiaotong-Liverpool University 西交利が消入学
 - > possible heat sink requirement

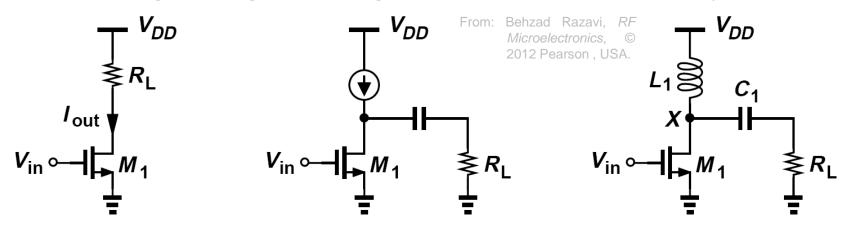
(other performance parameters & trade-off)

- ☐ Apart from efficiency, linearity and output power (as well as the power gain) are other important performance parameters in RF PAs.
 - > In modern wireless communication systems, complex modulation schemes (with amplitude & phase modulations combined together) are employed to increase the data throughput. This places higher linearity requirement on RF PAs.
- ☐ The RF PA design typically involves the **trade-off** between **efficiency** and **linearity**.
 - > It is <u>performance</u> and <u>cost</u> trade-off.
 - > It is difficult to satisfy all requirements.



(common-source configuration)

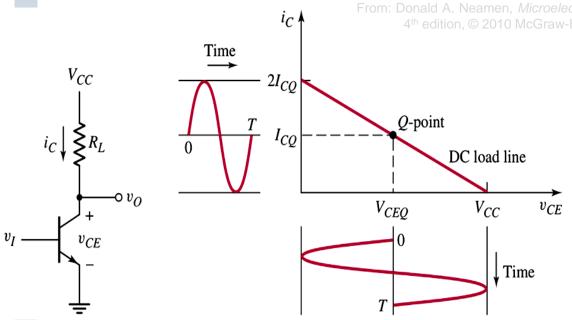
- □ An RF PA can be realised in CMOS technology using the standard **common-source** (CS) configuration as in small-signal amplifiers (e.g. LNA).
 - ➤ One difference in RF PAs is the large current (e.g. 200 mA) flowing through the single transistor in the CS amplifier.



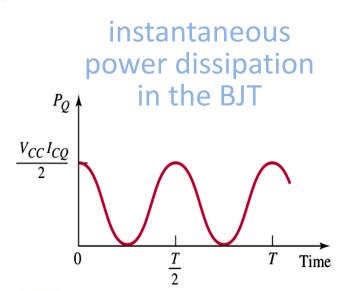
> The CS amplifier can have a resistive, current source or inductive load.

(common-emitter configuration)

☐ The use of the CS configuration in the RF PA is the same as that in the low-frequency PA design (e.g. audio amplifiers). The BJT counterpart is the common-emitter (CE) amplifier.



➤ The CE amplifier shown here is in class-A operation.

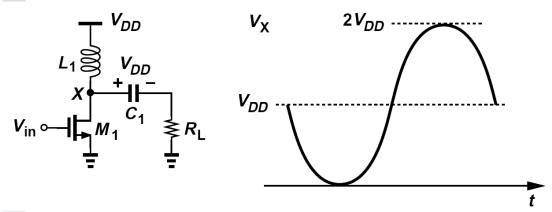




(inductive load & peak voltage)

- ☐ In the RF PA design with the common-source configuration, an **inductive load** is typically used.
 - \triangleright With the inductive load, the instantaneous voltage at the drain terminal of the MOSFET can be as high as $|2V_{DD}|$ while the output swing is between $-V_{DD}$ and $+V_{DD}$.

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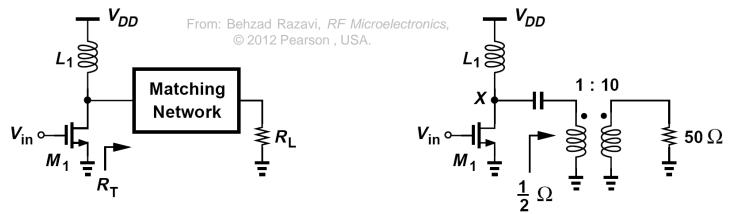
-V_{DD}

➤ This implies the high breakdown voltage requirement of the MOSFET.



(inductive load & peak voltage)

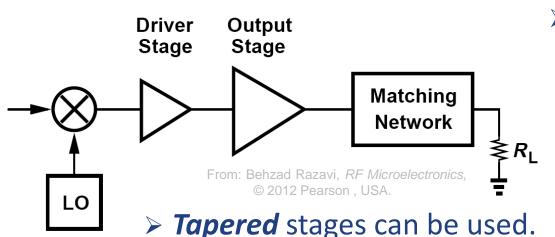
□ In order to reduce the peak voltage experienced by the transistor in the CS configuration, **matching network** is typically used between the RF PA and the antenna load.



- \succ The **matching network** *transforms* the load resistance to a much lower value (e.g. 50 Ω to 0.5 Ω).

(high currents & tapered stages)

- ☐ With the matching network for impedance transformation, the peak-to-peak voltage swing experienced by the transistor in the RF PA can be lowered.
- □ To deliver the same power to antenna load with a reduced voltage swing, it means that the current flowing into the RF PA must be increased.



➤ It needs a large size

(W/L) for the MOSFET

in the RF PA. Large size

however causes large

input capacitance.

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(efficiency definitions)

- ☐ Two common definitions of **efficiency** are used in quantifying the performance of RF PAs:
 - > the **drain efficiency** (when the MOS transistors are used): $\eta = \frac{P_{out}}{P_{supp}}$
 - > the power added efficiency (PAE): $\eta_{\text{PAE}} = \frac{P_{out} P_{in}}{P_{\text{supp}}}$

where P_{out} is the output power delivered to the load and P_{supp} is the DC power supplied to the RF PA.

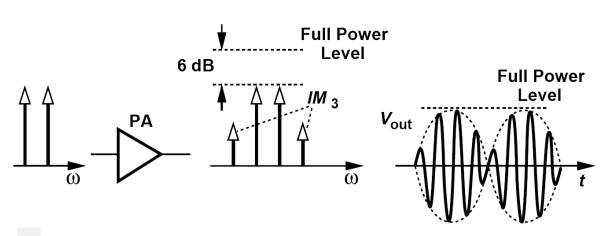
The drain efficiency η and PAE $\eta_{\rm PAE}$ will be very close if the RF PA has large enough power gain.

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(linearity)

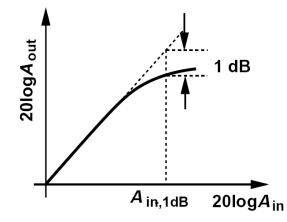
□ To gauge the **linearity** performance, the RF PA characterisation begins with two generic tests of nonlinearity based on unmodulated tones: **intermodulation** and **compression**.

Semester 1, 2019/2020 by S.Lam@XJTLU



The 3^{rd} order intercept point (IP3) and 1-dB compression point (P_{1dB}) are specified.

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(differential upconverter output)

- ☐ As the **antenna** is typically *single-ended*, most stand-alone RF PAs are single-ended.
- □ To avoid wasting half of the transmitter signal gain with the *differential* output signal from the upconverter, a balun can be used between the upconverter and the

single-ended RF PA.

Upconverter Upconverter Q Upconverter

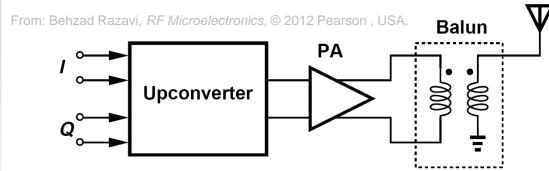
➤ A **balun** is a transformer used to convert an *unbalanced* signal to a *balanced* one or vice versa.



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(differential RF PA)

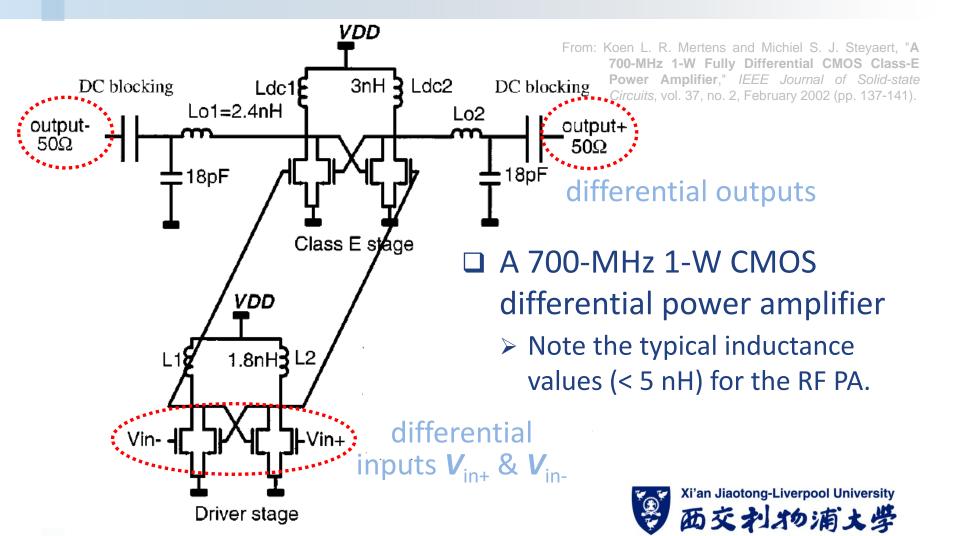
- □ As the RF PA circuit typically use some inductors, large transient currents can be induced easily in high frequency operation.
- □ A *differential* realisation of RF PAs can ease significantly the adverse effects of the large *transient* currents compared with the *single-ended* RF PAs.



With a differential RF PA, a balun is used to convert balanced signal to feed the single-ended antenna.



(differential implementation)



Classification of Power Amplifiers

(different classes & design trade-off)

- □ Despite the apparent simple circuit construction, power amplifiers are traditionally categorised in quite a number of classes: A, B, AB, C, D, E, F etc.
 - > The varying classes of RF PAs indicate the difficulty in the design **trade-off** to *optimise* the performance, especially **efficiency** and **linearity**.
- □ RF PAs of class A, B, AB and C have almost the same schematic circuit (e.g. CS or CE configuration) in terms of the power amplification transistor.
 - They are distinguished primarily by the <u>bias condition</u> of the transistor.
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Classification of Power Amplifiers

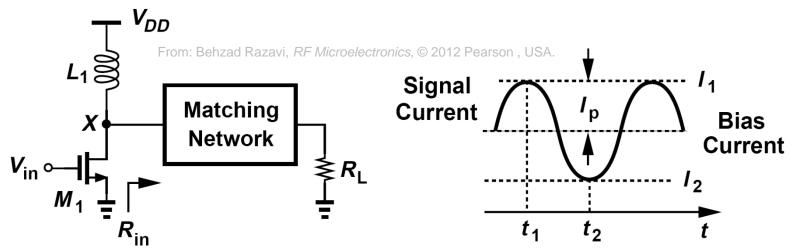
(linear & switching RF PAs)

- □ RF PAs of class A, B, AB and C share an attribute that both the input and output waveforms are presumably sinusoidal.
 - > They generally display better **linearity** in the signal amplification.
 - ➤ The power amplification transistors work as a voltage-controlled current source.
- □ RF PAs of class D, E and F are of the <u>switching type</u> that improves the **efficiency**.
 - > The transistor works as a <u>switch</u>.
 - > The waveforms are not sinusoidal.

Class A Power Amplifiers

(linear power amplifier)

□ In the common-source (or common-emitter in the BJT case) configuration, a <u>class A</u> power amplifier has the transistor always on and operating *linearly* across the full input and output range.



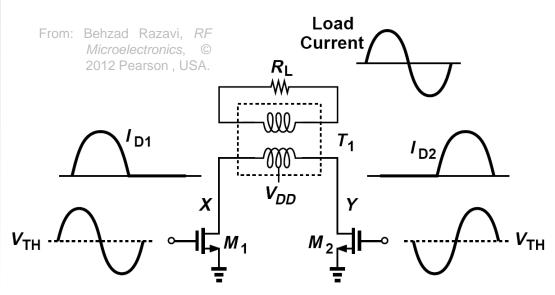
$$\eta = \frac{V_{DD}^2/(2R_{in})}{V_{DD}^2/R_{in}} = 50\%$$



Class B Power Amplifiers

(on half of the cycle)

- ☐ In a <u>class B</u> power amplifier, the power amplification transistors are on only during half of the cycle of the sinusoidal signal.
 - > Two transistors can be employed as parallel stages and each transistor conducts only half of the cycle.



- Class B power amplifiers have better efficiency than class A.
- $> \eta_B = \pi/4 \approx 79\%.$

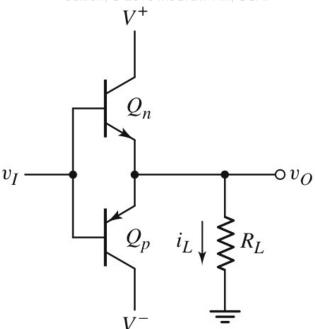


Class B Power Amplifiers

(use of complementary transistors)

□ A class B power amplifier can also be implemented using a pair of *complementary* transistors (e.g. npn & pnp BJTs, nMOSFET & pMOSFET).

From: Donald A. Neamen, *Microelectronics:*Circuit Analysis & Design, 4th
edition, © 2010 McGraw-Hill, USA.



- This configuration is called a complementary push-pull output stage as used in audio amplifiers.
 - BJT Q_n conducts during the positive half of the input cycle while Q_p conducts during the negative half-cycle.

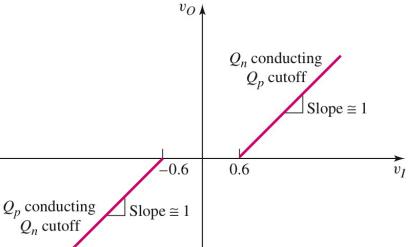
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Push-Pull Output Stage

(crossover distortion)

In the simple *complementary* push-pull output stage, the voltage transfer characteristics have a range of input voltage around 0 V where both transistors are cut-off and the output voltage is zero. $v_{O, \uparrow}$ $v_{O, \uparrow}$





From: Donald A. Neamen, *Microelectronics: Circuit Analysis & Design*, 4th

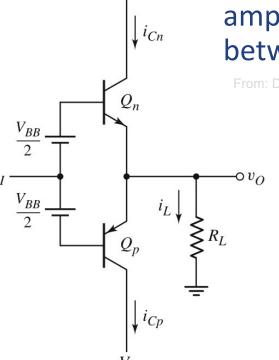
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Class AB Power Amplifiers

(crossover distortion elimination)

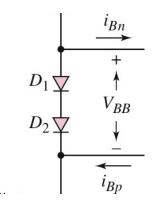
□ Crossover distortion in the *complementary* push-pull output stage can be avoided by applying a small bias voltage on each output transistor. V_{DD}

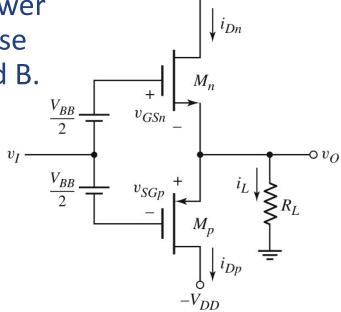


➤ This is the <u>class AB</u> power amplifier, a compromise between classes A and B.

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Classification of Power Amplifiers

(conduction angle less than 180° or π)

- ☐ In the classification of power amplifiers, it is helpful to distinguish the classes by the **conduction angle**.
 - > It is defined as the percentage of the signal period during which the output transistors conduct current multiplied by 360° (or 2π).
- □ The conduction angle is 360° (or 2π) for class A power amplifiers, 180° (or π) for class B, and slightly more than 180° (or π) for class AB.



Class C Power Amplifiers

(conduction angle less than)

- ☐ It can be seen in classes A and B power amplifiers that a smaller **conduction angle** gives higher **efficiency**.
- ☐ In class C power amplifiers, the output transistor is biased to conduct in less than half of the cycle.

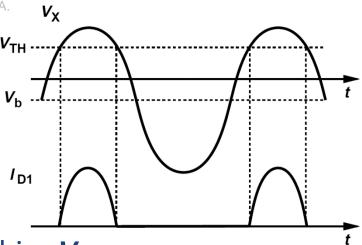
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RFC

Filtering/
Matching

RL

Vin



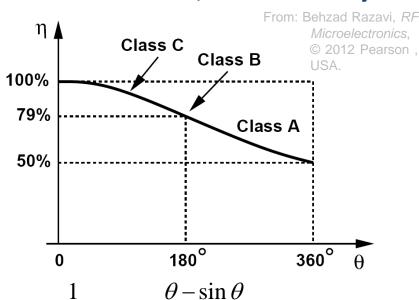
 \triangleright In the CS configuration, the bias V_b makes V_X above V_{TH} of the MOSFET for only a fraction of the cycle.



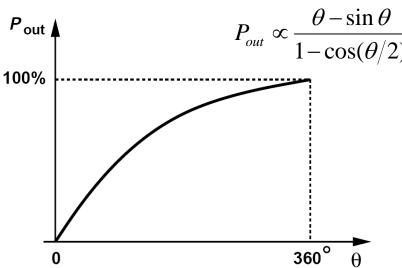
Power Amplifiers

(efficiency & conduction angle)

- ☐ As the transistor conducts current for a smaller fraction of the period, class C power amplifiers have higher **efficiency** than that of classes A and B.
 - > However, the **linearity** and **output power** become worse.



$$\eta = \frac{1}{4} \frac{\theta - \sin \theta}{\sin(\theta/2) - (\theta/2)\cos(\theta/2)}$$

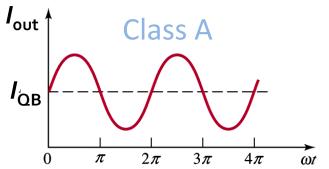


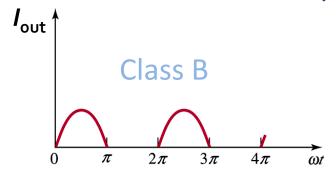


Classes A, B, AB & C

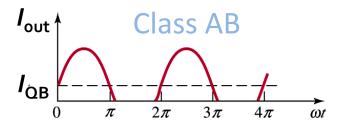
(same type but different conduction angle)

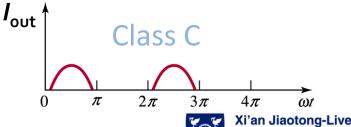
□ Power amplifiers of classes A, B, AB and C are of the same type with the difference in the quiescent biasing for the transistors to conduct in different fraction of the period.





From: Donald A. Neamen, *Microelectronics Circuit Analysis & Design*, 4th edition, © 2010 McGraw-Hill, USA.





Classes A, B, AB & C RF PAs

(sinusoidal waveform & power dissipation)

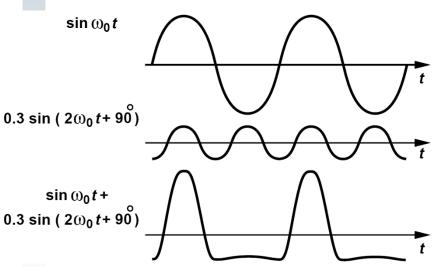
- ☐ Sinusoidal signals are assumed at the input and output in classes A, B, AB and C radio-frequency power amplifiers.
- ☐ If the current and/or voltage <u>waveforms</u> at the drain (or collector) are non-sinusoidal, the output transistors' power dissipation can be minimised.
 - ➤ The non-sinusoidal waveforms can minimise the time during which the output transistors conduct current while sustaining a large voltage.
 - > The power amplifier efficiency can then be raised by reducing power consumption of the output transistors.
 - Power = $V(t) \times I(t)$, where V(t) & I(t) are the output voltage and current of the transistor and may have different phases.



Non-Sinusoidal Waveforms

(harmonics arising from nonlinearity)

- ☐ With the large-signal amplification, the output transistor's current can swing by a large amount. As a result, there can always be some degree of **nonlinearity**.
- ☐ There can be second and/or third **harmonics** (<u>at</u> the drain) arising from large current signals.

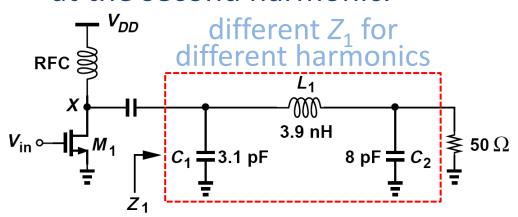


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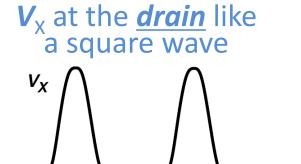
High-Efficiency Class A RF PA

(with harmonic enhancement)

- □ The waveform at the drain (or collector in the BJT case) of the output transistor can be shaped by the output matching network.
 From: Behzad Razavi, RF Microelectronics, © 2012 Pearson, USA.
- \square The **matching network** can be designed such that its input impedance Z_1 is low at the fundamental and high at the second harmonic.



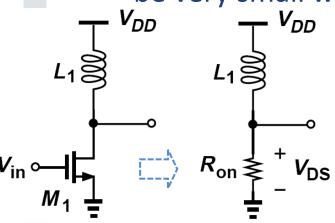
➤ In the signal delivered to the load, the harmonic content is not raised.

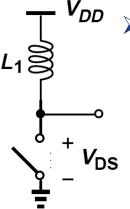




(transistor as a switch)

- ☐ To further improve the efficiency of RF power amplifiers, the output transistor can be used as a switch, instead of a voltage-controlled current source (in classes A, B, AB and C PAs).
 - ➤ Ideally, the output transistor turns on and off *abruptly*.
 - \succ To serve as a good **switch**, the on-resistance R_{on} should be very small when the transistor is turned on.





When it is off, it should be like open-circuit and hence no current flows through it.

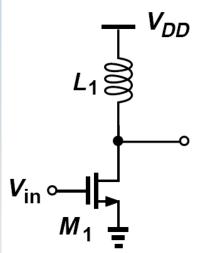
(switching RF PA)

- Such a circuit configuration using the transistor's switching action for power amplification is the class E power amplifier and it is called a "switching power amplifier".
 - ➤ It is a type of *nonlinear* amplifiers that achieve efficiency approaching 100% while delivering full power to the antenna.
 - > The perfect efficiency is based on the reasoning that an *ideal* **switch** dissipates no power: there is either zero voltage across it or zero current through it.
 - ➤ The switching amplifier version of the push-pull output stage is the class D PA.

(switching RF PA)

□ Such a "switching power amplifier" topology achieves a high efficiency if:

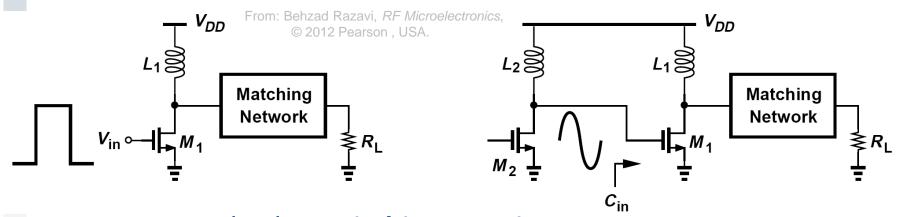
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- 1. the output transistor M_1 sustains a small voltage $V_{\rm DS}$ when it carries current;
- 2. M_1 carries a small current when it sustains a finite voltage;
- 3. the transition times between the on- and off-states are minimised.
- > It is like minimising power dissipation in the CMOS inverter.
- □ The input signal V_{in} for *switching* the transistor should approximate a <u>rectangular</u> waveform.

(switching RF PA)

- While the gate of the output MOSFET must be switched as abruptly as possible to maximise the efficiency, its typically large transistor size (W/L) in RF PA makes resonance necessary at the input.
 - > This inevitably gives rise a nearly sinusoidal waveform.

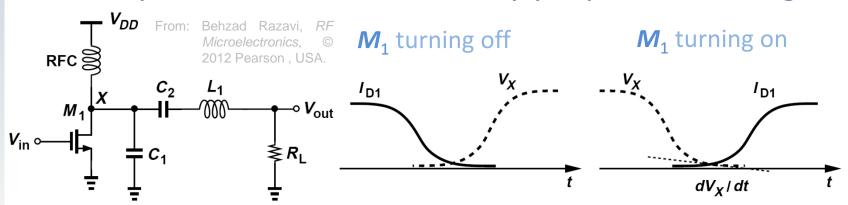


➤ As a result, the *switching* transistor is driven by a *gradual* waveform.



(finite input & output transition)

- ☐ The operation of <u>class E</u> power amplifiers in reality never has the perfect rectangular waveform to **switch** on and off the transistor **abruptly**.
- ☐ The finite input and output transition times in class E amplifiers can be handled by proper load design.

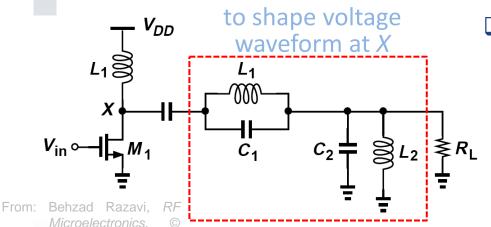


> The passive output network is designed such that I_{D1} drops to zero when V_x rises and vice versa.



(overcome issues of class E PA)

- ☐ The class E power amplifier has quite poor **power handling capability** as it results in a large peak current and voltage.
- Besides, the large drain current can degrade the **efficiency** as the transistor used as a **switch** has a non-zero on-resistance R_{on} .
- ☐ The **harmonic enhancement** technique for high-efficiency class A power amplifiers can be used in class E amplifiers.



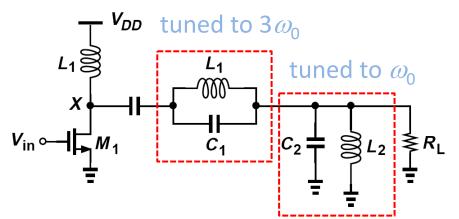
2012 Pearson, USA.

The output matching network can be used to shape the waveform at the drain. This is the class F PA.

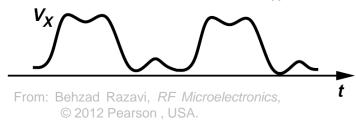


(harmonic enhancement)

□ In the generic switching stage, the passive output network can be designed (with LC tanks) to provide a high termination impedance for the second or third harmonics. The voltage waveform across the transistor as a switch then exhibits sharper edges than a sinusoid ⇒ square wave.



sharper edges with harmonic enhancement of V_X

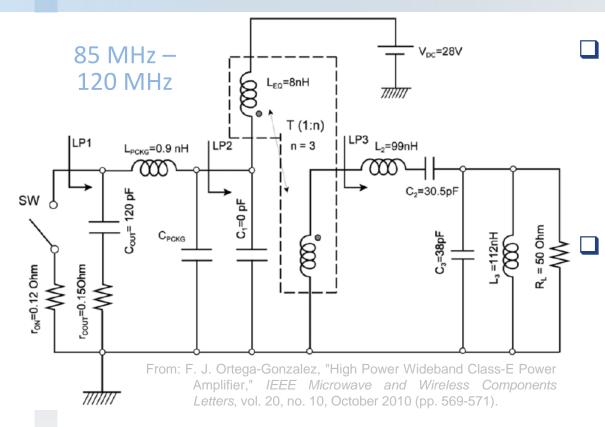


- > This reduces the power loss in the *switching* transistor.
- > Note the impedance is still seen from X as R_1 at the fundamental frequency ω_0 .



Wideband RF Power Amplifier

(passive output network)



- Do you know where the output transistor is?
- What type is the RF power amplifier?

- It can be seen about the importance of passive matching network in RF power amplifier design.
- In fact, passive **LC**circuits can be used
 for making an RF
 power amplifier for
 wideband
 amplification.



RF PA with Variable Gain

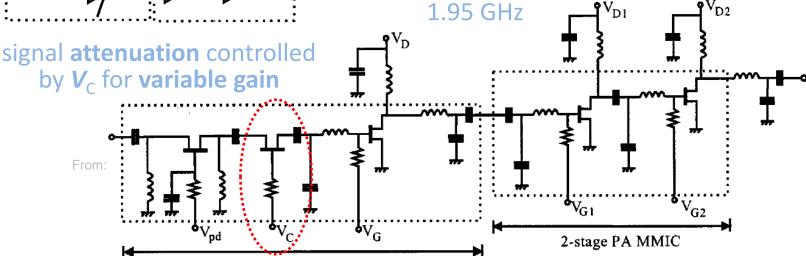
(pre-stage circuit driving output transistors)

□ RF power amplifiers can also be made with variable gain.

Linearized VGA MMIC PA MMIC

PD VGA PA PA

To avoid degrading the efficiency, the variable gain feature is introduced in the pre-stage circuit.



From: Gary Hau, Takeshi B. Nishimura, and Naotaka Iwata, "High Efficiency, Wide Dynamic Range Variable Gain and Power Amplifier MMICs for Wide-Band CDMA Handsets," *IEEE Microwave and Wireless Components Letters*, vol. 11, no. 1, January 2001 (pp. 13-15).

Linearized VGA MMIC

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