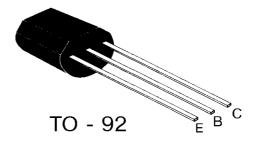
NPN Epitaxial Silicon Transistor

revision Oktober 1999

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PRE-AMPLIFIER, LOW LEVEL & LOW NOISE

- High total power dissipation (PT=450mW)
- High h_{FE} and good linearity



CLASSIFICATION her

Classification	Α	В	С	D
h _{FE}	60-150	100-300	200-600	400-1000

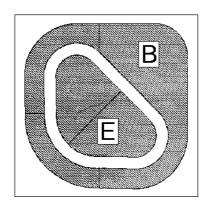
Absolute Maximum Ratings (Ta=25°C)

Symbol	Parameter	Rating	Unit
V_{CBO}	Collector-Base Voltage	50	V
V_{CEO}	Collector-Emitter Voltage	45	V
V_{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current	100	mA
P _C	Collector Dissipation	450	mW
T _i	Junction Temperature	150	0C
Tstg	Storage Temperature	-55 ÷ 150	0C

Electrical Characteristics (T_a = 25°C)

Liectifical O	ilaracieristics (r _a = 25°C)					
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 100 \mu A, I_{E} = 0$	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 1$ mA, $I_B = 0$	45			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5			V
I _{CBO}	Collector Cutoff Current	$V_{CB} = 50V, I_{E} = 0$			50	nA
I _{EBO}	Emitter Cutoff Current	$V_{EB}=5V$, $I_C=0$			50	nA
h _{FE}	DC Current Gain	V_{CE} =5V, I_{C} =1mA	60	280	1000	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 100 \text{mA}, I_B = 5 \text{mA}$		0.14	0.3	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_{C} = 100 \text{mA}, I_{B} = 5 \text{mA}$		0.84	1.0	V
V _{BE(on)}	Base-Emitter On Voltage	V_{CE} =5V, I_{C} =2mA	0.58	0.63	0.7	V
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_E = 0, f = 1MHz$		2.2	3.5	pF
f _T	Current Gain-Bandwidth Product	V_{CE} =5V, I_{C} =10mA	150	270		MHz
NF	Noise Figure	V_{CE} =5V, I_{C} =0.2mA f =1KHz, R_{S} =2K Ω		0.9	10	dB

Pad Location



 $\begin{array}{lll} \bullet & \text{DIE SIZE} & 350 \text{ X } 350 \mu\text{m} \\ \bullet & \text{DIE THICKNESS} & \text{Typ. 470 } \mu\text{m} \end{array}$

BONDING PAD SIZE

 $\begin{array}{ll} \text{Emitter} & 140 \text{ x } 140 \text{ } \mu\text{m} \\ \text{Base} & 110 \text{ x } 110 \text{ } \mu\text{m} \end{array}$