TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL TYPE (PCT PROCESS)

# 2 S C 3 7 0 9 A

#### HIGH CURRENT SWITCHING APPLICATIONS

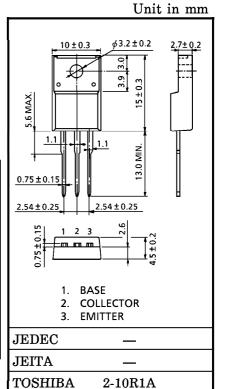
• Low Collector Saturation Voltage : V<sub>CE</sub> (sat)=0.4V (Max.)

• High Speed Switching Time :  $t_{stg} = 1.0 \mu s$  (Typ.)

Complementary to 2SA1451A

### MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Collector-Base Voltage	$v_{CBO}$	60	V	
Collector-Emitter Voltage	$v_{CEO}$	50	V	
Emitter-Base Voltage	$V_{ m EBO}$	6	V	
Collector Current	$I_{\mathbf{C}}$	12	A	
Base Current	$I_{\mathbf{B}}$	2	A	
Collector Power Dissipation	$P_{\mathbf{C}}$	30	w	
$(Tc = 25^{\circ}C)$	10	30	**	
Junction Temperature	$\mathrm{T_{j}}$	150	°C	
Storage Temperature Range	$ m T_{stg}$	-55~150	°C	



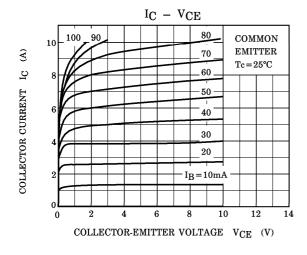
Weight: 1.7g (Typ.)

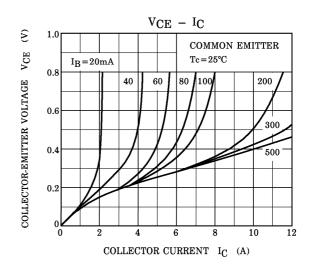
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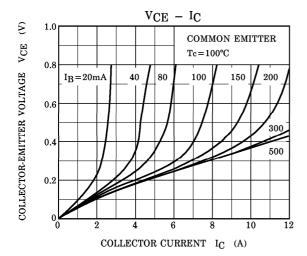
## ELECTRICAL CHARACTERISTICS (Tc = 25°C)

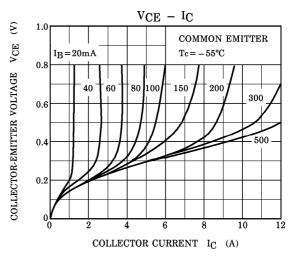
CHARAC	CTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-	off Current	$I_{CBO}$	$V_{CB} = 60V, I_{E} = 0$		_	10	$\mu$ A
Emitter Cut-o	ff Current	$I_{\mathrm{EBO}}$	$V_{EB}=6V$ , $I_{C}=0$	_	_	10	$\mu$ A
Collector-Emit Voltage	tter Breakdown	V (BR) CEO	$I_{\rm C} = 50 {\rm mA}, \ I_{\rm B} = 0$	50	_	_	V
DC Current Gain		h <sub>FE (1)</sub> (Note)	$V_{\text{CE}}=1V$ , $I_{\text{C}}=1A$	70	_	240	
	h <sub>FE (2)</sub>		$V_{CE}=1V$ , $I_{C}=6A$	40	_	_	
Collector-Emit Voltage	tter Saturation	V <sub>CE</sub> (sat)	$I_{C}=6A, I_{B}=0.3A$	_	0.25	0.4	v
Base-Emitter Voltage	Saturation	V <sub>BE</sub> (sat)	$I_{\rm C}$ =6A, $I_{\rm B}$ =0.3A	_	0.9	1.2	V
Transition From	equency	$ m f_{T}$	$V_{CE}=5V, I_{C}=1A$	_	90	_	MHz
Collector Output Capacitance		$C_{ m ob}$	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$		180	_	pF
Switching Time	Turn-on Time	$t_{ m on}$	$I_{B1} = I_{B2} = 0.3A,$ $DUTY CYCLE \le 1\%$ $I_{B1} = 0.3A$ $V_{CC} = 30V$ $V_{CC} = 30V$		0.2	_	
	Storage Time	$ m t_{stg}$		_	1.0	_	$\mu$ s
	Fall Time	$t_f$		_	0.2	_	

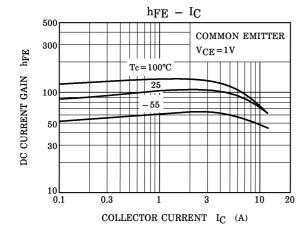
(Note) hFE (1) Classification O : 70~140, Y : 120~240

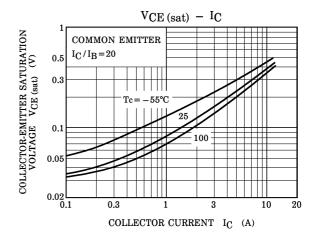




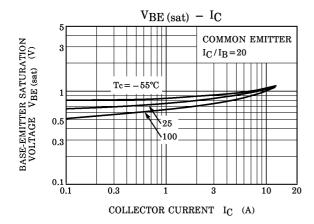


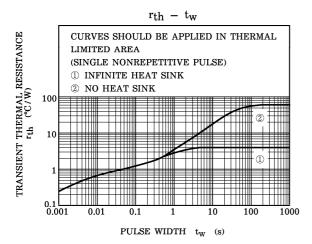


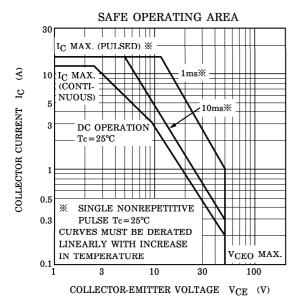


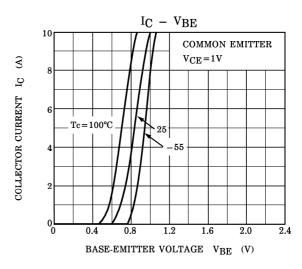


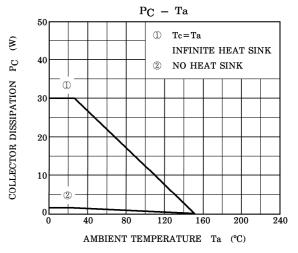
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