
2SC2732

Silicon NPN Epitaxial

HITACHI

ADE-208-1072 (Z)

1st. Edition

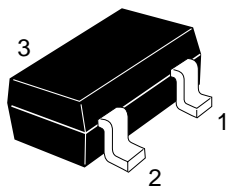
Mar. 2001

Application

UHF frequency converter

Outline

MPAK



- 1. Emitter
- 2. Base
- 3. Collector

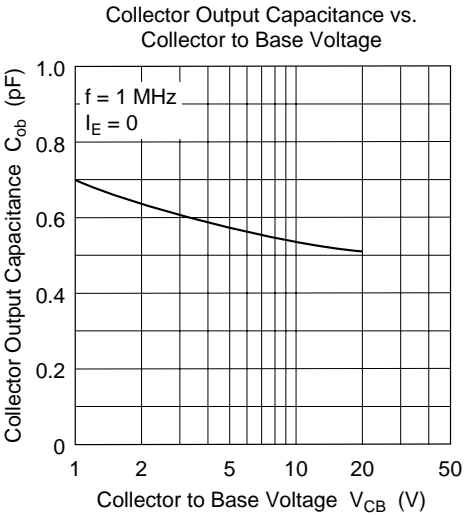
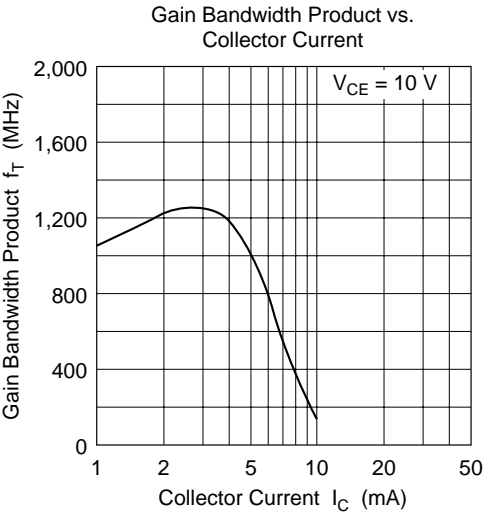
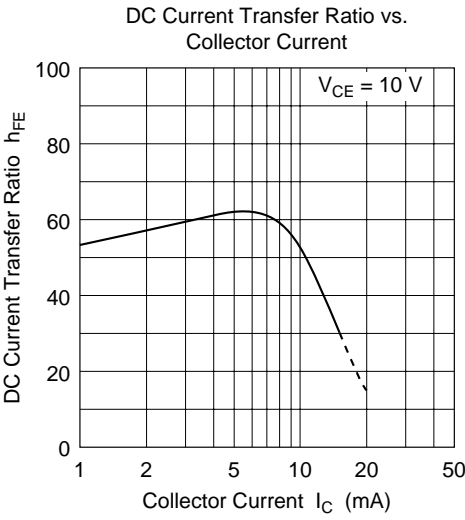
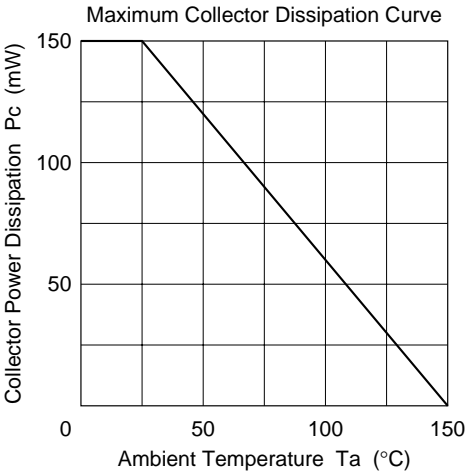
Note: Marking is "EC".

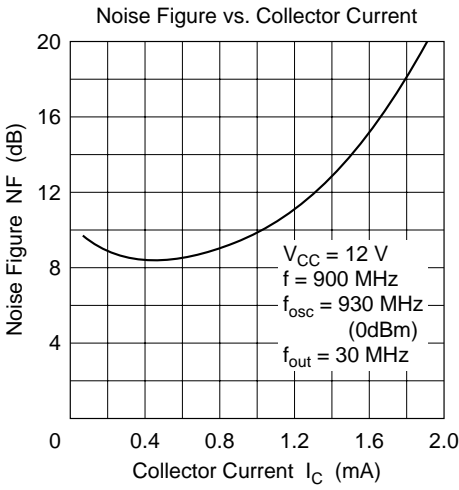
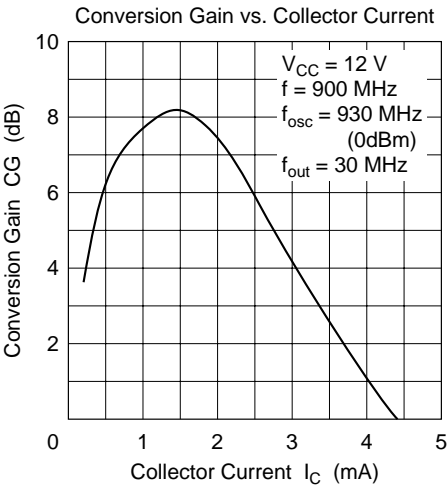
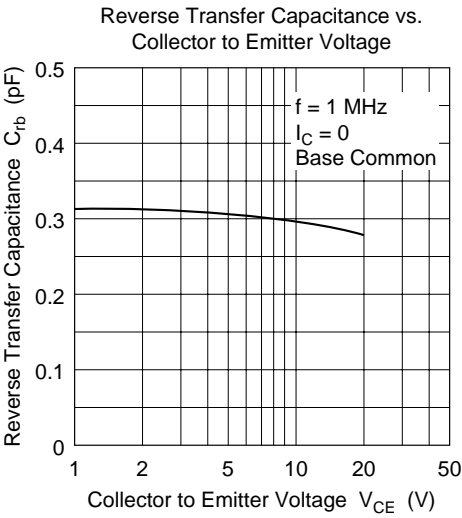
Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Item	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	30	V
Collector to emitter voltage	V_{CEO}	25	V
Emitter to base voltage	V_{EBO}	4	V
Collector current	I_{C}	20	mA
Collector power dissipation	P_{C}	150	mW
Junction temperature	T_{j}	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

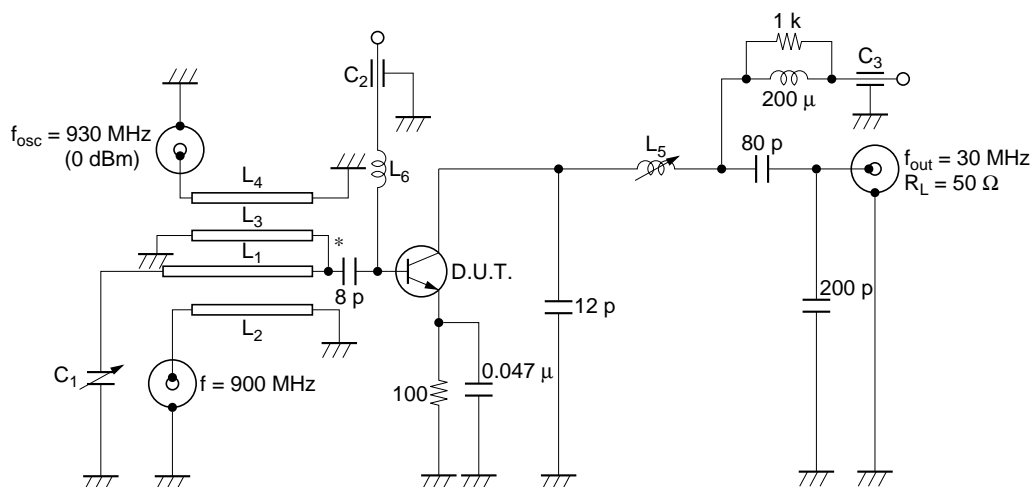
Electrical Characteristics ($T_a = 25^\circ\text{C}$)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(\text{BR})\text{CBO}}$	30	—	—	V	$I_{\text{C}} = 10\text{ }\mu\text{A}$, $I_{\text{E}} = 0$
Collector to emitter breakdown voltage	$V_{(\text{BR})\text{CEO}}$	25	—	—	V	$I_{\text{C}} = 1\text{ mA}$, $R_{\text{BE}} = \infty$
Emitter to base breakdown voltage	$V_{(\text{BR})\text{EBO}}$	4	—	—	V	$I_{\text{E}} = 10\text{ }\mu\text{A}$, $I_{\text{C}} = 0$
Collector cutoff current	I_{CBO}	—	—	0.5	μA	$V_{\text{CB}} = 10\text{ V}$, $I_{\text{C}} = 0$
Collector to emitter saturation voltage	$V_{\text{CE}(\text{sat})}$	—	—	5	V	$I_{\text{C}} = 10\text{ mA}$, $I_{\text{B}} = 1\text{ mA}$
DC current transfer ratio	h_{FE}	30	60	—		$V_{\text{CE}} = 10\text{ V}$, $I_{\text{C}} = 3\text{ mA}$
Gain bandwidth product	f_{T}	700	1000	—	MHz	$V_{\text{CE}} = 10\text{ V}$, $I_{\text{C}} = 5\text{ mA}$
Collector output capacitance	C_{ob}	—	0.5	0.8	pF	$V_{\text{CB}} = 10\text{ V}$, $I_{\text{E}} = 0$, $f = 1\text{ MHz}$
Conversion gain	CG	—	7.0	—	dB	$V_{\text{CC}} = 12\text{ V}$, $I_{\text{C}} = 1\text{ mA}$, $f = 900\text{ MHz}$, $f_{\text{OSC}} = 930\text{ MHz (0dBm)}$, $f_{\text{out}} = 30\text{ MHz}$
Noise figure	NF	—	10.0	—	dB	$V_{\text{CC}} = 12\text{ V}$, $I_{\text{C}} = 1\text{ mA}$, $f = 900\text{ MHz}$, $f_{\text{OSC}} = 930\text{ MHz (0dBm)}$, $f_{\text{out}} = 30\text{ MHz}$





Conversion Gain, Noise Figure Test Circuit

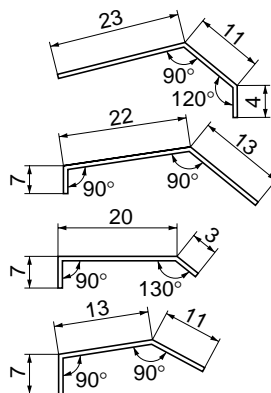


*.....Disk Capacitor

Unit R : Ω

C : F

L : H

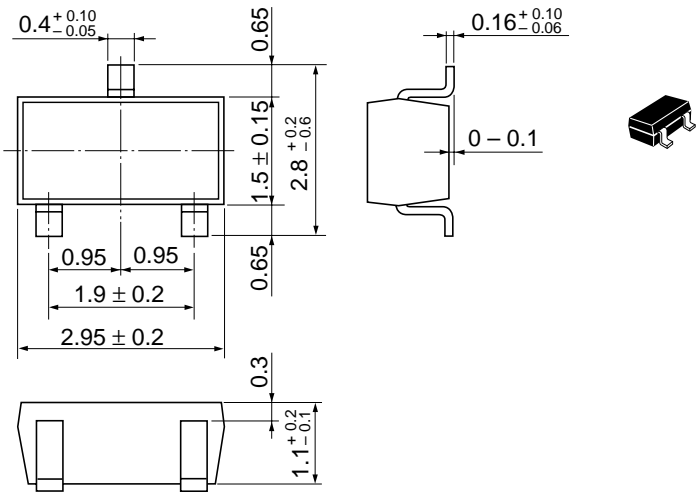
 L_1 : $\phi 1$ mm Enameled Copper wire L_2 : $\phi 1$ mm Enameled Copper wire L_3 : $\phi 1$ mm Enameled Copper wire L_4 : $\phi 1$ mm Enameled Copper wire

Unit : mm

 L_5 : Bobbin $\phi 0.5$ mm inside dia, $\phi 0.2$ mm Enameled Copper wire 20 Turns L_6 : $\phi 5$ mm Enameled Copper wire 1 Turns inside dia $\phi 6$ mm C_1 : 20 pF max. Air Trimmer Condenser C_2, C_3 : 1000 pF Air Core Capacitor

Package Dimensions

As of January, 2001
Unit: mm



Hitachi Code	MPAK
JEDEC	—
EIAJ	Conforms
Mass (reference value)	0.011 g

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