# TOSHIBA INTEGRATED CIRCUIT

TA 31142F/FN

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT
SILICON MONOLITHIC

FM IF DETECTION IC FOR PAGER.
(Built-in 2nd MIXER)

### TENTATIVE

TA31142F

TA31142FN

- Built-in 2nd MIXER for Duble Conversion methode.
   Mixer Operating Frequency: 10~50MHZ.
- Built-in low-pass filter and waveform shaping circuit enable the extraction of FSK signals from voice signal.
- · High transmit rate (1200bps)
- Built-in battery-saving function can reduce the load upon the battery which is functioning as power supply.
- Alarm function (ALM)
   Alarm sensitivity

: VALM=1.1V(TYP.)

 Constant voltage power supply can be fabricated through externally adding a transistor.

: VREG=1. OV (TYP. )

· Extremely low consumption current

: Iccg=1.1mA (TYP.)

· Power supply voltage

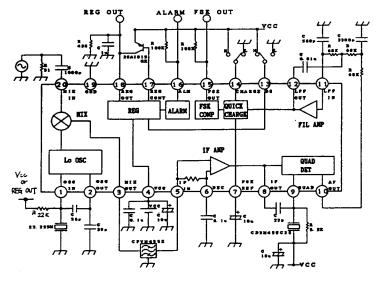
: VCC=1.1~3.5V

· Small package

: SSOP20PIN (1.00mm) SSOP20PIN (0.65mm) .

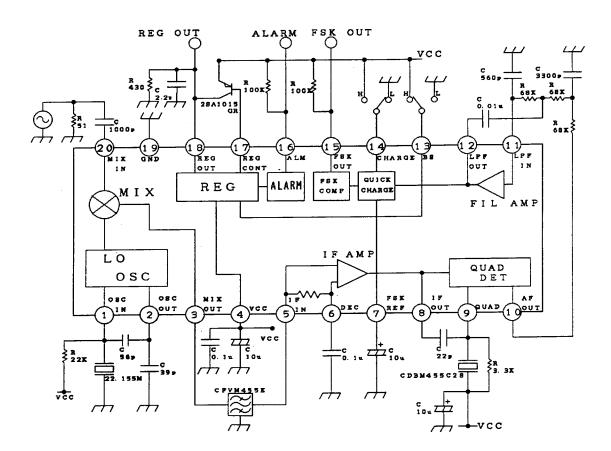
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### BLOCK DIAGRAM



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(T1A1232)

## TOSHIBA INTEGRATED CIRCUIT

#### MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC	SYMBOL	RATING 4		UNIT
Power Supply Voltage	Vcc			V
Power Dissipation	PD	F		■W
		FN		
Operating Temperature	Торг	0~45		<b>S</b>
Strage temperatur	Tstg	-55~150		T

### ELECTRICAL CHARACTERISTICS

(Unless otherwise specified Vcc=1.4V, Fin (MIX) =21.7MHZ, Fin (IF) =455KHZ, Fdev=±4KHz, Fm=600Hz, Ta=25C

CHARACTERISTICS	SYMBOL	TEST	TEST CONDITION	MIN	TYP	MAX	UNIT
Quiescent Current	Icc q				1.1	1.6	жA
Supply Current at	Icco			-	0	5	μA
Battery Saving							
MIXER Conversion Gain	GMV		FILTER LOSS=-1dB		11		dB
MIXER 3rd Interseptpoint	IP				-10	-	dB∎
MIXER Input Resistance	RMin			-	5		Kυ
IF Amp Input Resistance	RIFi			-	2		KΩ
Signal to Noise Ratio 1	S/N1		MIX IN, VIN (MIX) =60dBuEMF	-	55	-	dB
Signal to Noise Ratio 2	S/N2		IF IN, VIN (IF)=60dBuEMF	-			dB
Signal to Noise Ratio 3	S/N3		IF IN, VIN (IF) =20dBuENF	_			dB
-3dB Limitting Sensitivity 1	Vi (LIM) 1		MIX INPUT	-	14	-	dB # EMF
-3dB Limitting Sensitivity 2	Vi (LIM) 2		IF INPUT	-	22		dB $\mu$ EMF
Detected Output Level	VOD		VIN (IF) =60dBuEMF		45		nVrms
AM Rejection Ratio	AXIR		VIN (IF) =60dBuEMF, AM=30%	-	40	<u> </u>	dB
FSK OUT Duty Ratio	DR	Ī	VIN (IF) =60dBuEMF		50		%
Alarm detected Voltage	VALN			1. 05	1.1	1.15	V
"L" Level Output Voltage (ALM)	VALML		I=100 #A	-	-	0.4	V
"H" Level Output Current (ALM)	IALMH			-	-	2	μA
"L" Level Output Voltage (FSK)	VFSKL		I=100 µ Å	-	-	0.4	V
"H" Level Output Current (FSK)	IFSKH	1		-	-	2	μA
Constant voltage Output	VREG	<b>1</b>		0. 95	1.0	1. 05	V
Quick charge discharge Current	Io		V7=0V,		70		μA

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