NEC

NPN SILICON TRANSISTOR 2SC2784

DESCRIPTION

The 2SC2784 is the best for use as the middle range amplifier in Hi-Fi stereo control amplifiers, power amplifiers, and etc.

FEATURES

ullet High voltage. V_{CEO} : 120 V

• Low output capacitance. C_{ob} : 1.6 pF TYP. (V_{CB} = 30 V)

High h_{FE}

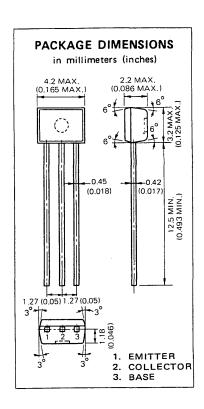
 h_{FE} : 600 TYP. ($V_{CE} = 6.0 \text{ V}, I_{C} = 1.0 \text{ mA}$)

• Super low noise. NV: 25 mV TYP. (See test circuit.)

• Complementary to the NEC 2SA1174 PNP transistor.

ABSOLUTE MAXIMUM RATINGS

Maximum Temperatures	
Storage Temperature	–55 to +125 °C
Junction Temperature +12	25 °C Maximum
Maximum Power Dissipation (Ta = 25 °C)	
Total Power Dissipation	300 mW
Maximum Voltages and Currents (Ta = 25 °C)	
V _{CBO} Collector to Base Voltage	120 V
V _{CEO} Collector to Emitter Voltage	
V _{EBO} Emitter to Base Voltage	
I _C Collector Current	
I _B Base Current	10 mA
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ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

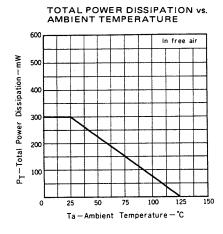
SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
hFE1	DC Current Gain	150	580		_	V _{CE} =6.0 V, I _C =0.1 mA
h _{FE2}	DC Current Gain	200	600	1200	_	V_{CE} =6.0 V, I_{C} =1.0 mA
fT	Gain Bandwidth Product	50	110		MHz	V _{CE} =6.0 V, I _E =1.0 mA
c _{ob}	Output Capacitance		1.6	2.5	рF	V _{CB} =30 V, I _E =0, f=1.0 MHz
NV	Noise Voltage	•	25	40	mV	V_{CE} =5.0 V, I_{C} =1.0 mA, R_{G} =100 k Ω
	•		•			G_V =80 dB, f=10 Hz to 1.0 kHz
Ісво	Collector Cutoff Current			50	nΑ	V _{CB} =120 V, I _E =0
IEBO	Emitter Cutoff Current			50	nΑ	V _{EB} =5.0 V, I _C =0
V _{BE}	Base to Emitter Voltage	0.55	0.59	0.65	V	V_{CE} =6.0 V, I_{C} =1.0 mA
V _{CE(sat)}	Collector Saturation Voltage		0.07	0.30	V	IC=10 mA, IB=1.0 mA

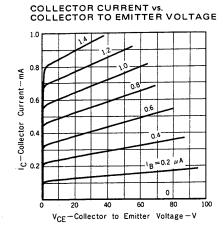
Classification of hFE2

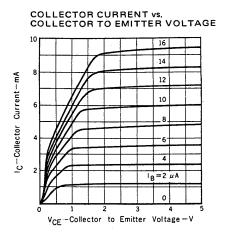
Rank	Р	F	E	U
Range	200 – 400	300 600	400 – 800	600 - 1200

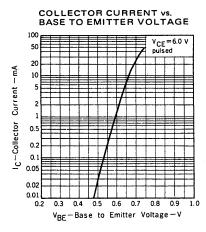
 h_{FE2} Test Conditions : $V_{CE} = 6.0$ V, $I_{C} = 1.0$ mA

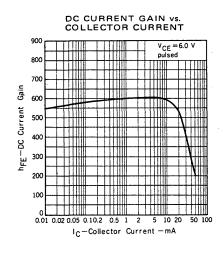
TYPICAL CHARACTERISTICS (Ta = 25 °C unless otherwise noted)

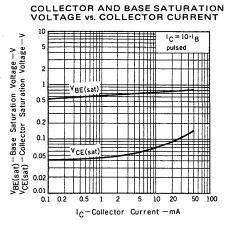


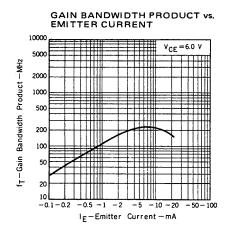


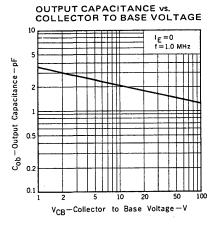


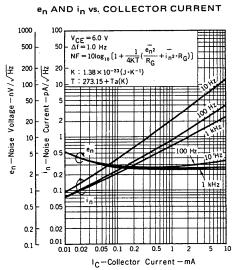


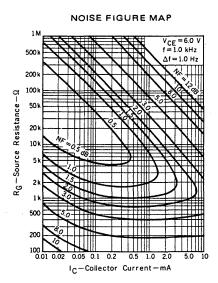




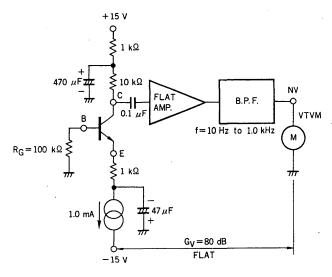








NOISE VOLTAGE TEST CIRCUIT



 $\mbox{$V_{CE}$$\stackrel{..}{=}$} 5 \mbox{$V$, $I_{C}$$} = 1.0 \mbox{ mA, } \mbox{$R_{G}$$} = 100 \mbox{ k}\Omega, \mbox{$G_{V}$$} = 80 \mbox{ dB, } \mbox{$FLAT(f=10$ Hz to 1.0 kHz)$} \label{eq:center}$