

50V/5A Switching Applications

Applications

· Relay drivers, high-speed inverters, converters.

Features

- · Low collector-to-emitter saturation voltage.
- · High f_T.
- · Excellent linearity of hFE.
- · Fast switching time.

(): 2SB1165

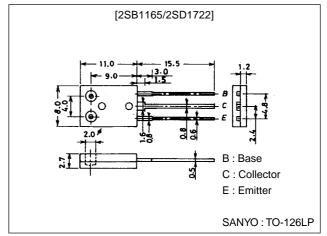
Specifications

Absolute Maximum Ratings at Ta = 25°C

Package Dimensions

unit:mm

2043A



Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-)60	V
Collector-to-Emitter Voltage	V _{CEO}		(-)50	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	Ic		(–)5	Α
Collector Current (Pulse)	ICP		(–)8	Α
Collector Dissipation	PC		1.2	W
		Tc=25°C	20	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Collector Cutoff Current	I _{CBO}	V _{CB} =(-)40V, I _E =0			(-)1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)1	μA
DC Current Gain	h _{FE} 1	V _{CE} =(-)2V, I _C =(-)0.5A	70*		400*	
	h _{FE} 2	V _{CE} =(-)2V, I _C =(-)4A	35			
Gain-Bandwidth Product	f _T	V _{CE} =(-)5V, I _C =(-)1A		180		MHz
				(130)		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		40(60)		pF

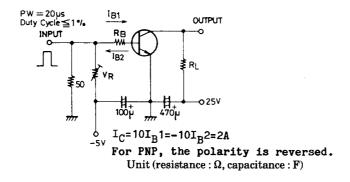
 \ast : The 2SB1165/2SD1722 are classified by 0.5A h_{FE} as follows :

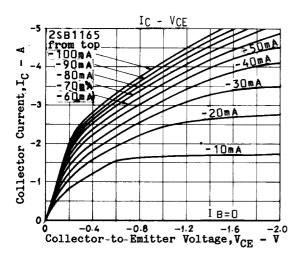
70 Q 140 100 R 200 140 S 280 200 T 400

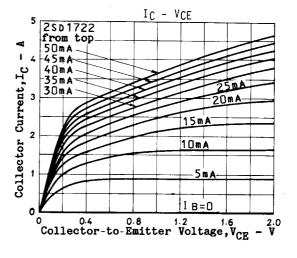
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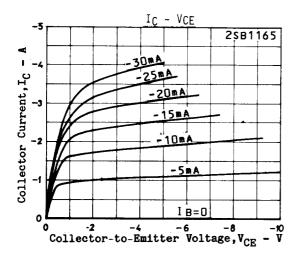
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)3A, I _B =(-)0.15A		220	400	mV
				(-280)	(-550)	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =(-)3A, I _B =(-)0.15A		(-)0.95	(–)1.3	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =(-)10μA, I _E =0	(–)60			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =(-)1mA, R _{BE} =∞	(-)50			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	$I_{E}=(-)10\mu A, I_{C}=0$	(–)6			V
Turn-ON Time	ton	See specified Test Circuit		(50)50		ns
Storage Time	t _{stg}	See specified Test Circuit		500		ns
				(450)		ns
Fall Time	t _f	See specified Test Circuit		20(20)		ns

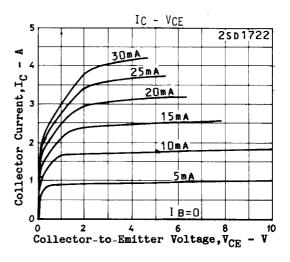
Switching Time Test Circuit

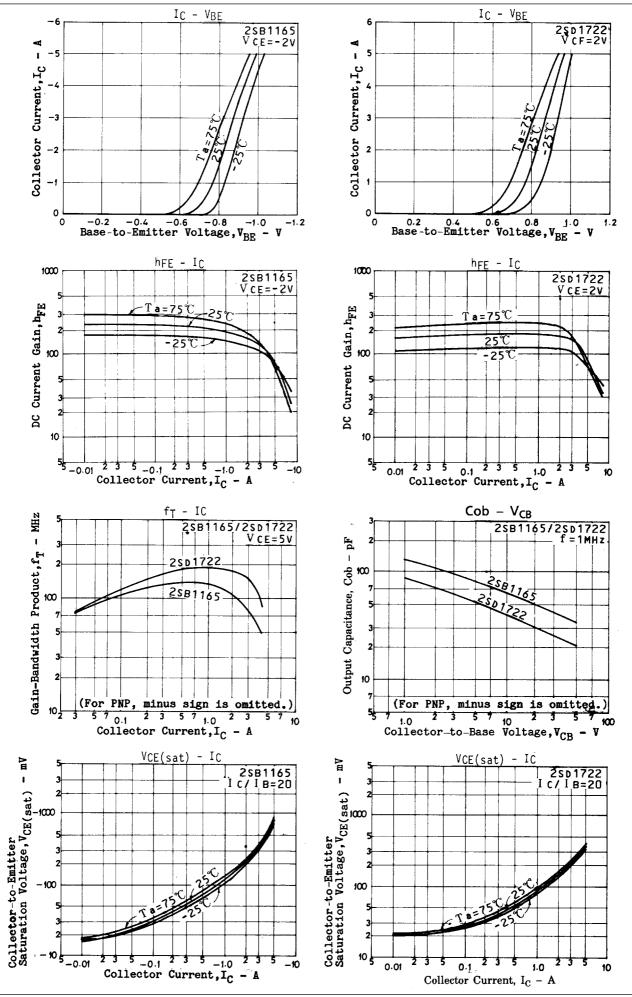


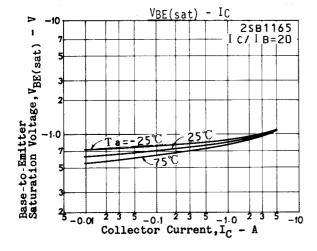


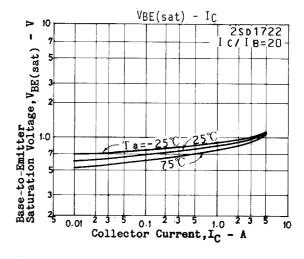


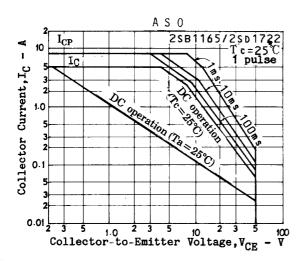


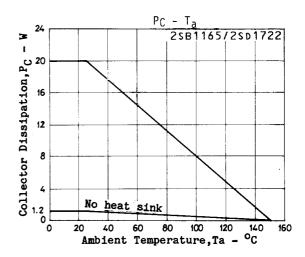












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