Ordering number:ENN3534

Monolithic Linear IC

LA4571MB

Low-voltage Headphone Amplifier for Stereo Audio

Overview

The LA4571MB is a low-voltage, stereo headphone amplifier incorporating both tape head preamplifiers and headphone power amplifiers in a single chip, making it ideal for portable battery-powered equipment. It features logic-level controlled output signal muting, excellent noise characteristics and easy interconnection with signal sources, such as an AM/FM tuner IC.

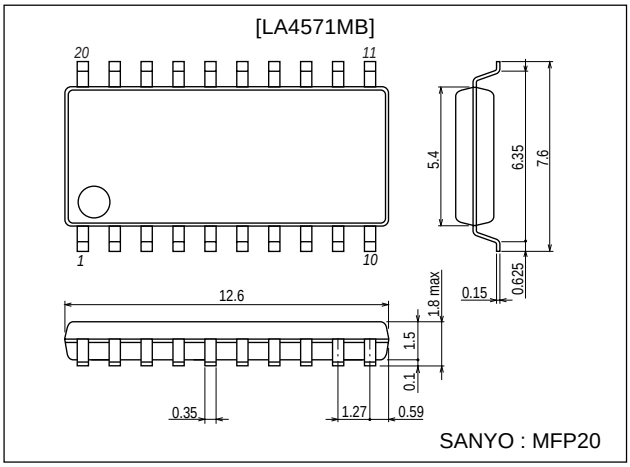
The LA4571MB requires no input or output coupling capacitors. A buffer amplifier with 10Ω output impedance reduces the size of the virtual-earth decoupling capacitor. The preamplifier and power amplifier inputs only require the addition of an external capacitor to provide high-frequency noise filtering.

The LA4571MB operates from a 3V supply and is available in 20-pin MFPs.

Package Dimensions

unit:mm

3036B-MFP20



Features

- Stereo tape head preamplifiers and headphone power amplifiers on chip.
- Output signal muting.
- Low noise.
- 8Ω speaker driver.
- 3V supply.
- 20-pin MFP.

Specifications

Maximum Ratings at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V_{CC} max		4.5	V
Allowable power dissipation	Pd max		400	mW
Operating temperature	Topr		–20 to +75	˚C
Storage temperature	Tstg		–40 to +125	˚C

Recommended Operating Conditions at Ta = 25˚C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V_{CC}		3.0	V
Supply voltage range	V_{CC} op		1.8 to 3.6	V

SANYO Electric Co.,Ltd. Semiconductor Company

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

Electrical Characteristics

Preamplifier and power amplifier at Ta = 25˚C, V_{CC}=3.0V, f=1kHz, R_L (pre)=10kΩ, R_L (power)=16Ω, 0dBm at 0.775V unless otherwise noted

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent supply current	I_{CCO}	Rg=2.2kΩ, V_I=0V		17	27	mA
Total voltage gain	VG_T	V_O=–5dBm	65	68	71	dB

Preamplifier at Ta = 25˚C, V_{CC}=3.0V, f=1kHz, R_L (pre)=10kΩ, R_L (power)=16Ω, 0dBm at 0.775V unless otherwise noted

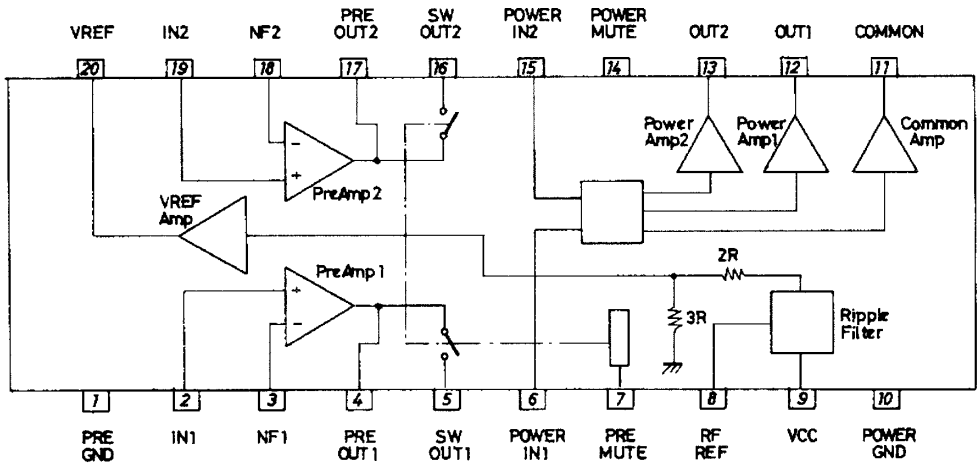
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Open-loop voltage gain	VG_O	V_O=–5dBm	70	80		dB
Closed-loop voltage gain	VG_1	V_O=–5dBm		40		dB
Maximum output voltage	V_O max	THD=1%, V_{CC}=1.8V	0.1	0.2		V
Total harmonic distortion	THD_1	V_O=0.2V, VG=40dB (NAB standard)		0.05	0.5	%
Input conversion noise voltage	V_{NI}	Rg=2.2kΩ, bandwidth=20Hz to 20kHz		1.3	2.0	μV
Channel crosstalk	CT_1	Rg=2.2kΩ, 1kHz tune	60	80		dB
Ripple rejection	R_{r1}	Rg=2.2kΩ, V_{CC}=1.8V, V_r=–20dBm, f=100Hz	40	50		dB

Power amplifier at Ta = 25˚C, V_{CC}=3.0V, f=1kHz, R_L (pre)=10kΩ, R_L (power)=16Ω, 0dBm at 0.775V unless otherwise noted

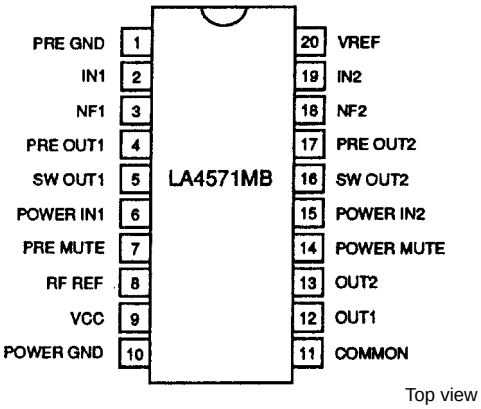
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Output power	P_O	THD=10%	23	32		mW
Closed-loop voltage gain	VG_2	V_O=–5dBm	25	28	31	dB
Total harmonic distortion	THD_2	P_O=1mW		0.4	1.0	%
Channel crosstalk	CT_T	V_O=–5dBm, R_V=0Ω	30	40		dB
Output noise voltage	V_{NO}	Rg=0Ω, BPF=20Hz to 20kHz		24	40	μV
Ripple rejection	R_{r2}	Rg=0Ω, V_r=–20dB, f=100Hz, V_{CC}=1.8V	45	60		dB
Input resistance	R_i		22	30	38	kΩ
Output DC offset voltage	$V_{ODC\ off}$		–90		+90	mV

Note : The maximum and minimum values for the power amplifiers closed-loop voltage gain (VG2) increase by 1dB when the load resistance (R_L) is 32Ω.

Block Diagram



Pin Assignment

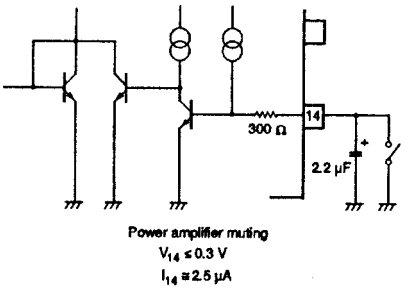


Top view

Pin Description

Number	Name	Description
1	PRE GND	Preamplifier ground
2	IN1	Channel 1 preamplifier input
3	NF1	Channel 1 preamplifier negative feedback input
4	PRE OUT1	Channel 1 preamplifier output
5	SW OUT1	Channel 1 preamplifier mute-control switched output RIN ≥500kΩ
6	POWER IN1	Channel 1 power amplifier input. R_{IN}≅30kΩ
7	PRE MUTE	Preamplifier mute control
8	RF REF	Ripple-filter capacitor connection
9	V_{CC}	Supply voltage
10	POWER GND	Power amplifier ground
11	COMMON	Common amplifier output
12	OUT1	Channel 1 power amplifier output
13	OUT2	Channel 2 power amplifier output
14	POWER MUTE	Power amplifier mute control
15	POWER IN2	Channel 2 power amplifier input. R_{IN}≅30kΩ
16	SW OUT2	Channel 2 preamplifier mute-control switched output. R_{IN} ≥500kΩ
17	PRE OUT2	Channel 2 preamplifier output
18	NF2	Channel 2 preamplifier negative feedback input
19	IN2	Channel 2 preamplifier input
20	VREF	Referecne-voltage amplifier output. I_{max}=±500μA

The internal circuit of the POWER MUTE pin is shown in the following figure.



The power amplifier input pin, POWER IN1, is biased at 1.8V, and the output pins at 1.2V

Design Notes

The preamplifier inputs should be connected to V_{REF} through a 2.2kΩ resistor if there is no tape head input signal source.

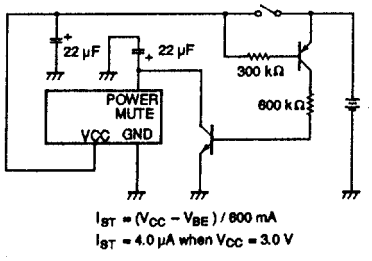
The mute release time capacitor of the power amplifier should be between 1.0 and 4.7µF. For V_{CC}=3.0V and C=2.2µF, the mute release time is 0.7s.

The ripple rejection ratio setting capacitor should be between 2.2 and 33µF. For 2.2µF, the ripple rejection ratio is 35dB, and for 22µF, it is 55dB.

When the output amplifier turns OFF, the protection circuit shown in the following figure detects the falling supply voltage and then mutes the power amplifier to protect the device.

Reference Voltage

The input to the voltage reference amplifier is a voltage divided level from the supply voltage ripple filter. The referecne voltage is given by $0.6 \times V_{CC}$. The supply voltage ripple filter requires the connection of an external filter capacitor to RF REF. A large capacitance results in a high ripple rejection ratio. Noise filtering is achieved by the addition of a single capacitor to VREF. Since the reference voltage amplifier has a buffered output, this capacitor can be as low as 1µF.



Sample Application Circuit