TOSHIBA TRANSISTOR SILICON PNP EPITAXIAL (PCT PROCESS)

# 2 S A 1 2 9 8

### LOW FREQUENCY POWER AMPLIFIER APPLICATION

#### **POWER SWITCHING APPLICATIONS**

• High DC Current Gain :  $h_{FE} = 100 \sim 320$ 

• Low Saturation Voltage :  $V_{CE (sat)} = -0.4 \text{ V (Max.)}$ 

 $(I_C = -500 \text{ mA}, I_B = -20 \text{ mA})$ 

• Suitable for Driver Stage of Small Motor

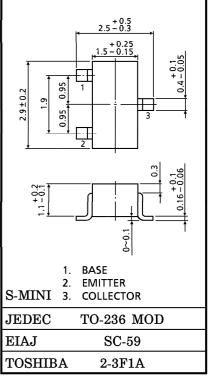
• Complementary to 2SC3265

• Small Package

#### MAXIMUM RATINGS (Ta = 25°C)

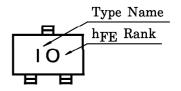
CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	$v_{CBO}$	-30	V
Collector-Emitter Voltage	$v_{CEO}$	-25	V
Emitter-Base Voltage	$V_{ m EBO}$	-5	V
Collector Current	$I_{\mathbf{C}}$	-800	mA
Base Current	$I_{\mathbf{B}}$	-160	mA
Collector Power Dissipation	$P_{\mathbf{C}}$	200	mW
Junction Temperature	$T_j$	150	°C
Storage Temperature Range	$T_{ m stg}$	-55~150	°C

Unit in mm



Weight: 0.012 g

Marking

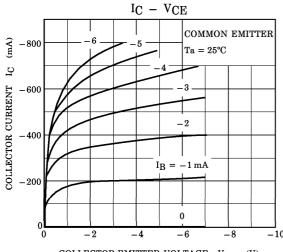


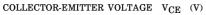
## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

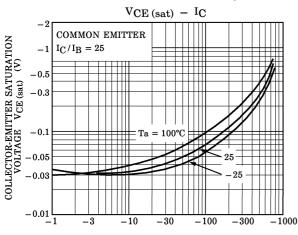
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	ICBO	$V_{CB} = -30 \text{ V}, I_{E} = 0$	_	_	-0.1	$\mu$ <b>A</b>
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = -50 \text{ V}, I_{C} = 0$	_	_	-0.1	$\mu$ <b>A</b>
Collector-Emitter Breakdown Voltage	V (BR) CEO	$I_{\rm C} = -10  {\rm mA}, \; I_{\rm B} = 0$	-25	_	_	V
Emitter-Base Breakdown Voltage	V (BR) EBO	$I_{\mathrm{E}}=-0.1\mathrm{mA},~I_{\mathrm{C}}=0$	-5	_	_	V
DC Current Gain	hFE (1) (Note)	$V_{ m CE} = -1   m V,  I_{ m C} = -100  mA$	100	_	320	
	h <sub>FE</sub> (2)	$V_{CE} = -1  V,  I_{C} = -800  mA$	40	_	_	
Collector-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	$I_{\rm C} = -500  {\rm mA},  I_{\rm B} = -20  {\rm mA}$	_	_	-0.4	V
Base-Emitter Voltage	$ m v_{BE}$	$V_{CE} = -1  V,  I_{C} = -10  mA$	-0.5	_	-0.8	V
Transition Frequency	${ m f_T}$	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	_	120	_	MHz
Collector Output Capacitance	$C_{ob}$	$V_{CB} = -10 V, I_{E} = 0,$ f = 1 MHz	_	13	_	pF

Note : hFE (1) Classification O : 100~200, Y : 160~320

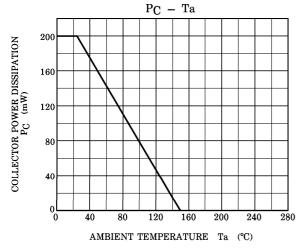
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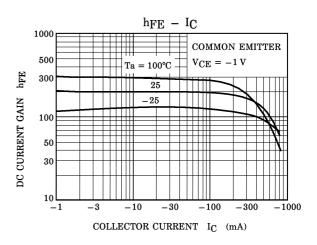


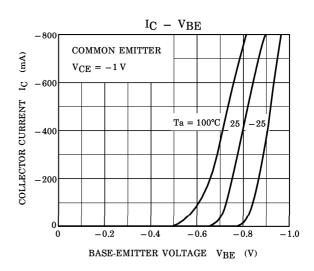




 $\begin{array}{cccc} \text{Collector current} & \text{I}_{C} & \text{(mA)} \end{array}$ 







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