

Class AB Stereo Headphone Driver

Features

Operating Voltage

Single Supply 3V to 7V Dual Supply ± 1.5 V to ± 3.5 V

High Signal-to-Noise Ratio
100dB

High Slew Rate
5V/ μs

Low Distortion -65dB

Large Output Voltage Swing

Excellent Power Supply Ripple Rejection

Low Power Consumption

Short-circuit Elimination

Wide Temperature Range

No Switch ON/OFF Clicks

• Available in 8 pin SOP or DIP Package

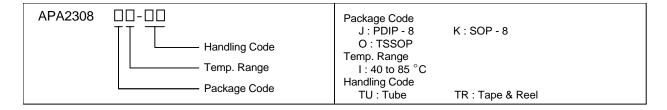
Applications

Portable Digital Audio

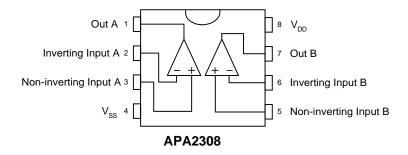
Gereral Description

The APA2308 is an integrated class AB stereo headphone driver contained in an SO-8 or a DIP-8 plastic package. The APA2308 is capable of delivering 280mW of max. Output power to an 8Ω load or 110mW to a 32Ω load with less than 10% (THD+N) from a 5V power supply. The device is fabricated in a CMOS process and has been primarily developed for portable digital audio applications .

Ordering Information



Block Diagram



ANPEC reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit	
V_{DD}	Supply Voltage	8	V	
T _{SC (O)}	Output Short-circuit Duration, at T _A =25°C, P _{TOT} =1W	20	S	
T _A	Operating Ambient Temperature range	-40 to 85	°C	
T _J	Maximum Junction Temperature	150	°C	
T _{STG}	Storage Temperature Range	-65 to +150	°C	
T _s	Soldering Temperature,10 seconds	260	°C	
V _{ESD}	Electrostatic Discharge	-3000 to 3000 *1	V	

Note : *1. Human body model : C=100pF , R=1500 Ω , 3 positive pulses plus 3 negative pulses

Thermal Characteristics

Symbol	Parameter	Value	Unit
R_{THJA}	Thermal Resistance from Junction to Ambient in Free Air		
	DIP-8	109	K/W
	SO-8	210	K/W

Electrical Characteristics

 $\rm V_{DD}\!\!=\!\!5V$, $\rm V_{SS}\!\!=\!\!0V$, $\rm T_{A}\!\!=\!\!25^{\circ}C$, $\rm f_{i}\!\!=\!\!1kHz$, $\rm R_{L}\!\!=\!\!32\Omega$ (unless otherwise noted)

Symbol	Parameter	Test Conditions	APA2308		8	Unit		
			Min.	Тур.	Max.			
Supply								
	Supply Voltage							
V _{DD}	Single		3.0	5.0	7.0	V		
	Dual		1.5	2.5	3.5			
V _{ss}	Negative Supply Voltage		-1.5	-2.5	-3.5	V		
I _{DD}	Supply Current	No Load		2.5	5	m A		
P _{TOT}	Total Power Dissipation	No Load		12.5	25	m W		
DC Chara	cteristics		•	•	•			
V _{I(OS)}	Input Offset Voltage			5		m V		
I _{BIAS}	Input Bias Current			10		pА		
V _{cм}	Common Mode Voltage		0		3.5	V		
G _v	Open-loop Voltage Gain	$R_L=5k\Omega$		75		dB		
Io	Max. Output Current	(THD+N)/S<0.1%		140		m A		
R _o	Output Resistance			0.25		Ω		
Vo	Output Voltage Swing	$R_1 = 32\Omega^{*1}$	0.25		4.75			
		$R_L = 16\Omega^{*1}$	0.5		4.5	V		
PSRR	Power Supply Rejection Ratio	f _i =100Hz		65		dB		
		$\dot{V}_{RIPPLE(P-P)} = 100 \text{mV}$						
$\alpha_{ t CS}$	Channel Separation	$R_L=32\Omega$		95		dB		
CL	Load Capacitance				200	pF		



Electrical Characteristics Cont.

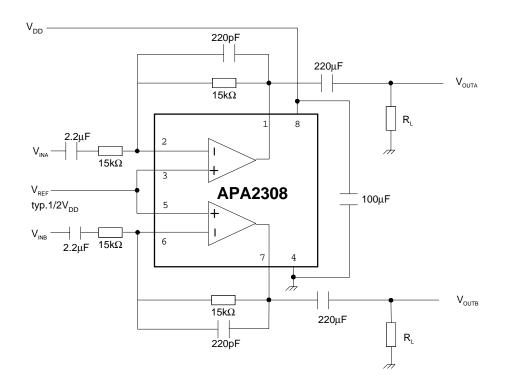
 V_{DD} =5V, V_{SS} =0V, T_A =25°C, f_i =1kHz, R_L =32 Ω (unless otherwise noted)

Symbol	Parameter	Test Conditions	APA2308		Unit	
			Min.	Тур.	Max.	
AC Charac	cteristics					
(THD+N)/S	Total Harmonic Distortion plus	$R_L=32\Omega^{*2}$		-65	-60	dB
	Noise to Signal Ratio			0.05	0.1	%
S/N	Signal to Noise Ratio		90	100		dB
f _G	Unity Gain Frequency	Open-loop, R _L =5kΩ		5		MHz
Po	Max. Output Power	(THD+N)/S < 0.1%		84		mW
Cı	Input Capacitance			3		pF
SR	Slew Rate	Unity Gain Inverting		5		V/μs
В	Power Bandwidth	Unity Gain Inverting		20		kHz

Notes : * $^{\mbox{\tiny 1}}$: Values are proportional to $\mbox{V}_{\mbox{\tiny DD}}$; (THD+N)/S < 0.1%

* 2 : V_{DD} =5.0V ; $V_{O(p-p)}$ =3.5V (at 0 dB)

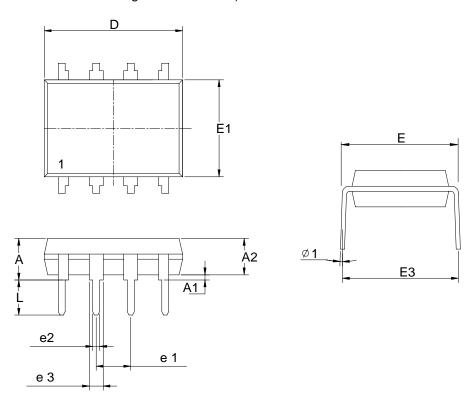
Test And Application Circuits





Packaging Information

PDIP-8 pin (Reference JEDEC Registration MS-001)

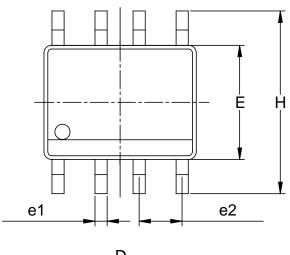


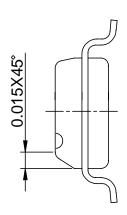
Dim	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
А		5.33		0.210	
A1	0.38		0.015		
A2	2.92	3.68	0.115	0.145	
D	9.02	10.16	0.355	0.400	
e1	2.54BSC		0.100BSC		
e2	0.36	0.56	0.014	0.022	
e3	1.14	1.78	0.045	0.070	
E	7.62 BSC 8.26		0.300 BSC 0.325		
E1	6.10	7.11	0.240	0.280	
E3		10.92		0.430	
L	2.92	3.81	0.115	0.150	
ф 1	0°	15°	0°	15°	

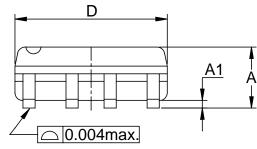


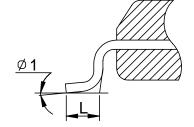
Packaging Information

SOP-8 pin (Reference JEDEC Registration MS-012)









Dim	Millimeters		Inches		
	Min.	Max.	Min.	Max.	
А	1.35	1.75	0.053	0.069	
A1	0.10	0.25	0.004	0.010	
D	4.80	5.00	0.189	0.197	
Е	3.80	4.00	0.150	0.157	
Н	5.80	6.20	0.228	0.244	
L	0.40	1.27	0.016	0.050	
e1	0.33	0.51	0.013	0.020	
e2	1.27	1.27BSC 0.50BS		BSC	
ф 1	0°	8°	0°	8°	