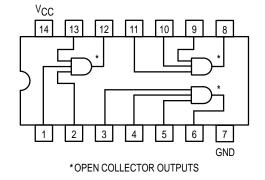


# **TRIPLE 3-INPUT AND GATE**

# SN54/74LS15

# TRIPLE 3-INPUT AND GATE LOW POWER SCHOTTKY





J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

#### **ORDERING INFORMATION**

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
Voн	Output Voltage — High	54, 74			5.5	V
lOL	Output Current — Low	54 74			4.0 8.0	mA

## SN54/74LS15

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits					
Symbol	Parameter		Min	Тур	Max	Unit	Test Conditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V <sub>IL</sub>	Input LOW Voltage	54			0.7	٧	Guaranteed Input LOW Voltage for All Inputs	
		74			0.8			
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	$V_{CC} = MIN$ , $I_{IN} = -18 \text{ mA}$	
IOH	Output HIGH Current	54, 74			100	μΑ	V <sub>CC</sub> = MIN, V <sub>OH</sub> = MAX	
V <sub>OL</sub>	Output LOW Voltage	54, 74		0.25	0.4	V	I <sub>OL</sub> = 4.0 mA	V <sub>CC</sub> = V <sub>CC</sub> MIN, V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> per Truth Table
		74		0.35	0.5	V	I <sub>OL</sub> = 8.0 mA	
l	Input HIGH Current				20	μΑ	$V_{CC} = MAX$ , $V_{IN} = 2.7 V$	
ΊΗ					0.1	mA	$V_{CC} = MAX$ , $V_{IN} = 7.0 V$	
I <sub>IL</sub>	Input LOW Current				-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.4 V	
Icc	Power Supply Current Total, Output HIGH Total, Output LOW				3.6	mA	V <sub>CC</sub> = MAX	
					6.6			

### AC CHARACTERISTICS ( $T_A = 25^{\circ}C$ )

		Limits		Limits		
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
tPLH	Turn-Off Delay, Input to Output		20	35	ns	V <sub>CC</sub> = 5.0 V
tPHL	Turn-On Delay, Input to Output		17	35	ns	$C_L = 15 \text{ pF}, R_L = 2.0 \text{ k}\Omega$