DUAL LOW VOLTAGE C-MOS POWER AMPLIFIER

■ GENERAL DESCRIPTION

The NJU7082B is a dual C-MOS Power Amplifier which is available to operate with single power supply and low voltage.

The NJU7082B realizes neary full-swing output with low voltage operation (2.4V). An output voltage is kept more than V_{DD}-0.3V or less than V_{SS}+0.3V when output current is 40mA, therefore it is suitable for a head-phone and speaker driver of the battery operated audio items.

■ PACKAGE OUTLINE

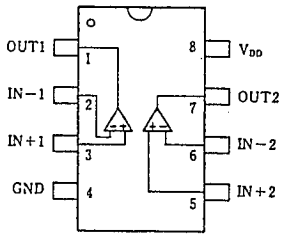
NJU7082BM

NJU7082BV

■ FEATURES

- Single Power Supply
- Wide Operating Voltage Range (V_{DD} 2.4V ~ 5.5V)
- Neary Full-Swing Output
  (V_{SS}+0.3V ~ V_{DD}-0.3V at Iout=±40mA)
- Low Distortion
  (0.05% at RL=38ohm, 1.0Vp-p)
- Low Operating Current
  (2mA at V_{DD}=3V)
- Package Outline -- DMP8 / SSOP8
- C-MOS Technology

■ PIN CONFIGURATION



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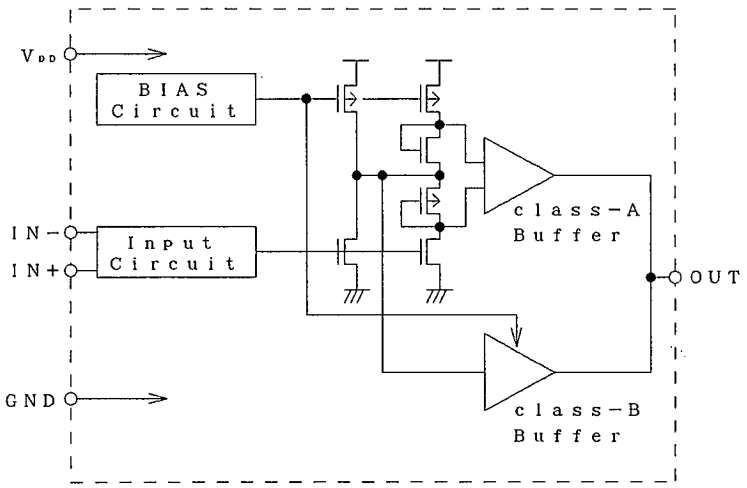
■ EQUIVALENT CIRCUIT (as single circuit)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{DD}	7	V
Input Voltage	V_{ID}	V_{SS}- 0.3 ~ V_{DD}+0.3	V
Power Dissipation	P_D	250(SSOP8) 300(DMP8)	mW
Operating Temperature	Topr	- 25 ~ + 75	°C
Storage Temperature	Tstg	- 40 ~ +125	°C

■ ELECTRICAL CHARACTERISTICS 1

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Voltage Range	V_{DD}		2.4		5.5	V

■ ELECTRICAL CHARACTERISTICS 2 (V_{DD}=3V)

(Ta=25°C, V_{DD}=3V, V_{SS}=0V, f=1kHz)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I_{DD}	No Load Condition : Voltage Follower V_O=1.5V		2	3	mA
Input Offset Voltage	V_{IO}		-10		10	mV
Input Offset Current	I_{IO}			10		pA
Input Bias Current	I_{IB}			10		pA
Input Impedance	R_{IN}			10^{12}		Ω
Input Common Mode Voltage Range	V_{ICM}		0.2~2			V
Maximum Output Voltage Range	V_{OM}	Iout= 40mA	2.6	2.7		V
		Iout=-40mA		0.3	0.4	
Maximum Output Current	I_{OM}	(D+N)/S<0.1% Source		30		mA
		(D+N)/S<0.1% Sink		-30		
Large-Signal Voltage gain	A_V		55			dB
Common Mode Rejection ration	CMRR	V_{ICM}=0.2~2.0V	53			dB
Supply Voltage Rejection ration	PSRR	V_{DD}=2.7~3.3V	55			dB
Total Harmonic Distortion	(D+N)/S	V_O=1.0Vp-p 0~10dB, 38Ω		0.05		%
Equivalent Input Noise Voltage	Ent	IEC-A		3		μVrms
Signal to Noise Ratio	S/N			110		dB
Unity Gain Bandwidth	Ft	CL=10pF, OPEN LOOP		1.5		MHz
Slew Rate	SR	Unity Gain Turn Over, CL=32pF RL=2kΩ		1		V/μs
Channel Separation	α	V_O=0.6Vrms		100		dB

NOTE1) The NJU7082B should be operated gaining of triple or more for stable operation.

NOTE2) When the NJU7082B using no-current-load and low gain application (voltage follower, etc.), oscillation will be worst. In this case, the stray capacitance of the output terminal should be less than 100pF.

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■ ELECTRICAL CHARACTERISTICS 3 (V_{DD}=5V)

(Ta=25℃, V_{DD}=5V, V_{SS}=0V, f=1kHz)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Operating Current	I_{DD}	No Load Condition : Voltage Follower V_O=2.5V		3	4	mA
Input Offset Voltage	V_{IO}		-10		10	mV
Input Offset Current	I_{IO}			10		pA
Input Bias Current	I_{IB}			10		pA
Input Resistor	R_{IN}			10^{12}		Ω
Input Common Mode Voltage Range	V_{ICM}		0.4～4			V
Maximum Output Voltage Range	V_{OM}	Iout= 40mA	4.6	4.7		V
		Iout=-40mA		0.3	0.4	
Maximum Output Current	I_{OM}	(D+N)/S<0.1% Source		30		mA
		(D+N)/S<0.1% Sink		-30		
Large-Signal Voltage gain	A_V		55			dB
Common Mode Rejection ration	CMRR	V_{ICM}=0.4～4.0V	53			dB
Supply Voltage Rejection ration	PSRR	V_{DD}=4.5～5.5V	55			dB
Total Harmonic Distortion	(D+N)/S	V_O=1.0Vp-p 0～10dB, 38Ω		0.05		%
Equivalent Input Noise Voltage	Ent	IEC-A		3		μVrms
Signal to Noise Ratio	S/N			115		dB
Unity Gain Bandwidth	Ft	CL=10pF, OPEN LOOP		1.5		MHz
Slew Rate	SR	Unity Gain Turn Over, CL=32pF RL=2kΩ		1		V/μs
Channel Separation	α	V_O=1.0Vrms		105		dB

NOTE3) The NJU7082B should be operated gaining of triple or more for stable operation.

NOTE4) When the NJU7082B using no-current-load and low gain application (voltage follower, etc.), oscillation will be worst. In this case, the stray capacitance of the output terminal should be less than 100pF.

■ APPLICATION CIRCUIT