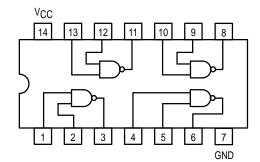


# **QUAD 2-INPUT NAND BUFFER**

# SN54/74LS37



QUAD 2-INPUT NAND BUFFER LOW POWER SCHOTTKY



J SUFFIX CERAMIC CASE 632-08



N SUFFIX PLASTIC CASE 646-06



D SUFFIX SOIC CASE 751A-02

#### **ORDERING INFORMATION**

SN54LSXXJ SN74LSXXN SN74LSXXD Ceramic Plastic SOIC

#### **GUARANTEED OPERATING RANGES**

Symbol	Parameter		Min	Тур	Max	Unit
VCC	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	54 74	-55 0	25 25	125 70	°C
ЮН	Output Current — High	54, 74			-1.2	mA
lOL	Output Current — Low	54 74			12 24	mA

## SN54/74LS37

### DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

			Limits						
Symbol	Parameter		Min	Тур	Max	Unit	Test C	onditions	
VIH	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs		
V <sub>I</sub> .	54				0.7	V	Guaranteed Input LOW Voltage for		
VIL	Input LOW Voltage	74			0.8	]	All Inputs		
VIK	Input Clamp Diode Voltage			-0.65	-1.5	V	$V_{CC} = MIN, I_{IN} = -18 \text{ mA}$		
V	Output HIGH Voltage	54	2.5	3.5		V	V <sub>CC</sub> = MIN, I <sub>O</sub>	H = MAX, VIN = VIH	
Vон		74	2.7	3.5		V	or V <sub>IL</sub> per Truth	Table	
Voi	Output LOW Voltage	54, 74		0.25	0.4	V	I <sub>OL</sub> = 12 mA	V <sub>CC</sub> = V <sub>CC</sub> MIN, V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub>	
VOL		74		0.35	0.5	V	I <sub>OL</sub> = 24 mA	per Truth Table	
1	Input HIGH Current				20	μΑ	$V_{CC} = MAX$ , $V_{IN} = 2.7 V$		
lιΗ					0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V		
IιL	Input LOW Current				-0.4	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.4 V		
los	Short Circuit Current (Note 1	)	-30		-130	mA	VCC = MAX		
Icc	Power Supply Current Total, Output HIGH Total, Output LOW				2.0	mA	V <sub>CC</sub> = MAX		
					12				

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

### AC CHARACTERISTICS $(T_A = 25^{\circ}C)$

		Limits				
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions
tPLH	Turn-Off Delay, Input to Output		12	24	ns	$V_{CC}$ = 5.0 V, $R_L$ = 667 $\Omega$
tPHL	Turn-On Delay, Input to Output		12	24	ns	C <sub>L</sub> = 45 pF