Power Transistor (-60V, -3A) **2SB1184 / 2SB1243**

Features

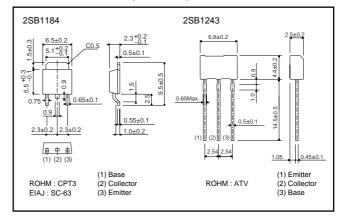
1) Low VCE(sat). VCE(sat) = -0.5V (Typ.) (Ic/IB = -2A / -0.2A)

2) Complements the 2SD1760 / 2SD1864.

Structure

Epitaxial planar type PNP silicon transistor

●External dimensions (Units : mm)



● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	-60	V	
Collector-emitter voltage		Vceo	-50	V	
Emitter-base voltage		VEBO	-5	V	
0.11		lc	-3	A (DC)	
Collector curren	it	Іср	-4.5	A (Pulse) *1	
Collector power dissipation	2SB1184		1	W	
		Pc	15	W (Tc = 25°C)	
	2SB1243		1	W *2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55~+150	°C	

^{*1} Single pulse, Pw = 100ms

^{*2} Printed circuit board, 1.7mm thick, collector copper plating 100mm² or larger.

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-60	-	-	V	Ic = -50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic = −1mA
Emitter-base breakdown voltage	ВVево	-5	-	-	V	Iε = -50μA
Collector cutoff current	Ісво	-	-	-1	μΑ	Vcb = -40V
Emitter cutoff current	ІЕВО	-	-	-1	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-1	V	Ic/I _B = -2A/-0.2A *
Base-emitter saturation voltage	V _{BE(sat)}	-	-	-1.5	V	Ic/I _B = -2A/-0.2A *
DC current transfer ratio	hfe	82	-	390	-	$V_{CE} = -3V$, $I_{C} = -0.5A$ *
Transition frequency	f⊤	-	70	-	MHz	Vce = -5V, Ie = 0.5A, f = 30MHz
Output capacitance	Cob	-	50	-	pF	Vcb = -10V, IE = 0A, f = 1MHz

^{*} Measured using pulse current.

● Packaging specifications and hFE

		Package	Taping	
		Code	TL	TV2
Туре	hfe	Basic ordering unit (pieces)	2500	2500
2SB1184	PQR		0	-
2SB1243	PQR		-	0

hre values are classified as follows:

Item	Р	Q	R
hfE	82~180	120~270	180~390

•Electrical characteristic curves

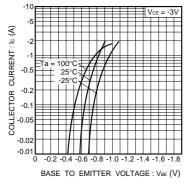


Fig.1 Grounded emitter propagation characteristics

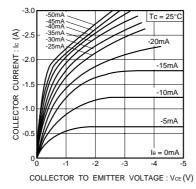


Fig.2 Grounded emitter output characteristics (I)

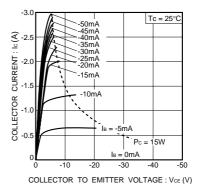


Fig.3 Grounded emitter output characteristics (II)

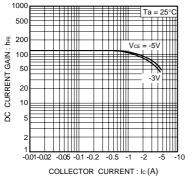


Fig.4 DC current gain vs. collector current (I)

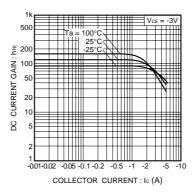


Fig.5 DC current gain vs. collector current (II)

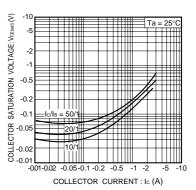


Fig.6 Collector-emitter saturation voltage vs.collector current

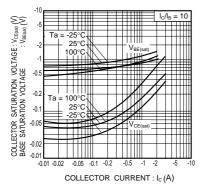


Fig.7 Collector-emitter saturation voltage vs. collector current
Base-emitter saturation voltage vs. collector current

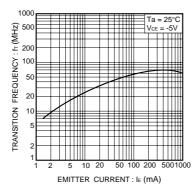


Fig.8 Gain bandwidth product vs. emitter current

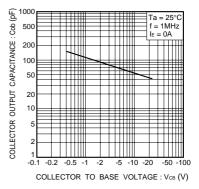


Fig.9 Collector output capacitance vs. collector base voltage

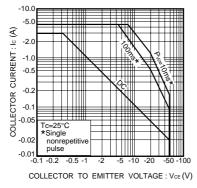


Fig.10 Safe operation area (2SB1184)

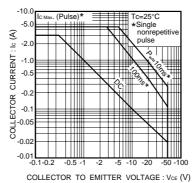


Fig.11 Safe operation area (2SB1243)