TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED TYPE (PCT PROCESS)

2 S C 3 4 2 5

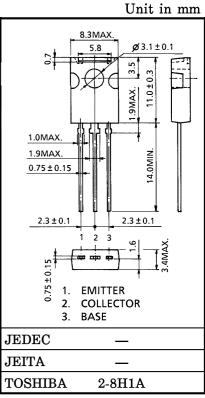
SWITCHING REGULATOR AND HIGH VOLTAGE SWITCHING APPLICATIONS

HIGH SPEED DC-DC CONVERTER APPLICATIONS

- Excellent Switching Times $t_r=1.0\mu s$ (Max.), $t_f=1.5\mu s$ (Max.) at $I_C=0.5A$
- High Collector Breakdown Voltage: VCEO=400V

MAXIMUM RATINGS (Tc = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Base Voltage		v_{CBO}	500	V	
Collector-Emitter Voltage		v_{CEO}	400	V	
Emitter-Base Voltage		$V_{ m EBO}$	7	V	
Callaston Command	DC	$I_{\mathbf{C}}$	0.8	A	
Collector Current	Pulse	I_{CP}	1.5	A	
Base Current	I_{B}	0.5	A		
Collector Power	Ta = 25°C	Da	1.2	W	
Dissipation	$Tc = 25^{\circ}C$	$^{\mathrm{P}_{\mathrm{C}}}$	10		
Junction Temperature		T_{j}	150	°C	
Storage Temperature Range		$\mathrm{T_{stg}}$	-55~150	$^{\circ}\mathrm{C}$	

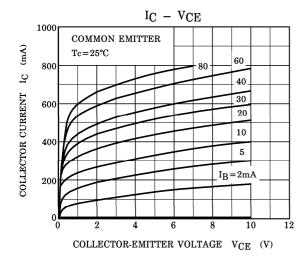


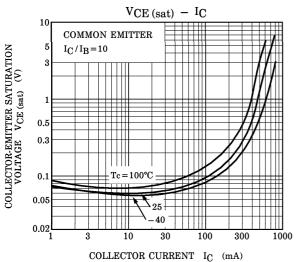
Weight: 0.82g (Typ.)

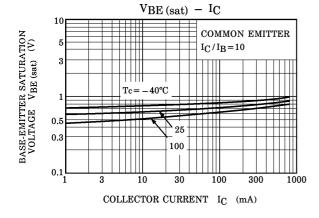
ELECTRICAL CHARACTERISTICS (Tc = 25°C)

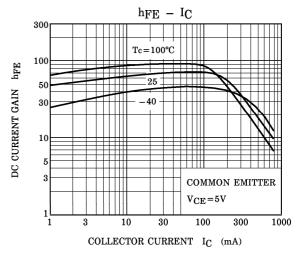
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CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current		I_{CBO}	$V_{CB} = 400V, I_{E} = 0$	_	_	100	μ A
Emitter Cut-off Current		$I_{ m EBO}$	$V_{EB} = 7V, I_C = 0$	_	_	100	μ A
Collector-Bas Voltage	se Breakdown	V (BR) CBO	$I_{C}=1$ mA, $I_{E}=0$	500	_	_	V
Collector-Em Voltage	nitter Breakdown	V (BR) CEO	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$	400	_	_	V
DC Current Gain		$_{ m h_{FE}}$	$V_{CE} = 5V, I_{C} = 0.1A$	20	_	100	
			$V_{CE}=5V$, $I_{C}=0.5A$	10	_	_	
Collector-Em Voltage	nitter Saturation	V _{CE (sat)}	$I_{\rm C}$ =0.1A, $I_{\rm B}$ =0.01A	_	_	0.5	V
Base-Emitter Saturation Voltage		V _{BE} (sat)	I _C =0.1A, I _B =0.01A	_	_	1.0	V
Switching Time	Rise Time	t_r	$I_{B1} \stackrel{20\mu s}{\prod}_{I_{B2}} \stackrel{I_{B1}}{\prod}_{I_{B2}} \stackrel{OUTPUT}{\bigcap}_{I_{B2}}$	_	_	1.0	
	Storage Time	$\mathbf{t_{stg}}$		_	_	2.5	μs
	Fall Time	tf	$\begin{bmatrix} I_{B1} = -I_{B2} = 0.05A, V_{CC} = 200V \\ DUTY \ CYCLE \le 1\% \end{bmatrix}$	_	_	1.5	

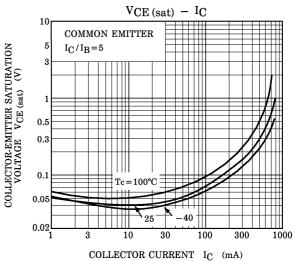
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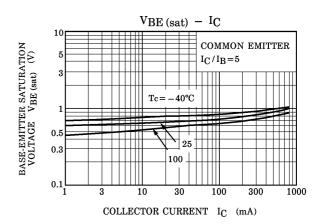


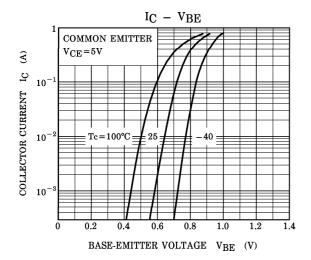


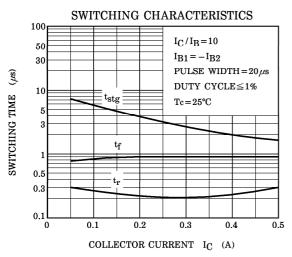


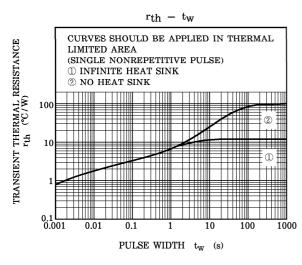


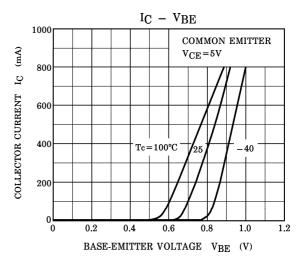


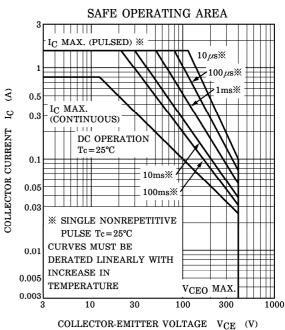












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