

LF347

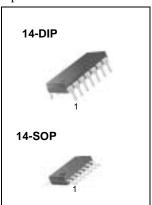
Quad Operational Amplifier (JFET)

Features

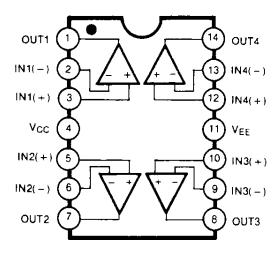
- · Low input bias current
- · High input impedance
- Wide gain bandwidth: 4 MHz Typ.
- High slew rate: 13 V/µs Typ.

Description

The LF347 is a high speed quad JFET input operational amplifier. This feature high input impedance, wide bandwidth, high slew rate, and low input offset voltage and bias current. LF347 may be used in circuits requiring high input impedance. High slew rate and wide bandwidth, low input bias current.

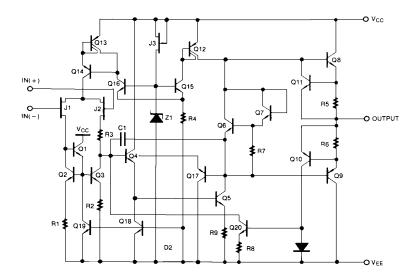


Internal Block Diagram



Schematic Diagram

(One Section Only)



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±18	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage Range	VI	±15	V
Output Short Circuit Duration	-	Continuous	-
Power Dissipation	PD	570	mW
Operating Temperature Range	TOPR	0 ~ + 70	°C
Storage Temperature Range	TSTG	-65 ~ + 150	°C

Electrical Characteristics

(VCC= +15V, VEE= -15V, TA=25 $^{\circ}$ C, unless otherwise specified)

Parameter	Cumbal	Conditions		LF347			l lmi4
Parameter	Symbol	Conditio	Conditions		Тур.	Max.	Unit
Input Offset Voltage	VIO	Rs = 10KΩ		-	5	10	ma\/
			Note 1	-	-	13	mV
Input Offset Voltage Drift(Note2)	ΔV10/ΔΤ	Rs = 10KΩ		-	10	-	μV/°C
Input Offset Current	lio			-	25	100	pA
			Note 1	-	-	4	nA
Input Bias Current	IBIAS			-	50	200	pA
			Note 1	-	-	8	nA
Large Signal Voltage Gain	G∨	$R_L = 2K\Omega$		25	100	-	V/mV
		V _O (P-P)= ±10V	Note 1	15	-	-	V/IIIV
Output Voltage Swing	VO(PP)	$R_L = 10K\Omega$		±12	±13.5	-	V
Input Voltage Range	VI(R)	-		±11	+15 -12	-	V
Common-Mode Rejection Ratio	CMRR	R _S ≤ 10KΩ		80	100	-	dB
Power Supply Rejection Ratio	PSRR	Rs ≤ 10KΩ		80	100	-	dB
Input Resistance	Rı	-		-	10 ¹²	-	Ω
Supply Current	Icc	-		-	7.2	11	mA
Slew Rate	SR	-		-	13	-	V/μS
Gain Bandwidth Product(Note2)	GBW	-		-	4	-	MHz
Channel Seperation	CS	f = 1Hz ~ 20Khz (input referenced)		-	120	-	dB
Equivalent Input Noise Voltage	en	R _S = 100Ω f = 1KHz		-	20	-	nV/ √Hz
Equivalent Input Noise Current	IN	f = 1KHz		-	0.01	-	pA/√Hz

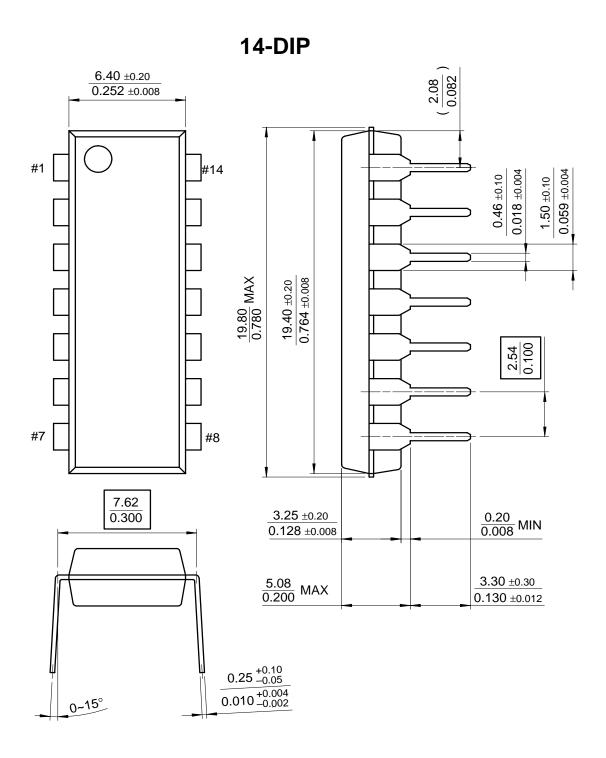
Note:

LF347 : 0≤T_A≤+70 °C
Guaranteed by design

Mechanical Dimensions

Package

Dimensions in millimeters

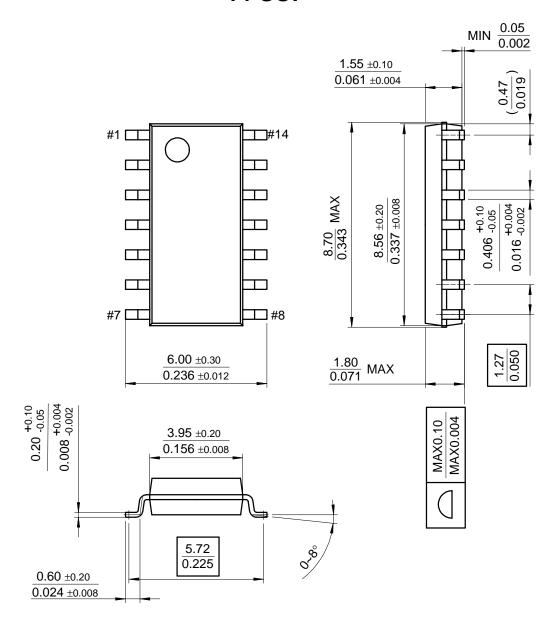


Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

14-SOP



Ordering Information

Product Number	Package	Operating Temperature
LF347N	14-DIP	0 ~ + 70°C
LF347M	14-SOP	0~+700

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com