

NVIDIA DRIVE OS 6.0.4 Linux

Release Notes (Rev. 1)



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Introduction

The NVIDIA DRIVE™ OS 6.0 Linux Release Notes are for NVIDIA DRIVE™ AGX Orin Development Kits.

Note:

This DRIVE OS and DriveWorks release may only be used for test and development.

Note:

All Orin-X SoC (TA990SA) related platforms marked as PS must use NVIDIA DRIVE OS release 6.0.3.0 or later.

All Orin SoC (TA979SA) and related platforms marked as PS must use NVIDIA DRIVE OS release 6.0.4.0 or later.

NVIDIA DRIVE™ OS is the reference operating system and associated software stack, which includes CUDA, TensorRT, NvMedia, NvStreams, and Developer Tools, designed specifically for developing and deploying autonomous applications on DRIVE AGX-based hardware.

DRIVE OS Development Kits

NVIDIA DRIVE™ OS Software Development Kit (SDK) is used to develop DRIVE OS applications for deployment on NVIDIA DRIVE AGX™ based hardware platforms.

NVIDIA DRIVE™ OS Platform Development Kit (PDK) is used to adapt NVIDIA DRIVE OS to run on custom hardware based on NVIDIA Automotive SoC (i.e., Orin).

DRIVE OS Base Operating Systems

DRIVE OS Linux "Standard"

DRIVE OS Linux "Standard" is a reference platform based on Ubuntu 20.04 Linux which is intended for prototyping and development of autonomous vehicle platforms. DRIVE OS Linux is production ready but does not go through the same rigorous safety assessment as DRIVE OS QNX for Safety.

The NVIDIA® DRIVE® OS 6.0 TensorRT™ 8.4.11 release includes a TensorRT Standard+Proxy package. The Standard+Proxy package for NVIDIA DRIVE OS users of TensorRT, which is available on all platforms except QNX safety, contains the builder, standard runtime, proxy runtime, consistency checker, parsers, Python bindings, sample code, standard and safety headers, and documentation. The builder can create engines suitable for the standard runtime and DLA. This release includes safety headers and the capability to build standard engines restricted to the scope of operations that will be supported by the safety and proxy runtimes in this and future NVIDIA DRIVE OS 6.0 releases.

Release Highlights

Key Features in this Release

For a complete list of new features and enhancements in this release, see <u>New Features and Enhancements</u>.



NVIDIA recommends developers to use DRIVE OS 6.0.4 (rev.1) release for using DriveWorks 5.6. Instructions to download and install DRIVE OS 6.0.4 (rev.1) can be found in the latest NVIDIA DRIVE OS 6.0 Installation Guide.

Deprecations in this Release

The following items are deprecated in this release:

Summary	Module	Impact
API names have been updated from *_system_* to *_vm_* to better depict the state transitions	Hypervisor	• nvdvms_get_system_state (enum NvDvmsState* state) and
implemented in these APIs.		• nvdvms_set_system_state (enum NvDvmsState state)
		were renamed to:
		• nvdvms_get_vm_state (enum NvDvmsState* state) nvdvms_set_vm_state (enum NvDvmsState state).
Standalone DRIVE OS OSS Source Dockers have been deprecated and are no longer provided as part of each release as OSS content is now included in the standard Linux Dockers.	OSS	-
NvMedia streamlining is complete and the following APIs are removed from DRIVE OS: NvMediaSurface APIs that help with Image creation	NvMedia, Standard	Transition away from the APIs for 6.0.4.0 if you have not already done so. This deprecation was announced and

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replacement APIs were provided as early NvMediaImage APIs that help create Image as 6.0.2.0. Surfaces • NvMediaVideoSurface APIs that help create Video Surfaces • NvMediaVideo APIs for Encoder, Decoder, Mixer etc., that use NvMediaVideoSurface as input/output Legacy NvMediaIEP APIs that use NvMediaImage or NvMediaSurface types/attrs as input/output Legacy NvMediaIDE APIs that use NvMediaImage or NvMediaSurface types/attrs as input/output Legacy NvMedialJPE APIs that use NvMediaImage or NvMediaSurface types/attrs as input/output Legacy NvMediaIJPD APIs that use NvMediaImage or NvMediaSurface types/attrs as input/output Legacy NvMedia2D APIs that use NvMediaImage or NvMediaSurface types/attrs as input/output Legacy SIPL APIs that use NvMediaImage or NvMediaSurface types/attrs as input NvMediaReleaseGetVersion NvMediaCoreGetVersion NvMediaArray APIs that help create buffer of type Array • NvMediaCVScratchPad APIs that help create Array based buffers The following APIs and libraries are marked deprecated and will be removed in 6.0.5.0. NvMediaDeviceCreate NvMediaDeviceDestroy Libnymedia.so Libnvmedia core.so Refer to the migration guide for details required to transitioning away from these APIs. The API signature is not changing for any Standard The NvMediaCore APIs, NvMediaDeviceCreate(), of the Tensor APIs, but the and NvMediaDeviceDestroy() are planned to be implementation has been changed. Hence deprecated. In line with this, the new version of the customers will have to use the NvMediaTensorCreateFromNvSciBuf() and recompiled libraries. NvMediaTensorFillNvSciBufAttrs() APIs will require a NULL pointer to be passed as input, The nvm_dlaSample application has been instead of a valid NvMediaDevice pointer. updated to remove the usage of The clients of NvMediaTensor are recommended NvMediaDevice APIs and NULL pointers to remove NvMediaDeviceCreate() and

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NvMediaDeviceDestroy() usage from the applications. Clients are also recommended to		are passed as input, where a NvMediaDevice handle is expected.
remove libnvmedia.so and libnvmedia_core.so from linker/makefiles.		Example from the app source code:
		File: dla_sample/tensor/tensor.cpp; Function: Tensor::Create()
		status = NvMediaTensorCreateFromNvSciBuf(NULL, bufObj, &m_pTensor);
<pre>IBuilder::setMaxBatchSize IBuilder::getMaxBatchSize</pre>	TensorRT builder	These functions are deprecated. Use explicit dimensions instead.

Planned Upcoming Changes

The following sections describe planned, upcoming changes.

Consistency checker

This release of the consistency checker performs most but not all possible checks to ensure that engines can be run in the safety runtime without invoking undefined or nondeterministic behavior. Operations within the safety scope are checked, tensor sizes and formats are checked, and inputs to each layer are analyzed to ensure no uninitialized values are read from memory. Some tactics require specialized kernels and internal data structures. Most, but not all, of these internal data structures are validated in this release. NVIDIA DRIVE OS 6.0.6.0 will contain a consistency checker that includes all expected checks.

Kernels that do not have consistency checker support include:

- Kernels that are used only in the first layer when optimizing three channel image input convolutions.
 - o sm80_xmma_fprop_image_first_layer_f16f16_f32_f16_nhwckrsc_ nhwc hmma
 - o sm80_xmma_fprop_image_first_layer_f32f16f16_f32_f16_nchwkr sc nhwc hmma
- Kernels used for deconvolution when certain restrictions on parameters are met.
 - o sm80_xmma_deconv_implicit_gemm_interleaved_indexed_i8i8_i8 i32_f32_nchw_vect_c_32kcrs_vect_n_32_nchw_vect_c_32 (strided)
 - o sm80_xmma_deconv_implicit_gemm_interleaved_indexed_i8f32_i 8i32_f32_nchw_vect_c_32kcrs_vect_n_32_nchw_vect_c_32 (strided)

New Features and Enhancements

This release includes support for these new features and enhancements.

New Features for DRIVE OS

DRIVE AGX Orin Revision D00 (P/N 940-63710-0010-D00)

DRIVE OS now supports DRIVE AGX Orin revision D00 (P/N 940-63710-0010-D00). If flashing the Developer Kit manually (outside of SDK Manager or Docker), refer to the flashing information for this board. For a complete list of DRIVE platform supported boards, refer to the DRIVE OS 6.0 Installation Guide.

ISliceLayer

The DRIVE OS supports ISliceLayer on DLA in TensorRT with the following Precision Modes, Configuration/Function combinations as detailed in table below, for each cell marked "Yes":

		А	В
		Precision Mode FP16	Precision Mode INT8
1	Slicing Operation	Yes	Yes

Note:

- 1. Dimensions to slice: given NCHW or NHWC data format, slicing operations are necessary in H (Height), W (Weight), and C (Channel) dimensions.
- 2. We can assume that indexing happens within a valid range, i.e., 0<= the queried index < the size in each dimension <= the limit in size in each dimension.

Fence Timestamping on iGPU

The DRIVE OS provides time stamping information associated with start and end of the processing tasks, on different Hardware Engines, with a minimum resolution of 1 us.

JTAG Debug Capability for FSI FW

The DRIVE OS provides documentation guidance for JTAG debug capability of FSI FW (Firmware), specifying

- 1. Supported debugger
- 2. iSystem only support (for more information, see the Debugger User Guide PDF in the FSI PDK package for 6.0.4.0)
- 3. Physical connections setup for the reference platform
- 4. Procedure to load FSI FW

Build-Kit support DRIVE OS RFS Generation

The DRIVE OS provides a uniform Build-FS solution for generating custom root file system (RFS/IFS).

Enable SoC Logging to Persistent Storage

The DRIVE OS provides a unified framework to log system level events, UART prints, error codes onto persistent storage.

Customer Fields in BR-BCT

The DRIVE OS provides a Guest OS library that allows customer applications to read Board-Specific Customer Data field in BR-BCT. The DRIVE OS provides a mechanism to update Board-Specific Customer Data field in BR-BCT via Flashing Scripts for the following types.

SI No	Fields
1	SKU ID
2	SKU Version
3	Board ID
4	Board Revision
5	Board Serial Number
6	2x Ethernet MAC ID

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Readable Unique ID (ECID) to Software on All Guest OSes

The DRIVE OS restricts read access of the Exclusive Chip Identification (ECID) to only those users who need it for their functionality. The DRIVE OS provides API to read the ECID in Guest OS.

Ethernet Interface Concurrent Timestamp API

The DRIVE OS provides an interface to report concurrent time values, as maintained in hardware timestamps, for Orin 1G(EQoS) and 10G(MGBE) Ethernet Controllers with minimal read delay possible.

Note:

This is applicable to all the 10G(MGBE) Orin Ethernet Controller Interfaces.

Option to Enable Encryption for All Boot Images in the Bootchain

The DRIVE OS supports the Confidentiality of Secure Boot Binaries stored in External Non-Volatile Memory. If secure boot is enabled, the image is encrypted at image creation time and decrypted by bootloaders after loading.

sMCU Fan Control

The DRIVE OS provides a mechanism to send SOC thermal information from Orin to MCU over Ethernet/VLAN200. The DRIVE OS supports Fan Control for DRIVE AGX Orin Platforms on the external Safety MCU, using thermal sensor information received from Orin over Ethernet.

Note:

This is meant for Orin Fan only, other Fans on the platform would have Fan control solely executed on safety MCU without any involvement of Orin.

cuDLA as a Backend for TensorRT

The DRIVE OS provides a CUDA Programming based DLA backend for TensorRT, to enable a unified model based for inference programming.

Support for Fixed ROI Stats from ISP

The DRIVE OS supports the use of a fixed ROI, set during init-time per camera, when calculating the necessary image statistics for exposure control and ISP pipeline processing.

Support eMMC Discard Command

The DRIVE OS supports discard command as part of mNAND library for eMMC device.

Power Profile Support for TA979SA on 3710-A01-CT02

NVIDIA delivers qualified clock settings for TA979SA SoC in 6.0.4.0. Customers using this SoC can update their clock setting.

NV_OEM_KEY2

NV_OEM_KEY2 was not available for key derivation in 6.0.3.1. In 6.0.4.0, it has been updated to support key derivation using SP800-108-Counter-KDF-HMAC-SHA256 scheme.

The two (2) keys (NV_OEM_KEY1 and NV_OEM_KEY2) are derived from OEM_KDK0 using the same SP800-108-Counter-KDF-HMAC-SHA256 scheme. They are different in the sense that the label and context used to derive these keys from OEM_KDK0 is different. While NV_OEM_KEY1 is the same for all devices, NV_OEM_KEY2 is a device-specific key as Exclusive Chip ID (ECID), which is unique to each device used as context for the derivation of this key. Use NV_OEM_KEY1 for generic use cases and NV_OEM_KEY2 for device-specific use cases.

Macsec

Macsec is enabled and the MTU is reduced by 34 bytes to accommodate SECTag, ICV, and MACSEC frame type. If you set an MTU of 2000, you will see the MTU set as 1966.

Security Service Token/Persistent Keys

Starting with the 6.0.4.0 release, token key changes are not written to secure storage immediately. Instead, they are saved when the single R/W session is closed (via C_CloseSession, C_CloseAllSessions or C_Finalize). You may experience a delay when the R/W session is being closed, as write to secure NOR is occurring. The token keys can be used within the single R/W session as usual before the writing happens. However, it is recommended to close the single R/W session once all token key modifications have taken place to ensure that they have been written.

The token keys written to the secure NOR are associated with Token ID 2 in the 6.0.4.0 release of DRIVE OS. In future releases of DRIVE OS support multiple tokens, the existing token keys on the secure NOR will be part of a token with ID 2. The access controls of the Token ID 2 will be applicable for these token keys.

Any wrapped for provisioning using offline tool prior to 6.0.4.0 fails to be unwrapped, both because of base key for unwrapping key derivation change and because the Token ID is not set. If no token field is set in the wrapped AAD field, that wrapped key now becomes a session object rather than a token object.

This behavior is subject to change in a future release.

New Features for TensorRT 8.4.11

This release includes support for these new features and enhancements.

- Sparse convolution support for FP16 and INT8 precision
- Less strict version checking for serialized engine plan files

Sparse Convolution Support

This release supports sparse convolutions for weights that satisfy certain requirements. Convolutions must be FP16 or INT8 precision and use weights that satisfy the requirements from Developer Guide: Sparsity. To enable sparse convolution support, make sure to set the ksparse_weights flag in the builder config and make sure that kFP16 and/or kINT8 modes are enabled.

In safe engines, only some convolution kernels support structured sparsity. In addition, there are some limitations on kernel sizes and padding sizes supported for convolution with structured sparsity. The product of kernel size must be less than or equal to 32, and the product of padding sizes must be less than or equal to 32. If a kernel is not found that supports structured sparsity in the given configuration, then a normal non-sparse kernel will be used. Kernels that support structured sparsity are expected to have higher performance.

TensorRT Standard Build

The TensorRT 8.4 release includes changes to the standard builder and runtime that appear in TensorRT for DRIVE OS 6.0. For more information, refer to the TensorRT 8.4.1 Release Notes.

Documentation Changes

The TensorRT 8.4.11 documentation has been updated accordingly:

The NVIDIA DRIVE OS 6.0 TensorRT 8.4.11 Developer Guide is based on the enterprise
TensorRT 8.4.x release. We have modified the TensorRT 8.4.x Developer Guide
documentation for DRIVE OS 6.0.4 accuracy. The TensorRT safety content has been removed.

The TensorRT safety content is located in the *NVIDIA DRIVE OS 6.0 Safety Developer Guide*. Refer to this PDF for all TensorRT safety specific documentation.

New Features in DriveWorks 5.6

- System Task Manager
 - cuPVA scheduling support
- Image Processing
 - PVA Tracker, Dectector, ORB descriptor integration.
 - The stereo rectifier has been improved so the principal point caluclation is more precise. User can expect its value to change compared to previous versions.

Fixed Issues

The following DRIVE OS issues from the previous release are resolved in this release:

Feature	Module	Description
3614780	Camera	What is the issue?
Core		Documentation to disable FSI support is missing in the 6.0.3.1_ED release.
		How does it impact the customer?
		Use the documentation below to disable FSI support.
		If there is a workaround, what is it?
		Disable FSI for DRIVE AV PCT by modifying profile makefiles.
		Add the following in profile makefiles to disable FSI. ENABLE_FSI := n
		For information on which profile makefile to modify for different PCTs, refer to the Profile Makefile section in the customer documentation. Is it for SDK/PDK?
		All.
3643521	NvSciIPC	BOOT FAILRUES seen on ToT due to kernel panic related to NVGPU.
3653610 DRIVE		What is the issue?
Update		
		DRIVE Update is hard coding a sector size of 512 bytes, causing issue to process GPT partitions in devices using 4096 bytes sector size
		How does it impact the customer?
		DRIVE Update is unable to process customized GPT partition created at UFS devices.
		If there is a workaround, what is it?
		N/A
		Is it for SDK/PDK? All
3465342	MCU	What is the issue?
	Firmware	OVLF fault is getting reported for rail - VDD_CV for the configured threshold value 1.160mV
		How does it impact the customer?
		The customer get VMON error notification due to this fault. This violates safety
		norms of voltage monitoring

		If there is a workaround, what is it? N/A
3466065	MCU Firmware	What is the issue? VRS12 does power-up sequence verification of rails during power on. The power-up sequence of rails is not consistent for every power-up cycle, thus leading to intermittent fault during power-up sequence verification.
		How does it impact the customer? The customer gets VRS12 power-up sequence failures during board power-on. This breaks voltage monitoring safety recommendations.
		If there is a workaround, what is it? N/A
		When can we expect the fix? 6.0.5.0
		Is it for SDK/PDK? All.
3597626	FSI	What is the issue?
		After closing nvsipl_camera application, repeated HW errors are reported from VI error collator with high frequency at de-init phase. These error reports interrupts FSI and errors are processed and notified to system error handler on FSI. High frequency of error reports from VI error collator leads to increased FSI interrupt load which might disrupt normal task execution and we see OS crash on FSI.
		How does it impact the customer?
		FSI OS crashes and FSI stops responding to Spi Master packet request from MCU, this leads to E2E error spews on MCU console.
		If there is a workaround, what is it?
		N/A
		Is it for SDK/PDK?
		All
3470744	Graphics	What is the issue? Corruption will be observed on buffers that are flipped by Weston clients to Weston via eglSwapBuffers.
		Have done it investable and an ext
		How does it impact the customer? Customers will observe corruption on graphics applications that call eglSwapBuffers (EGL/GL).
		If there is a workaround, what is it?
		If there is a workaround, what is it? The corruption is observed only when Weston assigns the graphics applications to overlays.

3512913	Graphics	As a WAR, forcing Weston to use gl-composition by setting WESTON_FORCE_RENDERER=1 will result in the correct content being rendered on display. Is the Issue part of SDK/PDK? Linux Standard What is the issue? Vulkan graphics applications on DRIVE OS Linux with raytracing support fail to run.
		If there is a workaround, what is it? N/A
3515496	Kernel	What is the issue? SC7 resume fails on the OEM Production fuse board when encryption is enabled. How does it impact the customer? You are not able to exercise SC7 on the OEM Production fuse board if encryption is enabled. On normal board, SC7 works properly. If there is a workaround, what is it? N/A Is it for SDK/PDK? Standard SDK/PDK.
3551548	Connectivit y	What is the issue? marvel phy firmware is not updated which is required for mgbe0 interface on the Orin board How does it impact the customer? MGBE0 Interface link will not work If there is a workaround, what is it?
		Fix is, update the firmware provided in BSP Is it for SDK/PDK?
		Both.
3558311	Display	What is the issue? The mouse cursor cannot move on the physical display connected to the target board. This affects any use case that uses the mouse cursor, such as Ubuntu desktop.
		If there is a workaround, what is it? With VNC display, the mouse cursor still moves on the virtual display, but there is no workaround for the physical display monitors that are connected to the board.
		Is it for SDK/PDK? Linux SDK

3582439	Virtualizati	What is the issue?
	on	Resources are being assigned to Guest VM0 incorrectly. Because of this, you hit a CBB error from Hypervisor context.
		How does it impact the customer?
		Customer will see the following CBB error being reported in the Hypervisor
		<pre>console and logs, also to the HSM. CBB2.0 ERR: FIREWALL_ERR - Attempt to access a region that is firewall protected</pre>
		If there is a workaround, what is it?
		N/A
		Is it for SDK/PDK?
		Linux both SDK and PDK.
3588727	TOS	What is the issue?
		Boot failure seen with RSA3K / EDDSA fused boards because of resource contention issue seen at TZVault.
		How does it impact the customer?
		Customers cannot proceed with their development as there is a boot failure.
		If there is a workaround, what is it?
		NVIDIA has a WAR that needs to be patched on top of embedded/6.0.3.0 in <pre><top>/drive-foundation/platform-</top></pre>
		config/hardware/nvidia/platform/t23x/automotive/pct/:
		<pre>diffgit a/drive_av/guest_config.h</pre>
		b/drive_av/guest_config.h
		index f1cd5b449e98b1 100644
		a/drive_av/guest_config.h +++ b/drive av/guest config.h
		@@ -678,7 +678,7 @@
		<pre>.mempool_id = 0xffffffff,</pre>
		},
		//! [capture example2]
		<pre>boot_delay = 0,</pre>
		+ .boot_delay = 1800,
		.hsp.hsp_inst[TOP_HSP_0] = {
		/* Mailbox Pair 0-1
		* 4 - SCE -> CCPLEX
		00 -957,6 +957,7 00
		<pre>.mempools = RESOURCE_POOL(88, 1),</pre>
		}, 1
		}, + .boot delay = 900,
		.sysmgr cfg = {
		.lazy boot ack flag = 0,

		},
		Is it for SDK/PDK?
		All
3614365	DRIVE	What is the issue?
3014303	Update	Special steps are required to update from 6.0.2.0 to 6.0.3.0 using
	opuace	sample_driveupdate.
		Sample_anveapaate.
		How does it impact the customer?
		You must follow the steps if sample driveupdate was used to update.
		γ
		If there is a workaround, what is it?
		1. Generate the 6.0.3.0 dupkg via 6.0.2.0 dupkg tool.
		2. scp 6.0.3.0 dupkg to the target.
		3. Use 6.0.2.0 content_server and 6.0.2.0 sample_driveupdate with the following
		commands to update inactive chain to 6.0.3.0:
		./content_server <package> &</package>
		./sample_driveupdate -i /gos-a/content/files
		4. After reboot, use 6.0.3.0 content_server, 6.0.3.0 sample_driveupdate with
		following commands to continue the deployment:
		<pre>./content_server <package> & ./sample driveupdate -p /gos-a/content/files</package></pre>
		./ Sample_ullveupdate p / gos a/ content/ llles
		Is it for SDK/PDK?
		All
3620931	DRIVE	What is the issue?
	Update	Structure define of DUTR_IVC_PARAM has been removed in dutransport.h
		How does it impact the customer?
		Customer would need to update their source code and re-compile on top of new
		PDK.
		If there is a workaround, what is it?
		Need to change from DUTR_IVC_PARAM to DUTR_NVSCI_PARAM, name
		parameter needs to be changed to ep[0]
3623406	System	What is the issue?
	Software	Yocto fails to build with Driveworks enabled due to mismatch in a patch file
		How does it impact the customer?
		Users who build Yocto with DWs enabled will face the build issue. Yocto without
		DWs enabled builds successfully
		If there is a workaround, what is it?
		Yes. Yocto Driveworks samples recipe can be modified to fix the build failure:
		<pre>Edit file: <top>/drive-linux_src/yocto/layers/meta-drive- samples/recipes-nv-samples/driveworks/driveworks-samples.bb</top></pre>
		1. Delete the line under SRC_URI "file://0001-fix-yocto-build.patch \"
		2. Add below task in the recipe (e.g., before "do_compile" line):
		<pre>do_configure() {</pre>
1		<pre>sed -i 's/dwchannel//g' \${S}/samples/CMakeLists.txt</pre>

		<pre>sed -i 's/cgf_nodes//g' \${S}/samples/CMakeLists.txt }</pre>
3623911	Software	What is the issue?
0020022		USB device is not getting detected/enumerated on type A ports (both).
		This is an intermittent issue.
		How does it impact the customer?
		On some boards, USB device is not getting detected/enumerated on type A ports.
		If there is a workaround, what is it?
		Type C USB port is working for all boards.
		Is it for SDK/PDK?
		Linux Standard SDK/PDK
3592188	TensorRT safety samples	Safety samples are provided in the Standard+Proxy package and are built from there. When building for QNX using static linking, the static libraries used were previously from Proxy and were thus the Proxy Runtime which was sometimes showing unexpected behavior.
		This release includes more detailed build instructions and build options to handle static linking without need for manual edits to build scripts.
3602138	DriveWorks	Initialize/release dwFrameCapture in loop leads system to run out of memory.
3597225	DriveWorks	Video exporter & header-dump tool dumps Segmentation Fault while exiting.
200782352	DriveWorks	LRAW Preview Extraction Tool fails.
3605267	DriveWorks	sample_cgf_dwchannel is failing for hybrid inter-process, inter-chip scenario and sync_mode p2c and its giving segmentation fault.

NVIDIA Software Security Updates

This release of NVIDIA DRIVE OS 6.0 Linux includes updates that address the following issue[s]:

CVE ID	NVIDIA Issue Number	Description
CVE-2020-13844	3521043	Arm Armv8-A core implementations utilizing speculative execution past unconditional changes in control flow may allow unauthorized disclosure of information to an attacker with local user access via a side-channel analysis, aka "straight-line speculation".
CVE-2022-23960	3566972	Computer systems with microprocessors utilizing speculative execution and indirect branch prediction, contain a cache speculation vulnerability known as Branch History Injection (BHI) or Spectre-BHB, where an attacker with local user access who performs a complex Spectre v2 style attack but using the shared branch history, may be able to infer sensitive information causing information disclosure.
CVE-2022-23960	3567284	Computer systems with microprocessors utilizing speculative execution and indirect branch prediction, contain a cache speculation vulnerability known as Branch History Injection (BHI) or Spectre-BHB, where an attacker with local user

		access who performs a complex Spectre v2 style attack but using the shared branch history, may be able to infer sensitive information causing information disclosure.
CVE-2019-14067	3588117	NVIDIA DRIVE OS contain a vulnerability in TZVault's memory comparison module, where an Observable Timing Discrepancy, may allow an unprivileged attacker with physical access to inference limited sensitive information.
Not Assigned	3591341	NVIDIA DRIVE OS contain a vulnerability in the Hypervisor Kernel for Automotive SMMU, where failure to validate the ending IPA of an allocation, may allow a local attacker to cause loss of Confidentiality.
CVE-2022-23960	3592037	Computer systems with microprocessors utilizing speculative execution and indirect branch prediction, contain a cache speculation vulnerability known as Branch History Injection (BHI) or Spectre-BHB, where an attacker with local user access who performs a complex Spectre v2 style attack but using the shared branch history, may be able to infer sensitive information causing information disclosure.

For more information about NVIDIA's vulnerability management, refer to the <u>NVIDIA Product Security</u> page.

Third-Party Software Security Updates

This release of NVIDIA DRIVE OS 6.0 Linux includes updates that address the following issue(s):

CVE ID	Description
CVE-2020-26555	NVIDIA Linux distributions contain a vulnerability in a Bluetooth BR/EDR PIN Pairing procedure, where Improper Authorization may permit an unauthorized nearby device to complete pairing without knowledge of the PIN, which may lead to loss of Confidentiality and/or Integrity Impact.

Known Limitations

The following sections describe known limitations in this release.

Feature	Module	Description
Yocto	DriveWorks	DriveWorks is not updated in Yocto.
DLA	TensorRT	DLA standalone mode does not support mixed precision.
DLA	TensorRT	DLA is not supported through the TensorRT safety runtime. The DLA loadables for standard and safety can be consumed by the cuDLA runtime and the NvMedia runtime.
DLA	TensorRT	When running on DLA, various layers have restrictions on supported parameters and input shapes. Some existing limitations for the convolution, fully connected, concatenation, and pooling layers were newly documented in this release. See the DLA Supported Layers section in the NVIDIA DRIVE OS 6.0 TensorRT 8.4.11 Developer Guide for details.
DLA	TensorRT	When running INT8 networks on DLA using TensorRT, operations must be added to the same subgraph to reduce quantization errors across the subgraph of the network that runs on the DLA by allowing them to fuse and retain higher precision for intermediate results. Breaking apart the subgraph in order to inspect intermediate results by setting the tensors as network output tensors, can result in different levels of quantization errors due to these optimizations being disabled.
DLA	TensorRT	There are two modes of softmax where the mode is chosen automatically based on the shape of the input tensor, where: • the first mode triggers when all non-batch, non-axis dimensions are 1, and • the second mode triggers in other cases if valid.
		The second of the two modes is supported only for DLA 3.9.0 and later. It involves approximations which may result in errors of a small degree. Also,

		batch size greater than 1 is supported only for DLA 3.9.0 and later. Refer to the DLA Supported Layers section in the NVIDIA DRIVE OS 6.0 TensorRT 8.4.11 Developer Guide for details.
Layers	TensorRT	For a list of safety-specific layer limitations, refer to the <i>Layer Limitations</i> In GPU Safety Restricted Mode section in the NVIDIA DRIVE OS 6.0 Safety Developer Guide.
I/O Formats	TensorRT	When using reformat-free I/O, the extent of a tensor in a vectorized dimension might not be a multiple of the vector length. Elements in a partially occupied vector that are not within the tensor are referred to here as vector-padding. For example: On GPU ofor input tensors, the application shall set vector-padding elements to zero. ofor output tensors, the value of vector-padding elements is undefined. In a future release, TensorRT will support setting them to zero. On DLA ofor input tensors, vector-padding elements are ignored. ofor output tensors, vector-padding elements are unmodified.
Safety samples	TensorRT	We cannot use -Xcompiler -Wno-deprecated-declarations options for safety samples; that is a standard certified option. We only add it for standard builds. Seeing the deprecated warnings during the build is expected for this case.
Execution context	TensorRT	Currently, the total execution context memory size is limited to 2 GiB due to internal safety constraints. This restriction is expected to be relaxed in a future release.

Known Issues

Note:

DriveWorks samples that provide GUIs/visualization are crashing when executed in Docker container due to host display not being accessible for the Docker container.

These are issues discovered during development and QA and are scheduled to be resolved in a future release.

Feature	Module	Description
3750024	AURIX	What is the issue?
		Occasionally SDKManager Flashing step will fail due to garbled control messages between the host and target device.
		How does it impact the customer?
		Flashing will fail.
		If there is a workaround, what is it?
		Customer needs to restart flashing process.
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		Both.
3726479	Kernel	What is the issue?
		When USB device is connected, SC7 resume fails.
		How does it impact the customer?
		If customer is connecting USB device to the board, then SC7 suspend/resume will not work.
		If there is a workaround, what is it?
		In 6.0.4.0 MCU firmware, after Orin enter SC7 the PREREG power is turned-off by pulling VBAT_SOC_ENA and SOC_PREREG_PWRON pins LOW from the MCU.
		If suspend-resume cycle is required with USB device the MCU should just put the Orin power regulators in SLEEP mode but should not turn-off the PREREG power.
		The VBAT_SOC_ENA and SOC_PREREG_PWRON should be driven HIGH from the MCU even when Orin is in SC7 mode.
		When can we expect the fix?
		Root cause analysis of why USB device is causing resume failure when PREREG is turned-off is still not concluded.

		Expected fix date will be updated after root-cause analysis.
		Is it for SDK/PDK?
		All.
2722770	ALIBIY	What is the issue?
3722779	AURIX	
		SC7 exit fails intermittently on a few boards.
		How does it impact the customer?
		Customers may see SC7 exit failure intermittently and need to perform aurixreset to recover from SC7 state.
		If there is a workaround, what is it?
		No
		When can we expect the fix?
		Not root caused. Contact NVIDIA support with the detailed logs if you see this issue.
		Is it for SDK/PDK?
		This is for all versions of SDK/PDK.
3679516	DRIVE	What is the issue?
30/9310	Update	
	opuate	Unexpected closing sample_driveupdate/content_server; other modules in VM print errors about being unable to connect to closed plugins.
		How does it impact the customer?
		If you press ctrl+c to end the sample_driveupdate/content_server, you may see errors
		similar to:
		[ERR][dutr_nvsci.c:dutrTxBufNvSci:1434]Handle Id 0x110005: Error 0x22 on NvSciIpcWrite
		[ERR][dulink_remote_helpers.c:dulinkTxMsg:438]Reach maximum retry count
		If there is a workaround, what is it?
		You can send abort to DU to cancel the deployment.
		When can we expect the fix?
		6.0.5.0
		Is it for Standard/Safty, SDK/PDK?
		All
3730015	Filesystem	What is the issue?
		The Aurix Firmware will not be updated on first boot after flashing on upgrade to 6.0.4.0 from a previous release
		How does it impact the customer?
		Additional features/bug fixes for the 6.0.4.0 Aurix FW vs. previous versions will not be
		available.
		If there is a workaround, what is it?
		After flashing execute the following:
		Target:
		<pre>\$ sudo rm -rf /persistent/driveos/security/etc/systemd/</pre>
		Host Aurix Console:
		NvShell> tegrareset

		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		All.
3741367	Bootburn	What is the issue?
		flash_bsp_images.py fails with -x option.
		How does it impact the customer?
		With flash_bsp_images.py , you cannot flash on a target if not in recovery mode and cannot find the board with "find board name" option, as find board option needs
		the -x option.
		If there is a workaround, what is it?
		For flashing with flash_bsp_images.py , you must set the Tegra in recovery mode.
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		AII.
3535820	Security	What is the issue?
		FSI does not have access to EMC registers for -
		Enabling DRAM uncorrected error reporting
		Reading bad page information and handling DRAM uncorrected errors
		How does it impact the customer?
		DRAM ECC uncorrected error cannot be handled , system continues to operate with DRAM ECC uncorrected error which is unsafe.
		If there is a workaround, what is it?
		No workaround available for this issue.
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		All
3724465	Bootburn	Intermittent flash failure observed.
3730926	Security	11 sub-tests of PKCS#11 test suite fail on ODM fused board.
3728354	NvMedia	ABI compatibility issue.
3719548	DRIVE Update	Deploy fails due to decomp persistent storage not being cleared and old data.
3726584	Bootburn	Boot to chain B fails after fuse programming and is unable to recover with aurixreset.
3712840	Connectivity	What is the issue?
		With tegrareset or aurix reset, the Guest VM log in the serial console stops at: pl:[I]: jumping to kernel at 0x80200000 (virtual 0x80000000)
		How does it impact the customer?
		How does it impact the customer? Serial console cannot be accessed on the case where issue is seen.
		Serial console cannot be accessed on the case where issue is seen. If there is a workaround, what is it?

		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		All
3711431	Video	Support for rgba and argb format for nvm_ijpd_sci.
3711131	Camera Core	Nvsipl_camera application reports error when run with control plugin:1.
3710589	Camera Core	What is the issue?
		Frame Discontinuities are observed when using are observed when running nvsipl_camera sample application with more than 2 IMX728 camera modules (8 MP) with display enabled under following condition
		3 ISP outputs are enabled simultaneously (none of thedisableISP0Output, disableISP1Output ordisableISP2Output are specified)
		How does it impact the customer?
		Customer can only use 2 ISP outputs from each camera for display when using 3 or more 8MP cameras
		If there is a workaround, what is it?
		No
		When can we expect the fix?
		To be determined.
		Is it for SDK/PDK?
		Issue is observed on Embedded Linux AV+L PDK/SDK.
3709049	FSI	Fabric error spews observed on demo reference firmware V 1.3.0 and V 1.3.1
3708327	Yocto	Starting Weston throws drm-backend.so undefined symbol error.
3704449,	SDK Manager	What is the issue?
3727638		Sometimes when user tries to install CUDA or DriveWorks on host, apt command reports "Hash sum mismatch" error. This is an intermittent issue that is not easily reproduced.
		How does it impact the customer?
		Customer will not be able to proceed with the installation of DRIVE OS 6.0.4 with SDKM tool, with this error.
		If there is a workaround, what is it?
		Retry installation.
		When can we expect the fix?
		Unknown. This ppears to be an Ubuntu issue and not related to NVIDIA packages.
		Is it for SDK/PDK?
		All.
3698885	Video	What is the issue?
		The system mLockTimeout will occur during the Decoding process for all those frames where Decoding time > mLockTimeout. For High Bitrate streams (mostly of 8K resolution), which do not decode in real time, the Decode Time may exceed the NVDEC mLocktimeout set. Hence certain streams that can't decode in real time due to very High Bitrate, may fail during Decoding on (AV +Q) and (AV + L) platforms. How does it impact the customer?
		Decoding of streams encoded in a High resolution (like 8k) with very high bitrates may fail

		If there is a workaround, what is it?
		No
		When can we expect the fix?
		Will not fix.
		Is it for SDK/PDK?
		All.
3664734	System	What is the issue?
	Software	DemoAppCom assumes that the current user (that is executing the app) is a member of the group nvfsicom,nvepl. The group nvfsicom already exists in the filesystem, but the membership is not. The permission occurs due to 2 reasons:
		Filesystem user account does not have nvfsicom,nvepl memberships preset.
		The instructions to execute DemoAppCom do not update the memberships of the user account before executing the app.
		How does it impact the customer?
		DemoAppCom fails to launch and reports permission denied.
		If there is a workaround, what is it?
		The workaround is to update the current user account's membership using the command below, reboot the system and launch the application DemoAppCom. \$ sudo usermod -aG nvfsicom, nvepl <user></user>
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		Standard and SDK.
3664337	System	What is the issue?
	Software	DemoAppCom assumes that the current user (that is executing the app) is a member of the group nvfsicom,nvepl. The group nvfsicom already exists in the filesystem, but the membership is not. The permission occurs due to 2 reasons:
		Filesystem user account does not have nvfsicom,nvepl memberships preset.
		The instructions to execute DemoAppCom do not update the memberships of the user account before executing the app.
		How does it impact the customer?
		DemoAppCom fails to launch and reports permission denied.
		If there is a workaround, what is it?
		WAR is to update the current user account's membership using the command below, reboot the system and launch the application DemoAppCom.
		\$ sudo usermod -aG nvfsicom,nvepl <user></user>
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		SDK.
3727547	ВРМР	Safe shutdown timeout happens in simulated safe poweroff MCU shell command (fails on P3663, but PASS on P3710).
3646829	AURIX	What is the issue?
		On P3663 board with AV+L, due to ethernet de-initialization timing, Safe Shutdown acknowledgment is not received from Orin to MCU when safe shutdown is performed

notified to system error handler on FSI.			
Safeshutdown failure is seen on Orin, and poweroff and aurixreset always completes on safeshutdown timeout at MCU side. However, safeshutdown works with the tegaraset command. If there is a workaround, what is it? N/A When can we expect the fix? Fix is ready and it will be delivered as a patch or in the next release 6.0.5.0. Is it for SDK/PDK? All. 3626664 Safety Services Additional error is getting injected and reported on injecting PSC_CLUSTER_CE fault (errorcode: 0x2e4e) on FSI and AURIX. 3698410 Camera Core Errors observed while nvisipl_camera is launched the first few times. 3699001 IST Increase ist-clk freq for production KIST. What is the issue? Some KeyIST configuration values are not POR value. How does it impact the customer? The KeyIST diagnostic may run longer the POR KPIs. In very rare conditions, the diagnostic may report a false failure. If there is a workaround, what is it? No workaround. When can we expect the fix? 6.0.5.0 Is it for SDK/PDK? All DRIVE Update What is the issue? DUPKG tool errors out when provided an absolute path for the "out" argument. How does it impact the customer? Customer will not be able to generate the package if an absolute path is provided to "-out" argument If there is a workaround, what is it? Workaround is to provide a relative path for "out" argument When can we expect the fix? 6.0.5.0 Is it for SDK/PDK? All 3698410 Camera Core What is the issue? After starting nvsipl_camera application, 1 - HW error (Code - 0x28b6) is reported from VI error collator. These error reports interrupts FSI and errors are processed and notified to system error handler on FSI.			
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Customer will not be able to generate the package if an absolute path is provided to " -out" argument If there is a workaround, what is it? Workaround is to provide a relative path for "out" argument When can we expect the fix? 6.0.5.0 Is it for SDK/PDK? All 3698410 Camera Core What is the issue? After starting nvsipl_camera application, 1 - HW error (Code - 0x28b6) is reported from VI error collator. These error reports interrupts FSI and errors are processed and notified to system error handler on FSI.		Update	DUPKG tool errors out when provided an absolute path for the "out" argument.
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from VI error collator . These error reports interrupts FSI and errors are processed and notified to system error handler on FSI.	3698410	Camera Core	What is the issue?
How does it impact the customer?			from VI error collator . These error reports interrupts FSI and errors are processed and
			How does it impact the customer?

		Error is seen by customer, but there is no functional impact, happens only first few times after boot, quality and throughput of images are not impacted in runtime.
		times after boot, quanty and timougnput of images are not impacted in fundine.
		If there is a workaround, what is it?
		No workaround. Error is not seen after 2 runs.
		When can we expect the fix?
		Debugging is in progress. The projected fix date is by 6.0.5.0 release.
		Is it for SDK/PDK?
		PDK.
3465334	MCU	What is the issue?
3403334	Firmware	VRS12 does power-down sequence verification of rails during power off. The power-down sequence of rails is not consistent for every power-off cycle, thus leading to intermittent fault during power-off sequence verification.
		How does it impact the customer?
		The customer gets VRS12 power-down sequence failures during power-off. This
		breaks voltage monitoring safety recommendations
		If there is a workaround, what is it? N/A
		Is it for SDK/PDK?
		All
3626664	Safety	What is the issue?
	Services	The security settings of error collators in PSC are set up such that only PSC could access the same. This is an exception to Global Rule that all Error Collators shall be accessible only by FSI.
		During Fault injection testing, when an error is injected FSI SW attempts to access Error Collators in PSC and Crashes with an illegal access exception.
		The Affected Fault names related to ECs in PSC are:
		PSC_CLUSTER_UE
		PSC_SE_UE
		PSC_DMA_UE
		PSC_FABRIC_UE
		PSC_AON_UE
		PSC_SE_CE
		PSC_DMA_CE
		PSC_CLUSTER_CE
		PSC_FABRIC_CE
		PSC_AON_CE
		PSC_FABRIC_AON_UE
1	i	PSC_FABRIC_AON_CE
		CAR_PSC_UE PADCTL_PSC_G8_UE

		How does it impact the customer?
		Whenever there is an Error in PSC reported via Error Collators FSI would also have an exception. The customer will not be able to perform fault injection testing.
		However, SOC_ERROR would be asserted by HW which could be detected by MCU SW.
		MCU would also observe SPI E2E error as FSI would crash (@shubhamj to Confirm the same)
		If there is a workaround, what is it?
		No workaround in 6.0.4.0; a fix will be provided in 6.0.5.0.
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		All
3694755	Safety	What is the issue?
3094733	Services	In DOS-SHR-5980, HSI T23X-QSPI HSIv2-3 has not yet been implemented. The HSI
		expects QSPI errors in PSC to be reported to the Safety Services SW in FSI.
		How does it impact the customer?
		Customers will not be able to receive QSPI errors in Safety Services SW in FSI.
		If there is a workaround, what is it?
		Errors are propagated back as return value along the calling sequence to the caller of
		DRIVE OS API.
		When can we expect the fix?
		6.0.5.0
		Is it for SDK/PDK?
		All.
3698885	Video	What is the issue?
		The system mLockTimeout will occur during the Decoding process for all those frames where Decoding time > mLockTimeout. For High Bitrate streams (mostly of 8K resolution), which do not decode in real time, the Decode Time may exceed the NVDEC mLocktimeout set. Hence certain streams that can't decode in real time due to very High Bitrate, may fail during Decoding on (AV +Q) and (AV + L) platforms.
		How does it impact the customer?
		Decoding of streams encoded in a High resolution (like 8k) with very high bitrates may fail
		If there is a workaround, what is it?
		No
		When can we expect the fix?
		WNF
		Is it for SDK/PDK?
		All
3708327	Yocto	What is the issue?
		Weston launch fails due to failure in loading drm-backend.so
		How does it impact the customer?

If there is a workaround, what is it? Weston source code is included in Drive OS Linux releases. Make the following edithe Weston sources and rebuild. (Note: A Yocto drive-os-av-image build automatically includes a Weston build.)		1	Customore using Weston need to see by a match and referrible
Weston source code is included in Drive OS Linux releases. Make the following edithe Weston sources and rebuild. (Note: A Yocto drive-os-av-image build automatically includes a Weston build.) In the file 3rdparty/weston/libweston/meson.build, insert the following line into to "srcs_drm" list, under line 201: 'drm-hdr-metadata.c', Then rebuild drive-os-av-image. When can we expect the fix? 6.0.5.0 Is it for SDK/PDK? All. 3709049 FSI What is the issue? Fabric Error Spews observed on FSI Console. This is due to memory access from FS SW on Core 0 to address 0x60000030, without disengaging DBB Isolation. Due to this MCU receives error: ErrCode-0x100c Reptrid-0xe00e ErrCode-0x100c Reptrid-0xe00e How does it impact the customer? No functional Impact, just error spews are observed on the FSI & MCU console. If there is a workaround, what is it? No When can we expect the fix? 6.0.5.0 Is it for SDK/PDK? All 3709711 Camera Core What is the issue? The SIPL and FuSa UMD libraries may emit Capture Status Timeout error logs at Deinitialization, if the StopModule API is implemented for the sensor driver. How does it impact the customer? The Capture Status Timeout error logs are benign and do not have any safety impact if there is a workaround, what is it? The Capture Status Timeout logs can be ignored, they emitted by user-mode applications and are not reported to the SEH.			Customers using Weston need to apply a patch and rebuild it.
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The Capture Status Timeout error logs are benign and do not have any safety imposing there is a workaround, what is it? The Capture Status Timeout logs can be ignored, they emitted by user-mode applications and are not reported to the SEH.			Deinitialization, if the StopModule API is implemented for the sensor driver.
If there is a workaround, what is it? The Capture Status Timeout logs can be ignored, they emitted by user-mode applications and are not reported to the SEH.			·
The Capture Status Timeout logs can be ignored, they emitted by user-mode applications and are not reported to the SEH.			
applications and are not reported to the SEH.			·
When can we expect the fix?			
			When can we expect the fix?
The issue with the Capture Status Timeout error logs will be resolved for the Drive 6.0.5.0 release.			The issue with the Capture Status Timeout error logs will be resolved for the Drive OS 6.0.5.0 release.
Is it for SDK/PDK?			Is it for SDK/PDK?
The issue with the Capture Status Timeout error logs is present in all the above packages where the sensor driver implements the StopModule API.			
What is the issue?			What is the issue?
drivers. At Deinitialization, the sensors will not receive a command to gracefully st			The StopModule API has not been implemented in the AR0820 and OV2311 sensor drivers. At Deinitialization, the sensors will not receive a command to gracefully stop streaming, and will instead continue transmitting data until the power is disabled.

İ	1	
		How does it impact the customer?
		The customer will see SEH error logs to the AURIX and CCPLEX UART terminals due to CSI pixel data continuing to be transmitted to Tegra despite the capture pipeline(s) having already been arrested. SEH logs may be reported to the application.
		If there is a workaround, what is it?
		The client should ignore Camera capture-related SEH error logs after the Deinitialization API(s) are called
		When can we expect the fix?
		The StopModule APIs will be implemented for the AR0820 and OV2311 sensor for the Drive OS 6.0.5.0 release, pending clarification with their vendors and testing.
		Is it for SDK/PDK?
		The issue affects the standard build for the AR0820 sensor and OV2311 sensor, for both the SDK and PDK for both sensors.
3711131	Camera Core	What is the issue?
		nvsipl_camera application reports error when run with command line option "-plugin 1"
		The error appears to be during SetExposure call in CDD which sets the new sensor exposure settings computed by auto control algorithm.
		"plugin 1" exercises a custom auto control plugin implemented in the sample app and hence may result in different settings than the default auto control algorithm
		How does it impact the customer?
		If any customer is writing a custom auto control plugin, they may also see the failures.
		in any customer is writing a custom auto control plagm, they may also see the failures.
		If there is a workaround, what is it?
		No workaround available as the issue is not root caused yet.
		,
		When can we expect the fix?
		Investigation is in progress. Fix is expected to be in next release (6.0.5.0).
		Is it for SDK/PDK?
		All
3711431	Video	What is the issue?
		nvm_ijpd_sci application and the underlying IJPD driver does not support ARB and RGBA formats (that the deprecated nvmimg_jpgdec supported) in the 6.0.4.0 release.
		How does it impact the customer?
		ARGB and RGBA output formats will not be functional.
		If there is a workaround, what is it?
		Use the available YUV output formats.
		When can we expect the fix?
		6.0.5.0 Is it for SDK/PDK?
		SDK
2710549	DRIVE	What is the issue?
3719548	Update	what is the issue?
	-1	

		All
		Is it for SDK/PDK?
		6.0.5.0
		When can we expect the fix?
		No workaround in 6.0.3.1 release for 55W profile.
		If there is a workaround, what is it?
		for 105W Profile
		We are updating 105W Profile POR to be set to PLLP (405Mhz) so this is not an issue
		-> ~202MHz) for 55W profile.
		How does it impact the customer? You may see high CBB performance as it is on PLLP (405MHz) than as per POR (PLLC2/
		test failures.
		Switching AXI CBB to PLLC2 (202 Mhz) as per POR causing (benchmark, memory etc)
200454454	Clocks	What is the issue?
		6.0.5.0
		When can we expect the fix?
		N/A
		If there is a workaround, what is it?
		0x4c000008 Timestamp-0x4cc3bce
		MCU_FOH: ErrReport: ErrorCode-0x28cb ReporterId-0xffff Error_Attribute-
		Platform power consumption exceeds target grade (55W profile only). MCU Console will see the following error spew on boot (all power profiles):
		How does it impact the customer?
		profile.
3687948		AXI CBB clock is running at rate above the maximum limit set by power workload
3597381,	MCU	What is the issue
		6.0.5.0
		When can we expect the fix?
		N/A
		This impacts the customers who use MAX96705 serializer in the camera module. If there is a workaround, what is it?
		How does it impact the customer?
		Link enable/disable feature does not work for GMSL1 camera.
3639779	Camera Core	What is the issue?
		All
		Is it for SDK/PDK?
		6.0.5.0
		Do not use decompressor if planned to have abort support. When can we expect the fix?
		If there is a workaround, what is it?
		Deployment with decompressor cannot be aborted.
		How does it impact the customer?
	1	lock up decompressor if was used in deployment

Yocto tegra-initrams-recovery build falls if the PDK packages are not installed. How does it impact the customer? Customers who install SDK-only debians and build Yocto tegra-initrams-recovery image will hit a build failure. However, the Recovery initrams is for the PDK package only, so requires PDK packages to be installed. If there is a workaround, what is it? Users with access to PDK debian packages (via NVOnline) may install those packages following the steps documented in the Developer's Guide, prior to launching the tegra-initrams-recovery Yocto build. When can we expect the fix? 6.0.5.0 is it for SDK/PDK? All.		1	
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Software SOC_ERROR is asserted by ORIN due to errors in SOC. However, SOC_ERROR assertion	2506705	Conto	
555_Emiliar is asserted by similar and to enters in 555. The wever, 555_Emiliar assertion	3506785	-	
		Juliwale	is a decision by SEH and for SEH is only sample implementation, hence suggested

	customers to look for notification as primary expected results. SOC_ERROR Pin need not be monitored.
	However, all error notifications should be tracked.
	How does it impact the customer?
	Customer has to handle SOC_ERRORs in customers implementation of SEH decide SOC_ERROR Assertion.
	If there is a workaround, what is it?
	Customer needs to ignore the list of Errors in SEH implementation.
	When can we expect the fix?
	TBD
	Is it for SDK/PDK?
	All
Virtualization	What is the issue?
	Warm/Guest OS reboot (i.e., "sudo reboot") not supported on AV+L/Linux platform for DRIVE OS 6.0/Orin
	How does it impact the customer?
	Customer is not able to use the traditional command to reboot the Linux Guest OS alone.
	If there is a workaround, what is it?
	Reboot the Hypervisor system using the Hypervisor reboot command from the Linux command line:
	\$ sudo su
	<pre>\$ echo 1 > /sys/class/tegra_hv_pm_ctl/tegra_hv_pm_ctl/device/trigger_ sys_reboot</pre>
	When can we expect the fix?
	6.0.5.0
Docker	What is the issue?
	With DRIVE OS 6.0, we have included the Docker runtime as part of the RFS and so Docker is now available in the Guest OS. However, the runtime is unable to access the internet.
	How does this impact the customer?
	The customer is unable to perform a subset of basic Docker-related actions such as pulling and pushing images.
	If there is a workaround, what is it?
	The workaround is to run apt-get installno-install-recommends ca-certificates after flashing the system and before attempting to use Docker in the Guest OS.

		When can we expect the fix? 6.0.5.0
3562408	Camera Core	What is the issue?
		Frame drops are observed when running nvsipl_camera sample application with more than 2 IMX728 camera modules (8 MP) with display enabled under following conditions:
		4K display is connected
		-OR-
		RAW output is enabled (enableRawOutput command line option to nvsipl_camera)
		How does it impact the customer?
		Customers may observe similar frame drops with their camera applications if they use a similar pipeline and have a 4K display connected with more than 2 camera modules of 8MP or higher.
		If there is a workaround, what is it?
		N/A
		When can we expect the fiv?
		When can we expect the fix? No fix available as the use cases here likely exceeds the available memory/processing
		BW of VIC/display.
		Is it for SDK/PDK?
		Linux AV+L
3605893	Foundation	What is the issue?
		The KeyOn IST test always fails.
		How does it impact the customer?
		Customer can only run KeyOff IST test.
		If there is a workaround, what is it?
		N/A
		When can we expect the fix?
		6.0.5.0
200775377	System	What is the issue?
	Software	PTP client connected to spruce port P7 fails to sync with PTP server due to known bug from Marvell switch firmware.
		How does it impact the customer?

		If there is a workaround, what is it?
		N/A
		When can we expect the fix?
		The issue is being addressed with the vendor; resolution date is TBD.
		Is it for SDK/PDK?
		All.
3410375	Resource	What is the issue?
	Manager	Intermittent deadlock in kernel nvhost driver during channel timeout recovery with virtualized nvhost-based engines.
		How does it impact the customer?
		If the application is triggering timeouts on virtualized nvhost-based engines, nvhost could deadlock and effectively cause a system hang. However, if timeouts are being triggered, the application is already not working correctly and likely the "pipeline is broken".
		If there is a workaround, what is it?
		N/A
		When can we expect the fix?
		N/A
		Is it for SDK/PDK?
		All.
200775377	System	What is the issue?
	Software	PTP client connected to spruce port P7 fails to sync with PTP server due to known bug from Marvell switch firmware.
		How does it impact the customer?
		Any sensor/device connected to spruce port P7 will not be able to sync with PTP server.
		If there is a workaround, what is it?
		N/A
		When can we expect the fix?
		N/A
3410375	Resource	What is the issue?
	Manager	Intermittent deadlock in kernel nyhost driver during channel timeout recovery with virtualized nyhost-based engines.
		How does it impact the customer?

		If the application is triggering timeouts on virtualized nvhost-based engines, nvhost could deadlock and effectively cause a system hang. However if timeouts are being triggered, the application is already not working correctly and likely the "pipeline is broken".
		If there is a workaround, what is it? N/A
		When can we expect the fix?
3470744	Graphics	What is the issue?
3470744	Graphics	Corruption will be observed on buffers that are flipped by Weston clients to Weston via eglSwapBuffers.
		How does it impact the customer?
		Customers will observe corruption on Graphics applications that call eglSwapBuffers (EGL/GL).
		If there is a workaround, what is it?
		The corruption is observed only when Weston assigns the graphics applications to overlays.
		As a WAR, forcing Weston to use gl-composition by setting WESTON_FORCE_RENDERER=1 will result in the correct content being rendered on display.
		When can we expect the fix? 6.0.5.0
3476824	Docker	What is the issue?
3470824	Docker	DRIVE OS support for building and running Docker in Linux Guest OS does not yet support access to the GPU as GPU support is still under study. Docker images can be built and run on the target, but users will not be able to run any GPU-accelerated containers.
		How does it impact the customer?
		The customer will not be able to run any containerized GPU workloads
		If there is a workaround, what is it?
		N/A
		The user must manually include the required devices and mounts in the docker command line using thedevice and -v flags. In addition, the user would have to run Idconfig in the container to ensure that the libraries are available in the LD cache
		When can we expect the fix?
		6.0.5.0
3480301	Bootloader	Device failed to boot (hung in QB) after PKC (RSA 3K) fusing.

3479678	System	What is the issue?
	Software	USB storage devices (Pen drive, hdd) are not immediately automounted when
		connected, there is a delay of 5-6 minutes before devices are mounted. This is
		whether hotplug or connected at boot.
		How does it impact the customer?
		The user will need to wait for USB storage devices to be mounted
		If there is a workaround, what is it? N/A
		N/A
		When can we expect the fix? 6.0.5.0
3478510	Multimedia	In DRIVE OS 5.2 all NvMedia engine APIs were based on NvMedialmage. But that is
		being removed in 6.0.
		In 6.0 there's a new set of APIs that are based on NvSciBuf. These APIs are
		incompatible with the old 5.2 APIs and customers will need to port their applications to use the new API.
		The new APIs are in the mentioned include/nvmedia_6x folder and the old APIs will be
		removed in a later DRIVE OS 6.0.x.0 release.
3477463	Display	What is the issue?
		EGLDevice based sample applications are failing to display their content on DELL
		2415b monitor.
		How does it impact the customer?
		Customers using DELL 2415b is not able to use EGLDevice based applications.
		If there is a workaround, what is it?
		N/A
		When can we expect the fix?
		6.0.5.0 release.
3474024	DRIVE	What is the issue?
	Update	DRIVE Update fails to switch chain via scratch method. That method is used to verify the newly updated boot chain before permanent switching of bootchain via BR_BCT.
		While switching boot chain via scratch method, a mb1 reset is observed which is
		switch the chain back to original chain. This bug can be blocker for SW update if they
		are using this feature.
		How does it impact the customer?
		Customer may not be able to validate the newly updated chain before final switch for
		boot chain.
		If there is a workaround, what is it?
		There is no WAR for this issue. We are still debugging root cause. Till now it seems p3663 platform specific.
		poods plation in specific.
		When can we expect the fix?
		We are working on root causing this issue. It depends on board availability and repro
2460507	DDMAD	rate. What is the issue?
3469587	ВРМР	verial is the issue;

	1			
		AXI_CBB clock is running at 408MHz frequency, the POR value for use case is 204MHz		
	How does it impact the customer? Potentially marginally higher performance for MMIO accesses than in final product potentially slightly higher power consumption than in final product. If there is a workaround, what is it?			
		No workaround. (It may be possible to lower the frequency via bootrom BCT files, but details of this option are not studied).		
		When can we expect the fix?		
		'		
3445088	MCU	What is the issue?		
	Firmware	IST specific VRS12 threshold values are not available and thus default value used during IST mode		
		How does it impact the customer?		
		The customer runs IST with default threshold values.		
		If there is a workaround, what is it?		
		N/A		
		When can we expect the fix?		
		6.0.5.0		
3436673	Bootloader			
3430073	Bootloader	P3663 ES is not flashing/booting after FSKP changes.		
200775377	System Software	What is the issue? PTP client connected to spruce port P7 fails to sync with PTP server due to known bug		
	MCU Firmware	from Marvell switch firmware.		
	Tilliware	How does it impact the customer?		
		Any sensor/device connected to spruce port P7 will not be able to sync with PTP server.		
		If there is a workaround, what is it?		
		N/A		
200770274		N/A When can we expect the fix?		
200770274		N/A When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD.		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU How does it impact the customer?		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU How does it impact the customer?		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU How does it impact the customer? The customer gets TMON alerts/notifications on board poweroff/powercycle		
200770274		When can we expect the fix? The issue is being addressed with the vendor; resolution date is TBD. What is the issue? TMON gets powered off before MCU when the board is disconnected from the power supply. Thus TMON pins go low which gets detected by MCU How does it impact the customer? The customer gets TMON alerts/notifications on board poweroff/powercycle If there is a workaround, what is it?		

		The following algorithms are not supported on Orin's OFA and/or PVA engines in this release: • ImageFilter (Recursive Gaussian Filter, BoxFilter, 2Dconv).
		• FAST9 Feature Detector, Standard Harris Corner Detector.
		IC and fastIC Feature Tracker.
		DenseOpticalFlow.
		• Stereo.
	<u> </u>	Template Tracker.
200778180		The CGF Demo is not functional on the host or target.
3416653		DriveWorks Graph UI Tool is not included in the packages; it can be provided upon request.
	Image and Point Cloud	The following algorithms are not supported on Orin's OFA and/or PVA engines in this release:
	Processing	ImageFilter (Recursive Gaussian Filter, BoxFilter, 2Dconv).
		FAST9 Feature Detector, Standard Harris Corner Detector.
		IC and fastIC Feature Tracker.
		DenseOpticalFlow.
		• Stereo.
	_	Template Tracker.
3478783		sample_image_pyramid_pva with 8MP camera input fails; other resolutions are supported.
		Tensor Streaming is not operational in this release.
3719823		Image Processing is not supported.
200782352		LRAW Preview Extraction Tool fails.
3602138		Initialize/release dwFrameCapture in loop will lead system run out of memory.
3597225		Video exporter & header-dump tool dumps Segmentation fault while exit.
3605267		sample_cgf_dwchannel is failing for hybrid inter-process, inter-chip scenario and sync_mode p2c and its giving segmentation fault.
3719823		Camera interfacing is not supported.
3263411	TensorRT builder	What is the issue? For some networks, building and running an engine in the standard runtime will have better performance than the safety runtime. This can be due to various limitations in scope of the safety runtime including more limited tactics, tensor size limits, and operations supported in the safety scope.
		How does it impact the customer? Inference in the safety runtime may be significantly slower than in the standard runtime.
		If there is a workaround, what is it?
		Depending on the network, it may or may not be possible to reorganize operations into a more efficient form matching the safety runtime scope.
		What is the recommendation?

		It is recommended to work with NVIDIA and provide proxy networks as early as
		possible that demonstrate key performance metrics close to actual production
		networks. Future releases will target performance improvements for networks within
		the safety scope.
200759535	TensorRT	What is the issue?
	builder	Due to the limitation in the DLA compiler adding copy operators for the bindable
		inputs, TensorRT builder falls back the concat layer to GPU if any of its input tensors is
		the input of the DLA subgraph.
		How does it impact the customer?
		If there is a concat layer in the network and any of its input tensors is the input of the
		DLA subgraph, the TensorRT builder fails to build the engine when
		allowGPUFallback is disabled.
		If there is a workaround, what is it?
		If allowGPUFallback or safe DLA must be enabled, you could explicitly add
		dummy node(s) supported by DLA as the producer of the concat layer.
		dummy mode(s) supported by bill the producer of the contact dyer.
		When can we expect the fix?
		The issue is not expected to be fixed in a future release.
3448473	TensorRT	What is the issue?
	builder	The DLA compilation process in NVIDIA DRIVE OS 6.0.4.0 has a deep recursive call
		which requires a lot of stack memory. On QNX, this may exceed the available stack
		space, leading to memory faults.
		How does it impact the customer?
		Building engines involving DLA for large networks or networks with large batch sizes
		may lead to memory faults on QNX.
		If there is a workaround, what is it?
		To avoid this issue, it's recommended to build the sample applications to utilize a
		bigger stack size. This can be done by adding the $-\mathbb{N}$ option to the linker flags while
		building. For example: use -N8000K to utilize an 8 MB stack when the application
		runs. Applications with larger stack footprint can adjust the size accordingly.
		When can we expect the fix?
		The issue is not expected to be fixed in a future release.
3698054	TensorRT	What is the issue?
3098034	builder	In some cases, the TensorRT builder may allow input and output tensors in HWC16
	bulldel	format in FP16 precision. This format is outside the safety scope.
		How does it import the quetomon?
		How does it impact the customer? The TensorRT builder may generate safe engines outside the safety scope which will
		fail consistency check and so should not be used for inference.
		If there is a workaround, what is it?
		Do not mark inputs and outputs to safe engines with HWC16 format.
		When can we expect the fix?
		A fix will be available in the DRIVE OS 6.0.6 release.
3494734	DLA	What is the issue?

		Some networks may suffer accuracy degradation when run on DLA with large batch sizes.
		How does it impact the customer?
		When running networks on DLA with batch sizes larger than 32, accuracy may
		degrade.
		If there is a workaround, what is it? To work around this issue, use a smaller batch size.
		To Work dround this issue, use a smaller suttin size.
		When can we expect the fix?
		The issue will be fixed in a future DLA release.
3494777	DLA	What is the issue?
		There is a known performance regression for Xavier DLA.
		How does it impact the customer?
		Performance may drop by up to 20% for AlexNet, MNIST, ResNet-50, and VGG-16
		when using DLA on Xavier platforms.
		When can we expect the fix?
		A fix will be available in the DRIVE OS 6.0.6 release.
3498326	DLA	What is the issue?
		There is a known issue with DLA clocks that requires users to reboot the system after
		changing the nvpmodel power mode or otherwise experience a performance drop.
		How does it impact the customer?
		Performance may drop significantly after changing the nvpmodel power mode.
		If there is a workaround, what is it?
		Reboot the system after changing the nvpmodel power mode.
		When can we expect the fix?
		A fix will be available in the DRIVE OS 6.0.6 release.
3656116	TensorRT	What is the issue?
	runtime	There is an up to 7% performance regression for the 3D-UNet networks compared to TensorRT 8.4 EA when running in INT8 precision on NVIDIA Orin due to a functionality fix.
		How does it impact the customer?
		When running 3D-UNet networks in INT8 precision, the latency will be up to 7%
		longer than in TensorRT 8.4 EA.
		If there is a workaround, what is it?
		To work around this issue, set the input type and format to kINT8 and kCHW32, respectively.
		When can we expect the fix?
		We do not plan to fix this performance regression since it was caused by a necessary
		fix for an accuracy issue.
3657753	TensorRT	What is the issue?
	runtime	

		There may be issues with large channel sizes with structured sparsity convolution	
		kernels (seen at size 4096).	
		How does it impact the customer?	
		Computation on the GPU may halt unexpectedly in this case.	
		If there is a workaround, what is it?	
		Turn off structured sparsity if this issue is present.	
		When can we expect the fix?	
		A fix will be available in the DRIVE OS 6.0.6 release.	
CUDA	CUDA	During ongoing testing NVIDIA identified that due to a rounding algorithm error in a very small number of corner cases (less than 0.0000005% of tested combinations) 64-bit floating point division results can differ from the IEEE754 standard by 1 least-significant bit.	
		Floating point operations have many sources of error accumulation and most algorithms will not have encountered this discrepancy. NVIDIA recommends that all developers requiring strict IEEE754 compliance update to CUDA Toolkit 11.7 Update 1 or newer.	
		The affected algorithm is present in both offline compilation as well as just-in-time (JIT) compilation. As JIT compilation is handled by the driver, NVIDIA recommends updating to driver version 515.48.08 or newer for full IEEE754 compliance when required and when using JIT.	
		This issue was identified late in the development cycle and will be addressed in the next DRIVE OS software release.	
200782948	DriveWorks	Lraw recording replay fails. However, raw/mp4 replay is unaffected.	
200777663	DriveWorks	LRAW recording works with an additional sensor parameter "encoder-instance=0". h264/5 recording is unaffected.	
200778225	DriveWorks	Camera Server Client crashes.	
200778085	DriveWorks	Video Exporter Tool fails.	
3409980			
3408375	DriveWorks	SIPL recorded playback of 16 8MP raw/lraw video failed.	
200618961	DriveWorks	Low frames per second (FPS) observed while replaying LRAW/RAW videos with the Camera Replay Sample.	
2539131	DriveWorks	Xsens has a reordering issue with timestamps when reset.	
3614812	DriveWorks	LRAW format recording is not working with sample_camera	
3795061	DriveWorks	Recorder and replayer tool with rig file fails with error: 'dw::core::OutOfBoundsException' what(): HashMap: Index not found	
3499987	DriveWorks	Lidar chop process get stuck for infinite time	
	DriveWorks	The following algorithms are not supported on Orin's OFA and/or PVA engines in this release:	
		ImageFilter (Recursive Gaussian Filter, BoxFilter, 2Dconv).Standard Harris Corner Detector.	
		IC and fastIC Feature Tracker.	

		 DenseOpticalFlow. Stereo. Template Tracker 	
3496936	DriveWorks	Sample_stereo_disparity dumps "Error calling GL deleter" on console	
3478840	DriveWorks	sample_feature_descriptor with raw video input fails, Fast9 task submission failed: PvaError_Error	
	DriveWorks	Tensor Streaming is not operational in this release.	
3597551	DriveWorks	Sample_feature_tracker with PVA Detector fails to run, DW_NOT_AVAILABLE: FeatureDetectPipelinePVA: PVA is not available on this platform	
3558283	DriveWorks	LRAW recording playback gives blank output & both raw & lraw playback fails to exit gracefully, need to kill the playback	
3777686	DriveWorks	Recorder app with 4 cam recording crashes Segmentation fault: NvMapMemAllocInternalTagged: 1074810371 error 12 NvMapMemHandleAlloc: error 0 (nvidia.com)	
3754813	DriveWorks	Video_exporter fails with error: DW_INVALID_ARGUMENT: calculateImageLayout: plane count 0 and format 0 combination is invalid	
3754693	DriveWorks	Sample_calibration_stereo, camera_seek, video_rectifier dumps error on exit: "Bus Error", functionality wise working fine	
3776375	DriveWorks	Sample_video_rectifierLDC fails to record the screen, crash with segmentation fault	
3776370	DriveWorks	Sample_image_capture fails to capture the screen, DW_NOT_AVAILABLE: dwFrameCapture_initialize is not available for serializeGL is truein current version	
3776381	DriveWorks	Sample_video_rectifier recorded mp4 video fails to playback: Driveworks exception thrown: DW_FILE_INVALID: ContainerMp4: error loading header	
3755898	DriveWorks	Playback of H264 video is corrupted which is extracted using extractLRawPreview tool.	
3741019	DriveWorks	Sample_video_rectifier crash on exit: DW_INVALID_ARGUMENT: SyncNvMedia2D::fillNvSciSyncAttrs: NvMedia2DUnregisterNvSciSyncObj failed.	
3432606	DriveWorks	DriveWorks exception thrown: DW_INVALID_HANDLE: Cannot cast to C++ handle when exiting "sample_image_streamer_cross".	
3404259	DriveWorks (DNN Framework)	Sample_dnn_plugin with custom digits fails to detect drawn digit on output window. Always show Detected digit: 1	
3744318	DriveWorks (Calibration)	Luminar H3 lidar self-calibration using DW sample sample_calibration_lidar is not working	
3795934	DriveWorks	STM and SSM samples binary not generating after compilation and cross compilation. sample_image_pyramid_pva is also missing	
3746011	DriveWorks	Sample_cgf_camera_interprocess failing with error: Failed with NvSciError_BadParameter(256) in src/dwcgf/channel/impl/ChannelNvSciStream_new.hpp:466	
3790584	DriveWorks	CFG Demo tool shows inconsistent behavior. Generates random exit codes. 207, 6, 0 etc.	
200776374 200776376	DriveWorks	Nuisance Error Messages that do not affect functionality:	

200778230 3401171		• Every GUI based sample dumps error on console: Failed to list sessions: Unit dbusorg.freedesktop.login1.service is masked.			
3773202 3598944		 All samples dump error on console: TimeSource Eth: PTP ioctl returned error. Synchronized time will not be available from this timesource. All samples dump error on console: No resources(.pak) mounted from '/usr/local/driveworks-5.0/data'. Please adjust path or some modules won't function properly DW_GL_ERROR visible in multiple samples applications Replayer tool dumps segmentation fault (core dumped) at the end of exit. Functionality is working properly 			
		 Recorder-qtgui and recorder-tui fail to launch: ModuleNotFoundError: No module named 'Crypto'. 			
3793123	DriveWorks	DW samples fail in Yocto FS			

Release Properties

The following table describes the release properties and software versions.

Release Properties		
Property	Description	
Linux	Specifies the operating system.	
20.04	Specifies the host Ubuntu operating system version.	
Focal Fossa	Specifies the codename for the host version of Ubuntu.	
20.04	Specifies the target root file system operating system version.	
embedded/6.0.4.0	Specifies the NVIDIA Git release branch.	
6.0.4.0	Specifies the NVIDIA release branch number.	
31006010	Specifies the build ID for the operating system.	
31009903	Specifies the build ID for Foundation.	
drive-linux	Specifies the product name.	
Linux	Specifies the platform.	
p3710-10-a01	Specifies the board name ¹ .	
p3710-10-a03		
p3710-10-a04		
p3710-12-a04		
p3710-10-s05		
p3710-12-a03		
p3710-12-s05		
940-63710-0010-000	Specifies the board SKU.	
940-63710-0012-000		
A00	Specifies the board revision ² .	

¹ This information is used for certain flashing commands. Refer to the Flashing chapter in the *NVIDIA DRIVE OS 6.0 Developer Guide* for more information.

² See the AV PCT Configuration chapter in the *NVIDIA DRIVE OS 6.0 Developer Guide* for more information.

B00	
C00	
D00	
So	ftware Version
Software	Version
cuDNN	8.4.2
TensorRT	8.4.11
CUDA GPU Driver	
OpenGL ES	3.2
OpenGL: Provided for development purposes. Production systems are expected to use OpenGL ES.	4.6
GCC Cross-compiler Toolchain for user applications and libraries for Yocto root file system.	9.3
GCC Cross-compiler Toolchain for user applications and libraries for Ubuntu root file system.	9.3
Wayland	1.18
DriveWorks	5.6
DLA	3.10 ³
CUDA	11.4.17
TensorFlow	1.15.0
ONNX	1.9.0 and opset 13

DRIVE OS Supported Sensors

The following cameras are supported in this release. Future releases will include support for additional cameras.

Camera Make/Model	Sensor	Availability
Sekonix SF3324		Please contact the vendor for information on how to access the DRIVE OS driver for this camera.
Entron F008A030RM0AES	ON Semi AR0820 (120)	The driver for this camera is included with the DRIVE OS SDK release.

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 $^{^{3}}$ DLA versions 1.3.7, 1.3.8, and 3.9.0 are also supported.

Component Properties

CUDA

The following table describes CUDA support.

Host OS	Host OS Version	Target OS	Target OS Version	Compiler Support
Ubuntu	20.04 LTS	Ubuntu	Ubuntu 20.04	GCC 9.3

Standard

The current release label is 11.4.17. The various components in the toolkit are versioned independently. The following table shows each component and its version:

Component Name	Version Information	Supported Architectures	
CUDA Runtime (cudart)	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
cuobjdump	11.4.212	Linux (aarch64), Linux (x86_64)	
CUPTI	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA cuxxfilt (demangler)	11.4.212	Linux (aarch64), Linux (x86_64)	
CUDA Demo Suite	11.4.212	Linux (x86_64)	
CUDA GDB	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA NVCC	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA nvdisasm	11.4.212	Linux (aarch64), Linux (x86_64)	
CUDA NVML Headers	11.4.212	Linux (aarch64), Linux (x86_64)	
CUDA nvprof	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA nvprune	11.4.212	Linux (aarch64), Linux (x86_64)	
CUDA NVRTC	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA NVTX	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA NVVP	11.4.212	Linux (x86_64)	
CUDA Samples	11.4.212	l4t_aarch64, Linux (aarch64), Linux (x86_64)	
CUDA Compute Sanitizer API	11.4.212	Linux (aarch64), Linux (x86_64)	
CUDA Thrust	11.4.212	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	

CUDA cuBLAS	11.6.5.69	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA cuDLA	11.4.212	Linux (aarch64), qnx-standard_aarch64	
CUDA cuFFT	10.6.0.116	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA cuRAND	10.2.5.211	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA cuSOLVER	11.2.0.211	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA cuSPARSE	11.6.0.211	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA NPP	11.4.0.201	Linux (aarch64), Linux (x86_64), qnx- standard_aarch64	
CUDA nvJPEG	11.5.2.211	Linux (x86_64), qnx-standard_aarch64	
Nsight Compute	2021.2.4.1	Linux (x86_64), qnx-standard_aarch64	
Nsight Systems	2022.1.3.2	Linux (x86_64)	
NVIDIA Linux Driver	470.103.01	Linux (x86_64)	

TensorRT

The following sections are related to TensorRT.

	Linux x86-64	Linux AArch64
Supported CUDA versions	11.4	11.4
Supported cuDNN versions	8.4.2	8.4.2
TensorRT Python API	Yes	Yes
NvUffParser	Deprecated	Deprecated
NvOnnxParser	Yes	Yes

Note: Version numbers must match (in major, minor, patch, and build) for the previously generated serialized engine to be compatible. Version numbers for major, minor, and patch must be earlier or equal to the runtime version numbers, and later than or equal to 8.4.11.

Hardware and Precision

The following table lists NVIDIA hardware and which precision modes each hardware supports. It also lists availability of Deep Learning Accelerator (DLA) on this hardware. For standard runtime, TensorRT supports SM 7.x or SM 8.x. For proxy runtime, TensorRT supports all hardware with capability of 8.x. For safety runtime, TensorRT supports hardware with capability of 8.7.

For more information, refer to the "If I build the engine on one GPU and run the engine on another GPU, will this work?" question in the FAQ section in the NVIDIA DRIVE OS 6.0 TensorRT 8.4.11 Developer Guide.

CUDA Compute Capability	Example Device	TF32	FP32	FP16	INT8	FP16 Tensor Cores	INT8 Tensor Cores	DLA
8.7	NVIDIA Orin	No (TensorRT safety) Yes (TensorRT standard)	Yes	Yes	Yes	Yes	Yes	Yes
8.6	NVIDIA A10	Yes	Yes	Yes	Yes	Yes	Yes	No
8.0	NVIDIA A100/GA100 GPU	Yes	Yes	Yes	Yes	Yes	Yes	No
7.5	NVIDIA T4	No	Yes	Yes	Yes	Yes	Yes	No
7.2	Jetson AGX Xavier	No	Yes	Yes	Yes	Yes	Yes	Yes
7.0	NVIDIA V100	No	Yes	Yes	Yes	Yes	No	No
6.1	NVIDIA P4	No	Yes	No	Yes	No	No	No
6.0	NVIDIA P100	No	Yes	Yes	No	No	No	No

Software Versions Per Platform

The following lists supported platforms per software version.

Platform	Compiler Version	Python Version
Ubuntu 20.04 x86-64	gcc 9.3.0	3.8
Ubuntu 20.04 AArch64	gcc 9.3.0	3.8

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