

Divide and Conquer Network Load Balancing in Large-Scale Distributed Training

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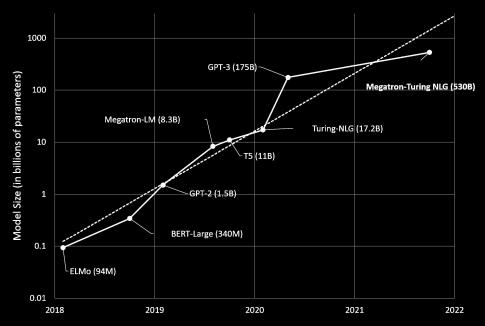






Fantel Side Meeting, IETF 122 18 March, 2025

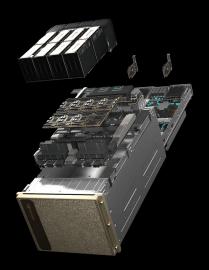
- Exponentially growing compute requirements of language models





Trend of sizes of state-of-the-art NLP models over time

- Enter parallelization





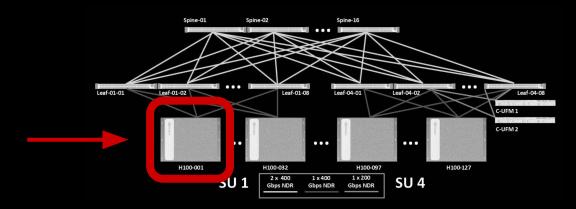
- Multiple multi-GPU servers can be connected together to create a "Super Pod"



Nvidia DGX Super Pod



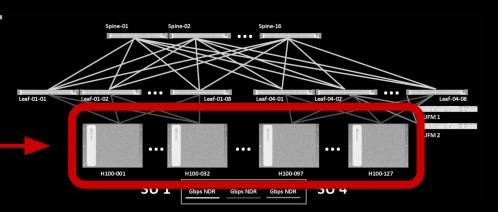
- Multiple multi-GPU servers can be connected together to create a "Super Pod"
- Each DGX server provides 8 GPUs for parallelization



Nvidia DGX Super Pod



- Multiple multi-GPU servers can be connected together to create a "Super Pod"
- Each DGX server provides 8 GPUs for parallelization
- Interconnecting multiple servers allows for scaling to a large cluster
- e.g, GPT-4 was trained on a cluster of ~25000 GPUs



Nvidia DGX Super Pod



- GPUs *communicate* during the forward and backward pass of training
 - e.g., GPUs compute local gradients and perform AllReduce operation
 - GPUs in a cluster exchange gradients in order to aggregate results



Load Balancing Problem under Link Failures

- Granularity
 - Per-packet
 - Per-flow
 - Sub-flow
- Path selection
 - Static
 - Adaptive
- Rerouting and fast notification
 - ECN
 - Timeout
 - Explicit failure notifications
 - -



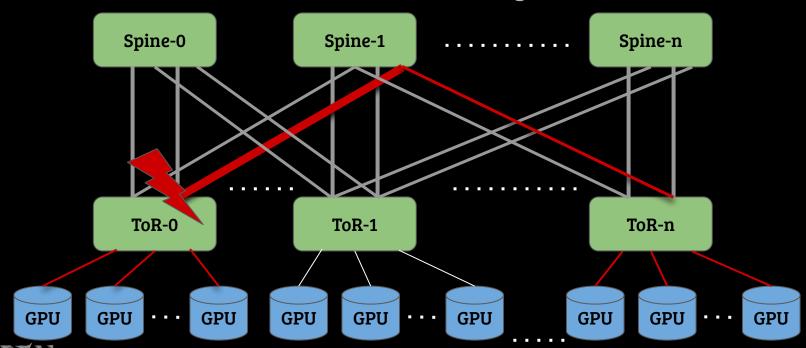
Load Balancing Problem under Link Failures

- ~100 milliseconds just to identify link failure *locally*
 - A network switch continues to send packets to the failed port during the 100ms
 - At 400 Gbps, this leads to 5 GB dropped packets



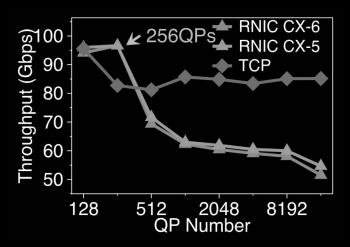
ECMP: Load Imbalance due to Low Entropy

Hash collisions lead to load imbalance and congestion



MP-RDMA: Entropy at the cost of NIC Resources

- Split every flow *k* times
- Increased entropy to ECMP hash
- Better load balancing but at the cost of increased number of QPs at the NIC



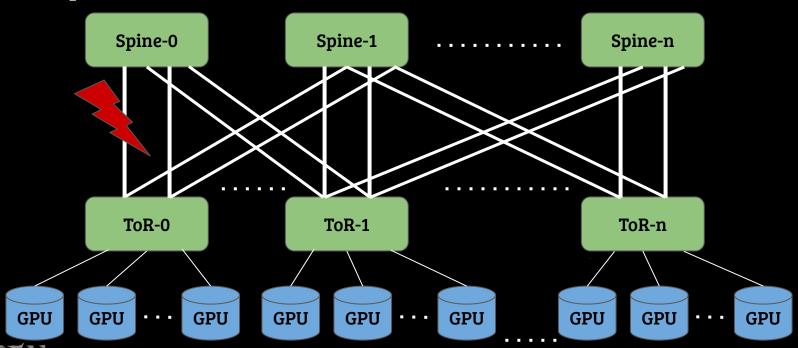




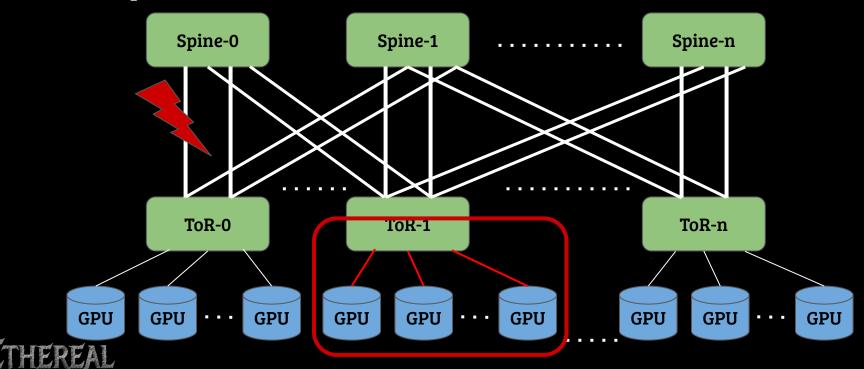
- Optimal load balancing in a CLOS-based topology
- Suboptimal under asymmetry



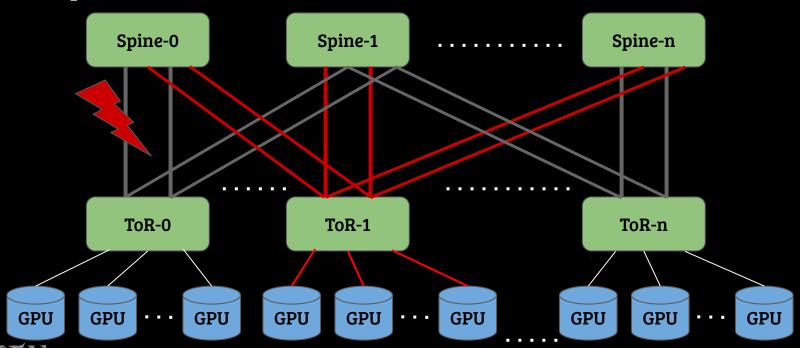
Example: One link incident to ToR-0 fails



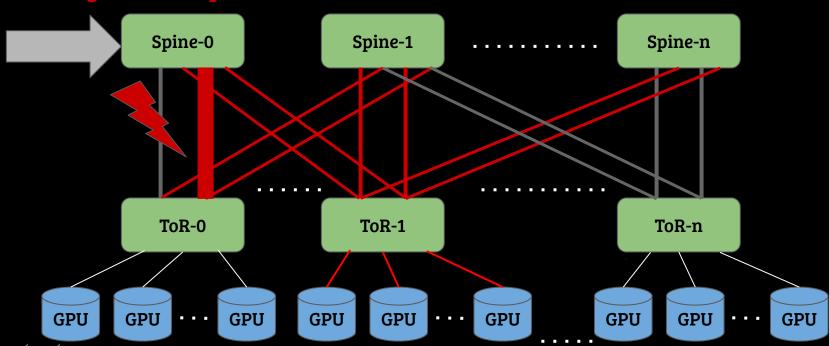
Example: All-to-All Traffic from Tor-1 to ToR-0



Example: All-to-All Traffic from Tor-1 to ToR-0



• Congestion at Spine-0



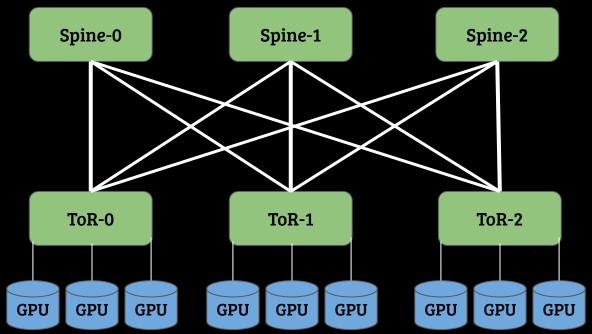
- Optimal load balancing in a CLOS-based topology
 - Novel transport and hardware requirements
 - Handling out-of-order packet delivery
 - Loss recovery mechanisms
 - 0 ...
 - o see Ultra Ethernet Consortium whitepaper
- Suboptimal under asymmetry
 - Congestion under failures
 - Non-trivial to react to failures even with perfect failure notifications



- A novel load balancing algorithm being discussed in industry and UEC
- Explore and Cache good paths
- Change path (reroute) upon receiving ECN
- Can lead to path flapping

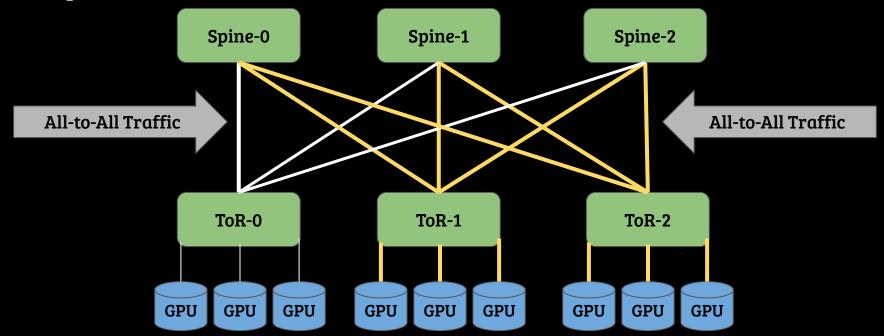


Example: All-to-All across GPU 3 to 8

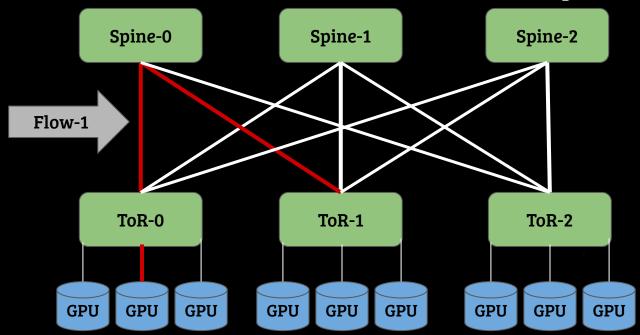




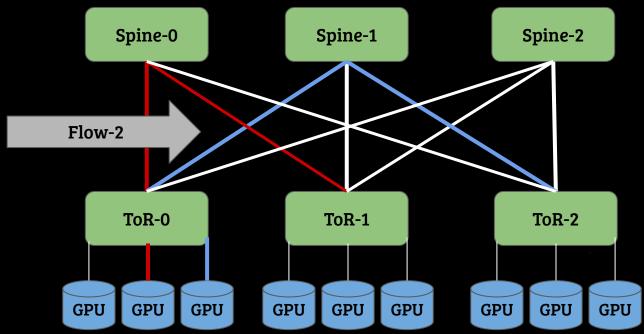
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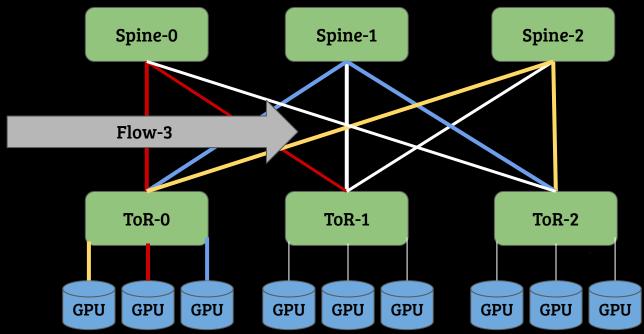




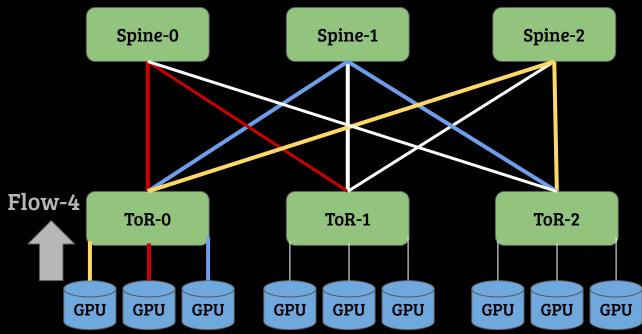






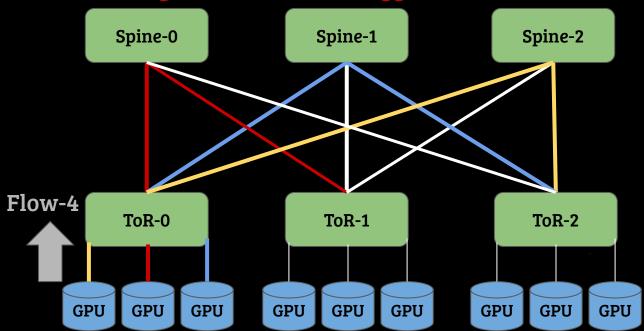








The 4th flow causes congestion (ECN) and triggers reroute

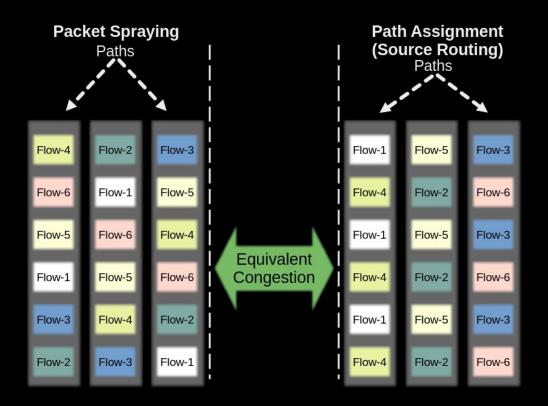




Summary and Design Goals

- Path context helps in identifying failures
- Number of flows fundamentally limits singlepath load balancing
- Goals:
 - o Uniform load balancing, like Packet Spraying
 - Identify failed paths quickly, like REPS





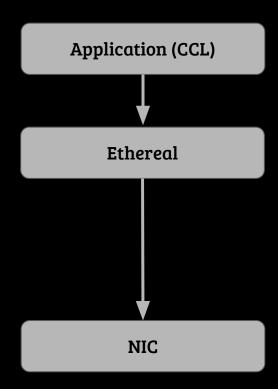


- Load balancing at the application layer (Communication Library)
- Source routing for path selection
- Minimal flow splitting
 - Key enabler: Flow size is known upon arrival, unlike traditional storage/search workloads
 - Prevents path flapping
 - Achieves optimal load balancing
- Flow timeout notification from NIC to application
- Full details: https://arxiv.org/pdf/2407.00550

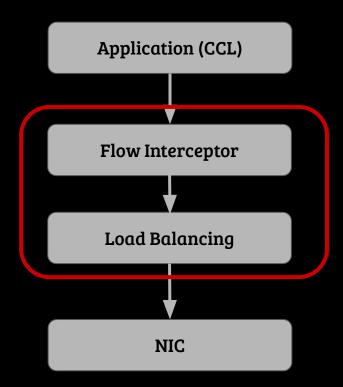


- Load balancing at the application layer (Communication Library)
 - Simple
 - Does not require new hardware
 - Challenge: RDMA NIC (hardware) handles all the network stack and flow transmission
 - How can link failures be notified to the application?

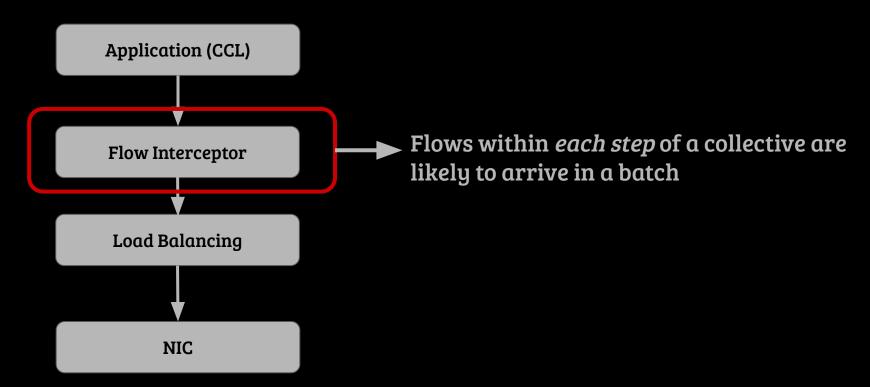




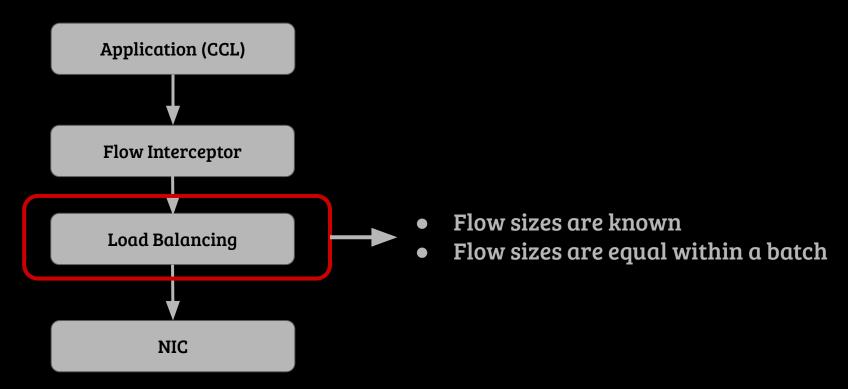




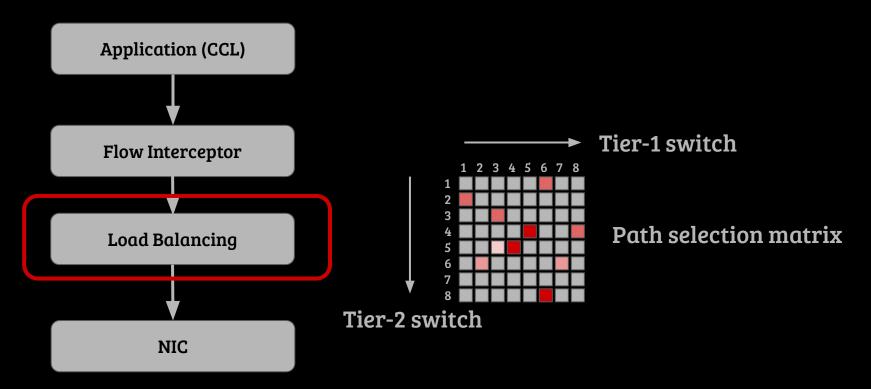




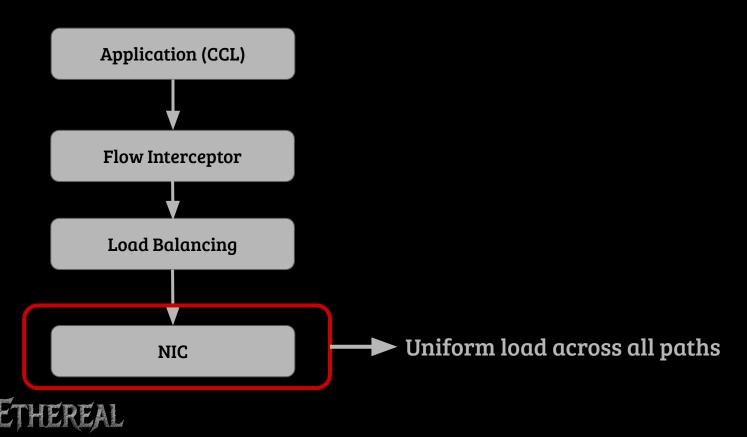


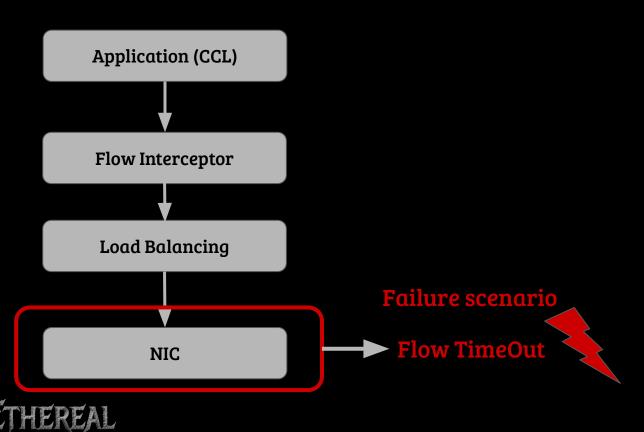


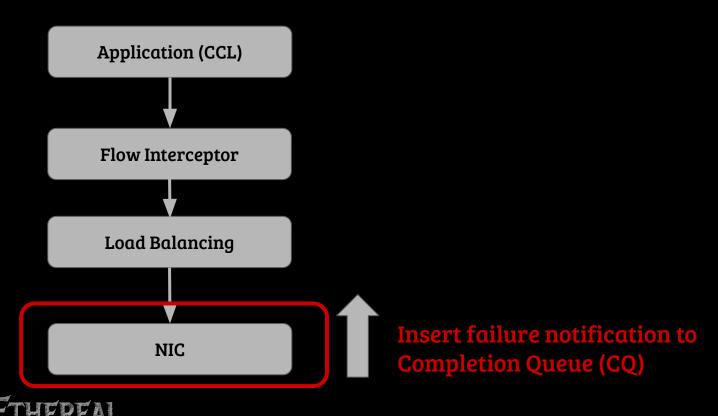


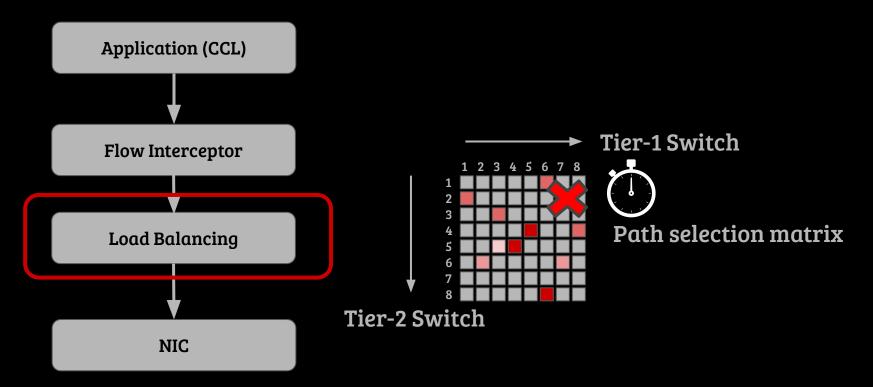




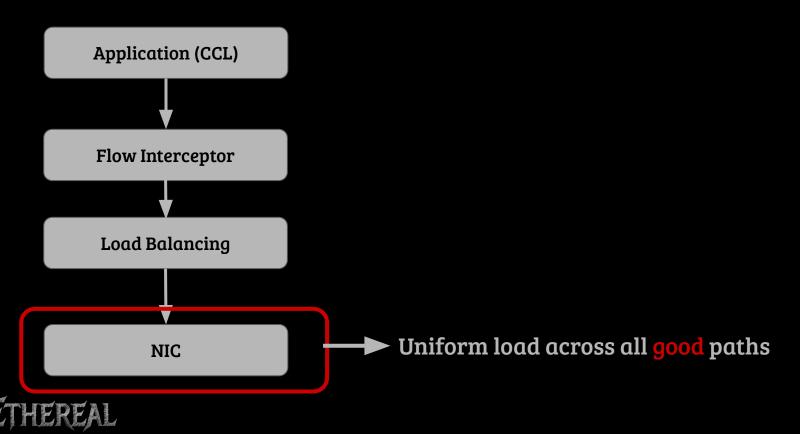












Failure Notifications

- NIC to Application notification
 - Currently relying on flow timeouts
 - Speed vs accuracy tradeoff
- Network to NIC notification: Open for discussion!
 - Source Quench?
 - Transceivers and drivers require fundamental changes to identify local failures rapidly!

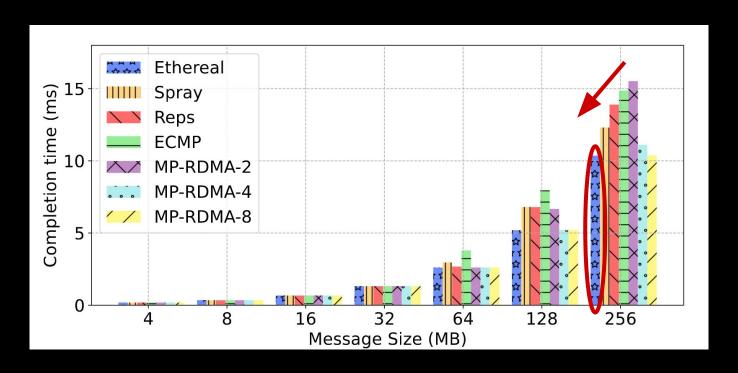


Evaluation

- Simulations: Astrasim & MLCommons Chakra
- Fat tree topology
 - 512 GPUs
 - 32 ToR
 - 32 Aggregation
 - 16 Core
- 400 Gbps links
- 64MB switch buffer size
- Timeout: 1 ms

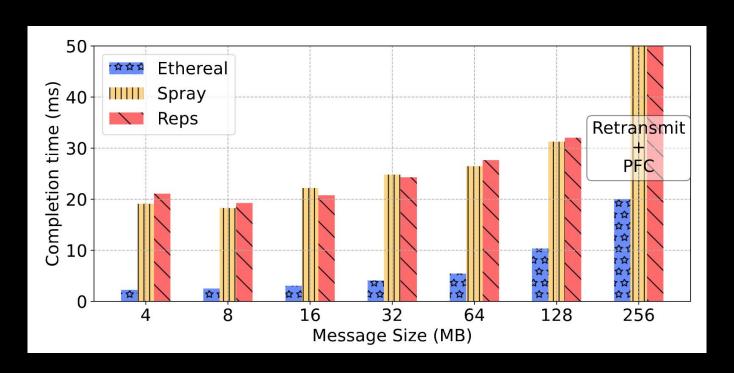


Ethereal Improves Collective Completion Times



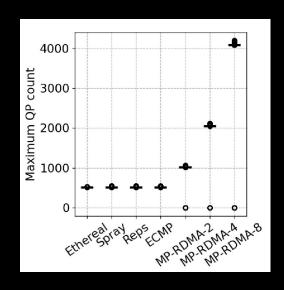


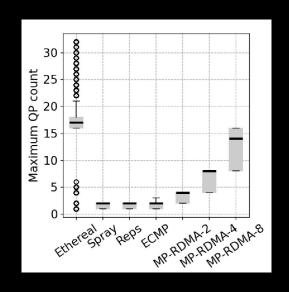
Ethereal Reacts to Failures Promptly

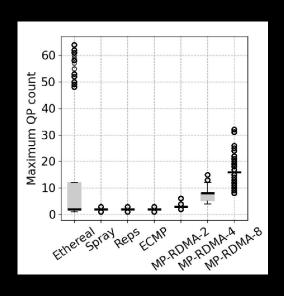




Ethereal Does not Significantly Increase QP requirements







All-to-All

Recursive Doubling

Ring



Conclusion

- Uniform load balancing in CLOS topologies does not require significant hardware changes to NIC
- Ethereal achieves optimal load balancing using singlepath transport
 - Source routing
 - Minimal flow splitting
- Failure notifications from the network can further improve performance
- Source code available online
 - On request currently, please send an email or ping me on LinkedIn



Thank You

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