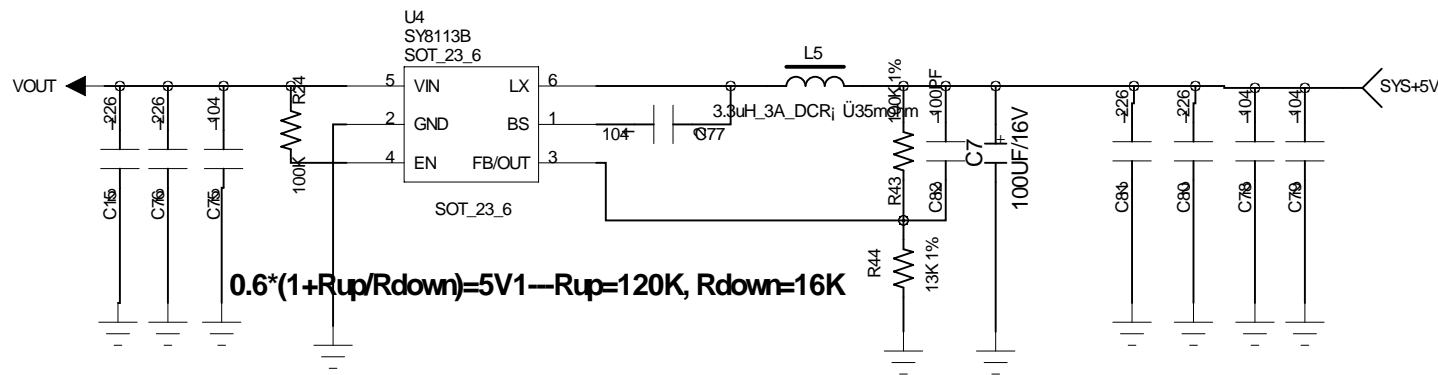


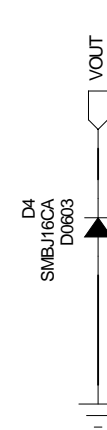
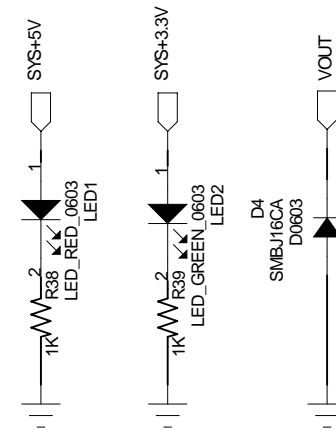
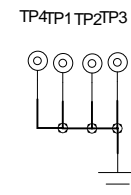
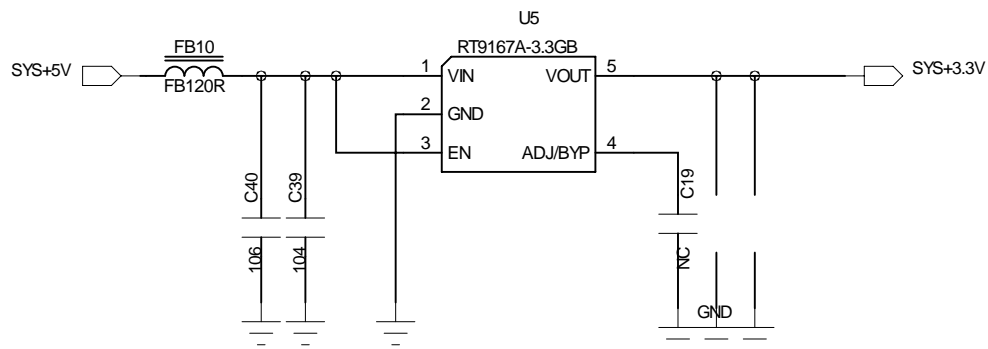
POWER

12V to 5V BUCK

5.1V/3A



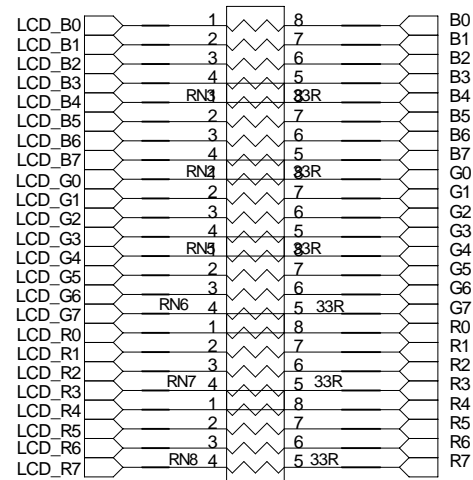
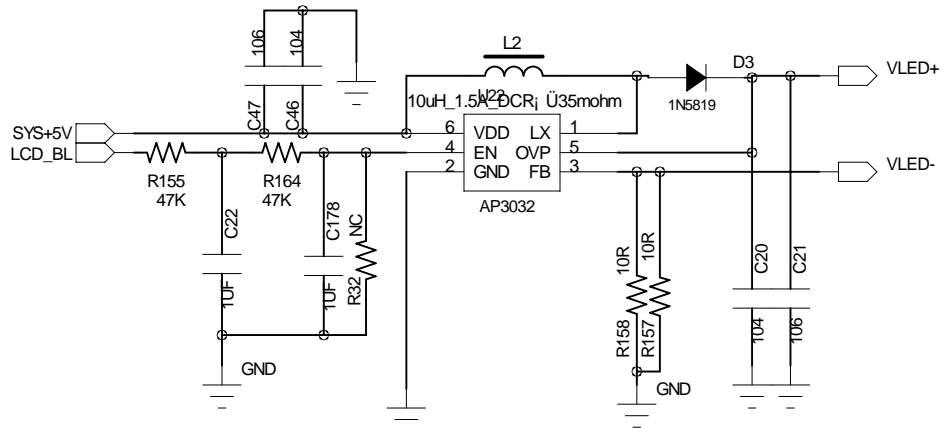
$$0.6 \cdot (1 + R_{up}/R_{down}) = 5V1 \rightarrow R_{up} = 120K, R_{down} = 16K$$



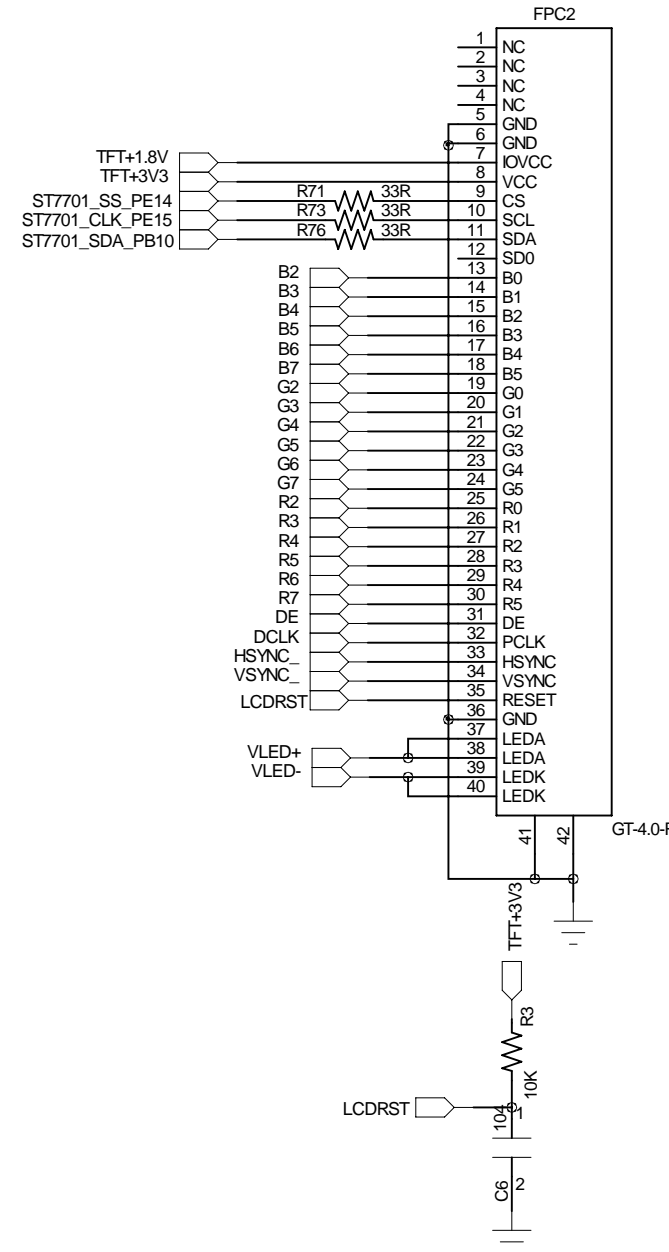
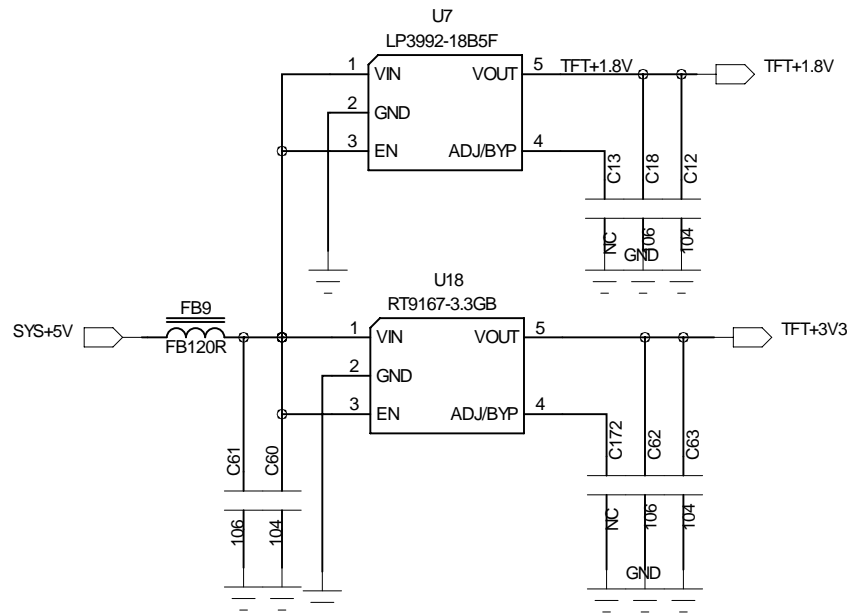
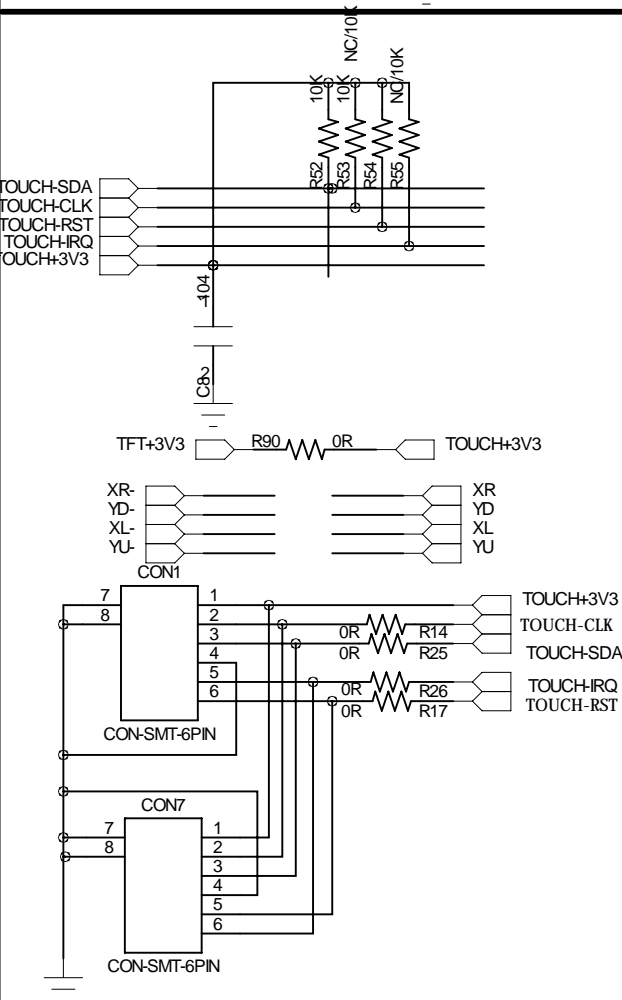
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Screen backlight circuit



Display interface/ power supply



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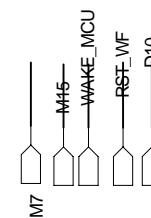
Project:	GT-4.0 "serial screen module circuit diagram"		
File:			
Date:	2023-03-27	Rev:	V1.0
Designed by:	HGL	Sheet:	4 of 5

The schematic diagram illustrates the electrical connections for the PCF8563TS RTC module. The module is represented by a central block with pins 1 through 8. The connections are as follows:

- Pin 1 (OSCI):** Connected to the external oscillator input.
- Pin 2 (OSCO):** Connected to the external oscillator output.
- Pin 3 (/INT):** Connected to the BATT+ supply.
- Pin 4 (GND):** Connected to ground.
- Pin 5 (SDA):** Connected to the RTC_SDA signal line.
- Pin 6 (SCL):** Connected to the RTC_SCL signal line.
- Pin 7 (VDD):** Connected to the SYS+3.3V supply.
- Pin 8 (CLKOUT):** Connected to the BATT+ supply.

Additional components and connections include:

- Resistors:** R11 (10K) on the OSCI line, R10 (10K) on the OSCO line, R27 (10K) on the BATT+ line, and R9 (4.7R) on the SYS+3.3V line.
- Capacitors:** C41 (100nF) on the BATT+ line, C43 (6PF) on the OSCI line, and C42 (6PF) on the OSCO line.
- Diode:** D1 (B5819WS) is connected between the BATT+ and SYS+3.3V lines.
- Power Supply:** The BATT+ supply is connected to the BATT pin of the module.

[illegible]

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