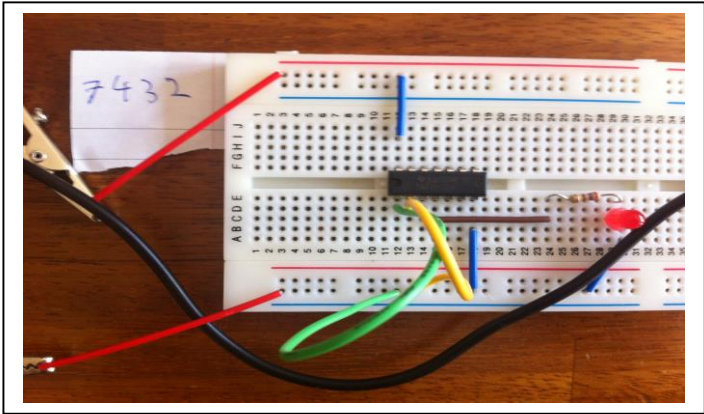


Task 1: Practical - Build a logic circuit/circuits to show the operation six digital IC's

Circuit 1:



Logic Gate Name: OR

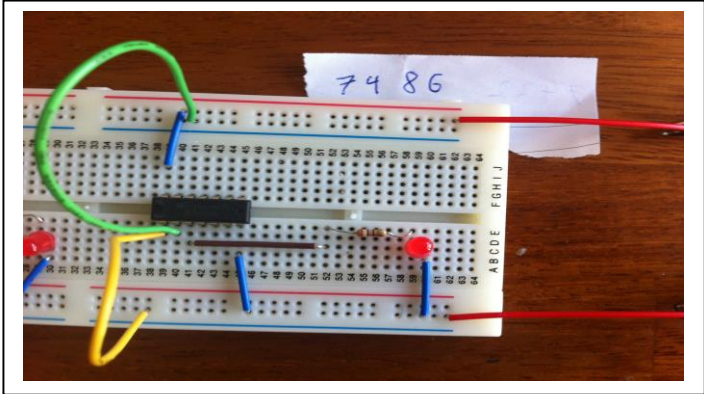
Logic Gate Symbol:

Chip Number: 7432

Create a truth table for this logic circuit.

P	Q	
1	1	1
1	0	1
0	1	1
0	0	0

Circuit 2:



Logic Gate Name: XOR

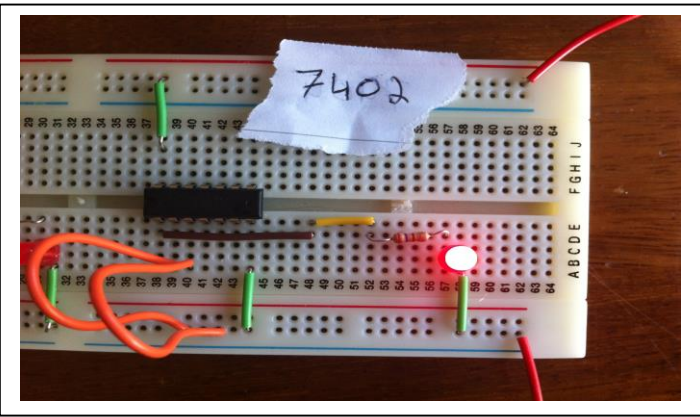
Logic Gate Symbol:

Chip Number: 7486

Create a truth table for this logic circuit.

P	Q	
1	1	0
1	0	1
0	1	1
0	0	0

Circuit 3:



Logic Gate Name:

NOR

Logic Gate Symbol:

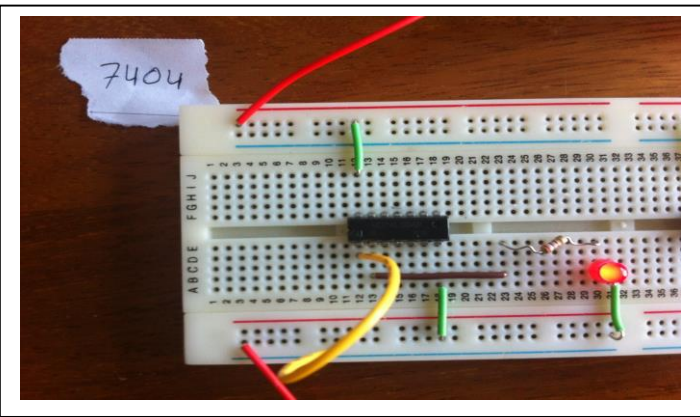
Chip Number:

7402

Create a truth table for this logic circuit.

P	Q	
1	1	0
1	0	0
0	1	0
0	0	1

Circuit 4:



Logic Gate Name:

NOT

Logic Gate Symbol:

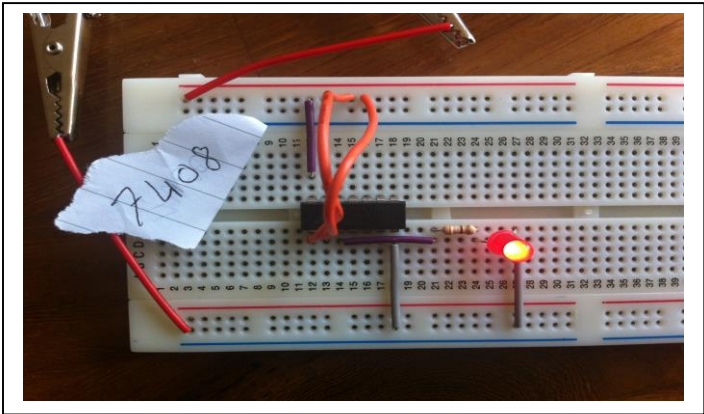
Chip Number:

7404

Create a truth table for this logic circuit.

P	
1	0
0	1

Circuit 5:



Logic Gate Name: AND

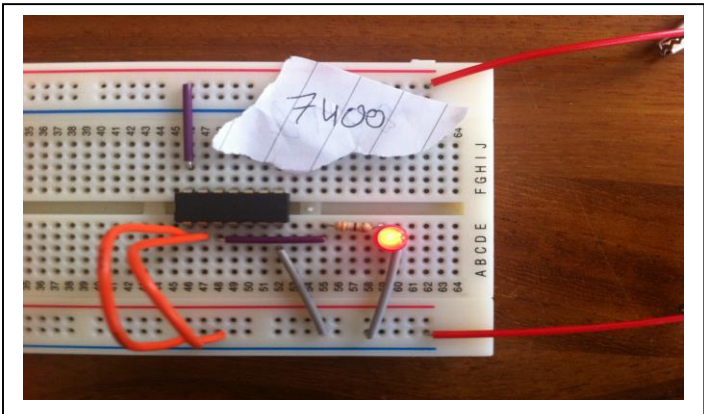
Logic Gate Symbol:

Chip Number: 7408

Create a truth table for this logic circuit.

P	Q	
1	1	1
1	0	0
0	1	0
0	0	0

Circuit 6:



Logic Gate Name: NAND

Logic Gate Symbol:

Chip Number: 7400

Create a truth table for this logic circuit.

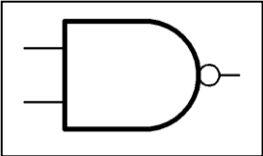
P	Q	
1	1	0
1	0	1
0	1	1
0	0	1

Circuit 7: Assemble a logic gate circuit to represent the following statement $\sim (p \wedge q)$

See end of report

Logic Gate Names:

NAND

Logic Gate Symbols: 

Chip Numbers:

7408 and 7404

Create a truth table for this logic circuit.

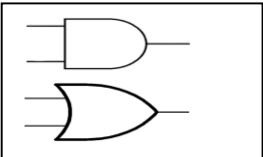
P	Q	$P \wedge q$	$\sim (p \wedge q)$
1	1	1	0
1	0	0	1
0	1	0	1
0	0	0	1

Circuit 8: Assemble a logic gate circuit to represent the following statement $r \vee (p \wedge q)$

See end of report

Logic Gate Names:

AND OR

Logic Gate Symbols: 

Chip Numbers:

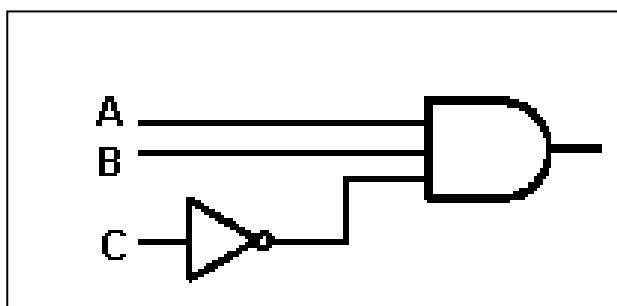
7408 7432

Create a truth table for this logic circuit.

P	Q	R	$P \wedge q$	$R \vee (p \wedge q)$
1	1	1	1	1
1	1	0	1	1
0	1	1	0	1
1	0	1	0	1
1	0	0	0	0
0	1	0	0	0
0	0	1	0	1
0	0	0	0	0

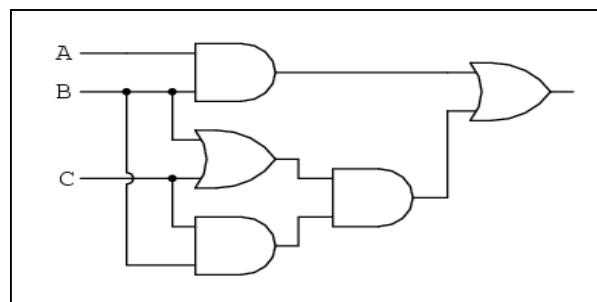
Task 2: Problems Solving:

1. Write a Boolean expression to represent the following logic gate circuit



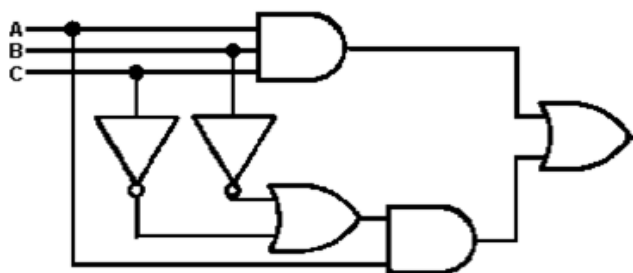
Boolean Expression: $a \wedge b \wedge \sim c$

2. Write a Boolean expression to represent the following logic gate circuit



Boolean Expression: $(a \wedge b) \vee ((b \vee c) \wedge (b \wedge c))$

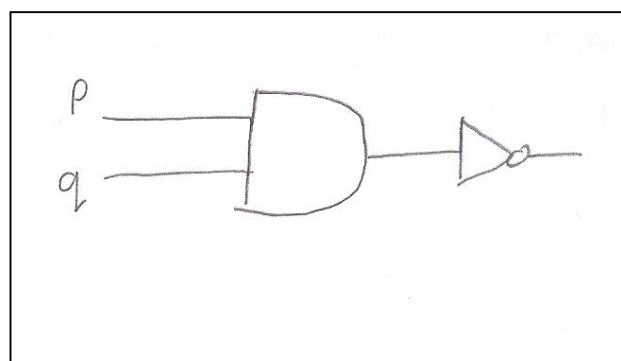
3. Write a Boolean expression to represent the following logic gate circuit



Boolean Expression: $(a \wedge b \wedge c) \vee (\sim b \vee \sim c) \wedge a$

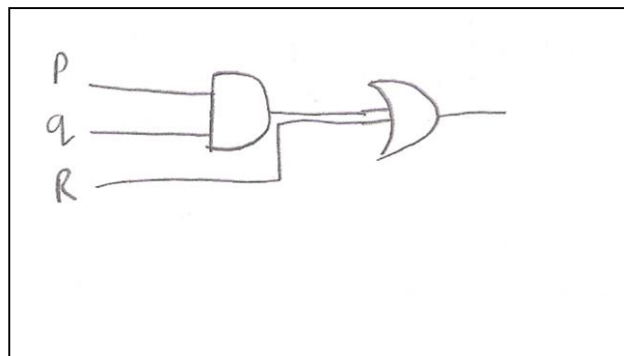
4. Draw the logic gates to represent this Boolean expression.

$$\sim (p \wedge q)$$



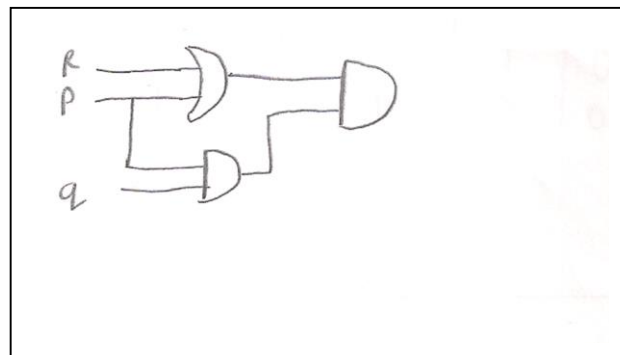
5. Draw the logic gates to represent this Boolean expression.

$$r \vee (p \wedge q)$$



6. Draw the logic gates to represent this Boolean expression.

$$(r \vee p) \wedge (p \wedge q)$$



7. Use a truth table to determine if the following statement is true or false.

$$p \wedge (q \vee r) \equiv (p \wedge q) \vee (p \wedge r)$$

P	Q	R	$Q \vee r$	$P \wedge (q \vee r)$	≡	$P \wedge q$	$P \wedge r$	$(p \wedge q) \vee (p \wedge r)$
1	1	1	1	1		1	1	1
1	1	0	1	1		1	0	1
0	1	1	1	0		0	0	0
1	0	1	1	1		0	1	1
1	0	0	0	0		0	0	0
0	1	0	1	0		0	0	0
0	0	1	1	0		0	0	0
0	0	0	0	0		0	0	0

8. Using truth tables prove de Morgan's law that

$$\sim (p \wedge q) \equiv \sim p \vee \sim q$$

P	Q	$P \wedge q$	$\sim (P \wedge q)$	≡	P	Q	$\sim p$	$\sim q$	$\sim p \vee \sim q$
1	1	1	0		1	1	0	0	0
1	0	0	1		1	0	0	1	1
0	1	0	1		0	1	1	0	1
0	0	0	1		0	0	1	1	1

9. Design an interactive worksheet to convert from base 2, 8 and 16 to the base 10 equivalent.

a .Explain the operation of converting a base 2, base 8 and base 16 number to it's decimal equivalent. Include a minimum of 3 worked examples.

To convert from any base to base 10, we use the place values of the binary number as powers. We then multiply each digit of the binary number by the base to the place value power. We then add the results to get the decimal equivalent.

From Base 2 to Base 10:

1(4) 0(3) 0(2) 1(1) 1(0) (base 2)

$1 \times 2^{\text{(power 4)}} + 0 \times 2^{\text{(power 3)}} + 0 \times 2^{\text{(power 2)}} + 1 \times 2^{\text{(power 1)}} + 1 \times 2^{\text{(power 0)}}$

$= 16 + 0 + 0 + 2 + 1$

$= 19$

From Base 8 to Base 10:

5(2) 4(1) 5(0) (base 8)

$5 \times 8^{\text{(power 2)}} + 4 \times 8^{\text{(power 1)}} + 5 \times 8^{\text{(power 0)}}$

$= (5 \times 64) + (4 \times 8) + (5 \times 1)$

$= 320 + 32 + 5$

$= 357$

From Base 16 to Base 10:

1(2) 6(1) 5(0) (base 16)

$1 \times 16^{\text{(power 2)}} + 6 \times 16^{\text{(power 1)}} + 5 \times 16^{\text{(power 0)}}$

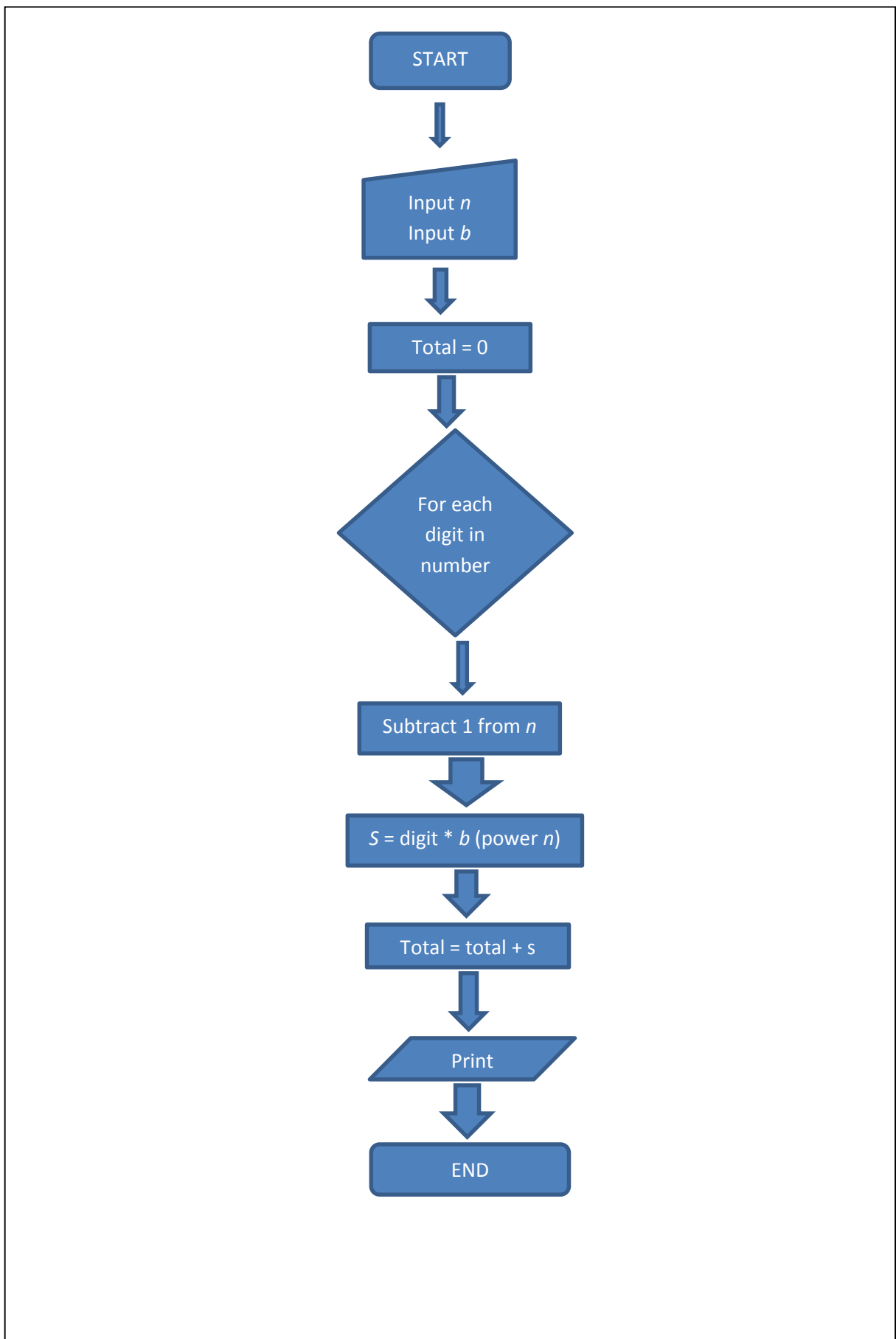
$= 256 + 96 + 5$

$= 357$

b . Produce an algorithm of the above task.

1. Start
2. Let n be the number of digits in the number
3. Let b be the base of the number
4. Set total (s) to zero
5. For each digit in the number
 - a. Subtract 1 from n
 - b. Multiply the digit times b to the power of n and add it to s
6. Print total
7. End procedure

c. Produce a flowchart of the above algorithm.



d. Include screenshots of worksheet showing solutions to problems specified in part a.

Base 2 to Base 10:

	A	B	C	D	E	F	G	H	I	J	K	L
1												
2	Place Value	128	64	32	16	8	4	2	1			
3	Power	7	6	5	4	3	2	1	0		BASE:	2
4					1	0	0	1	1			
5												
6												
7				BASE 10:	19							
8												
9												

Base 8 to Base 10:

	A	B	C	D	E	F	G	H	I	J	K	L
1												
2	Place Value	2097152	262144	32768	4096	512	64	8	1			
3	Power	7	6	5	4	3	2	1	0		BASE:	8
4							5	4	5			
5												
6												
7				BASE 10:	357							
8												
9												

Base 16 to Base 10:

	A	B	C	D	E	F	G	H	I	J	K	L
1												
2	Place Value	2.68E+08	16777216	1048576	65536	4096	256	16	1			
3	Power	7	6	5	4	3	2	1	0		BASE:	16
4							1	6	5			
5												
6												
7				BASE 10:	357							
8												
9												

10. Design an interactive worksheet that can add and subtract two 6 digit

binary numbers.

a .Explain the operation of adding and subtracting two . Include a minimum of 3 worked examples.

When adding two binary numbers, we must follow a set of rules (1+0=1, 0+0=1) unless both numbers are a 1, in which case we put down a 0 and carry a 1 (as 1+1 in binary is 10(one zero)). If there are three 1's, we then put down a 1 and carry a 1 (as 1+1+1 in binary is 11(one one)).

Examples:

$$\begin{array}{r}
 \\
 \\
 + \\
 =
 \end{array}$$

When subtracting two binary numbers, we must first get the 1's complement of the number. This is achieved by changing 1's to 0's, and 0's to 1's (e.g. 100110 becomes 011001). We then find the 2's complement of our inverted binary number, this is done by adding 1 to the least significant digit. Once we've done that, we then add our original number to the 2's complement.

Example:

$$\begin{array}{r}
 \\
 \\
 + \\
 =
 \end{array}$$

b . Produce an algorithm of the above task.

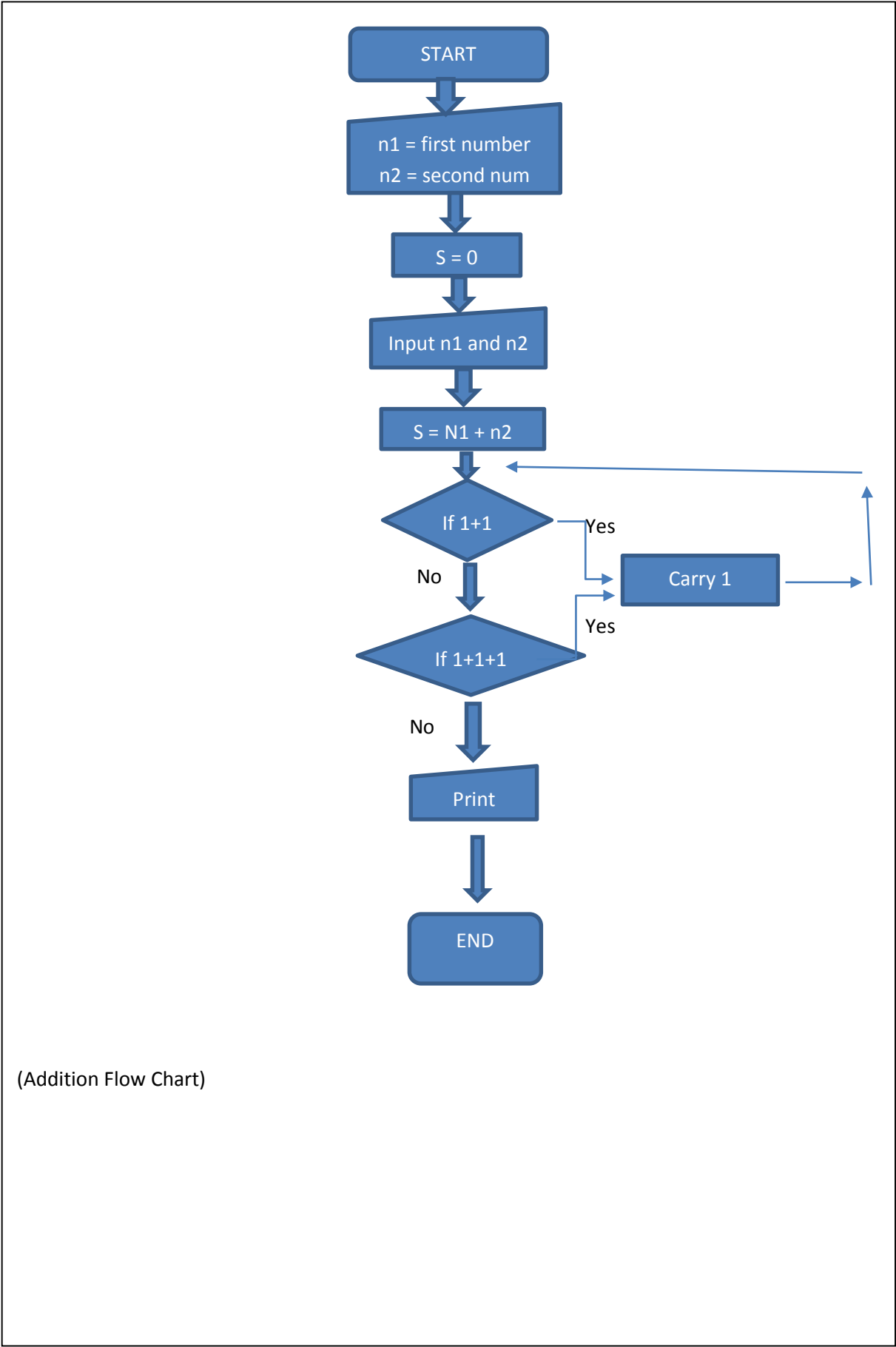
Addition:

1. Start
2. Let $n1$ be the first number
3. Let $n2$ be the second number
4. Let s be the total
5. Add the two numbers
 - a. Put down 0 and Carry 1 if the sum is $1 + 1$
 - b. Put down 1 and Carry 1 if sum is $1 + 1 + 1$
6. Print result
7. End

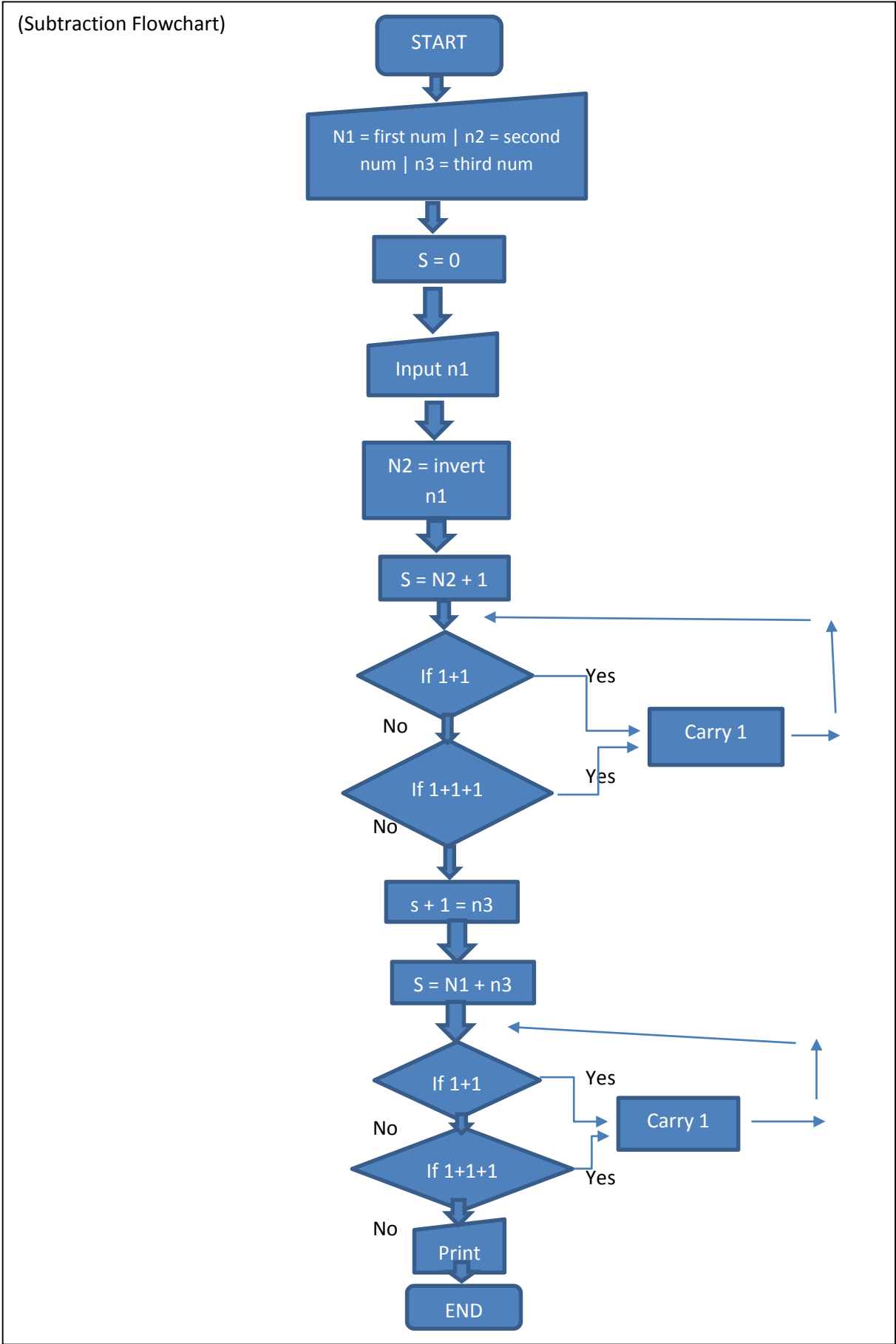
Subtraction:

1. Start
2. Let $n1$ be the original number
3. Let $n2$ be the 1's complement
4. Let $s = \text{total}$
5. Invert $n1$ to find $n2$
6. Add 1 to $n2$ to get $n3$
 - a. Put down 0 and carry 1 if the sum is $1+1$
 - b. Put down 1 and carry 1 if the sum is $1+1+1$
7. Let $n3$ be the 2's complement
8. Add $n1$ and $n3$
 - a. Put down 0 and carry 1 if the sum is $1+1$
 - b. Put down 1 and carry 1 if the sum is $1+1+1$
9. Print result
10. End

c. Produce a flowchart of the above algorithm.



c. Produce a flowchart of the above algorithm.



d. Include screenshots of your worksheet showing solutions to problems specified in part a above

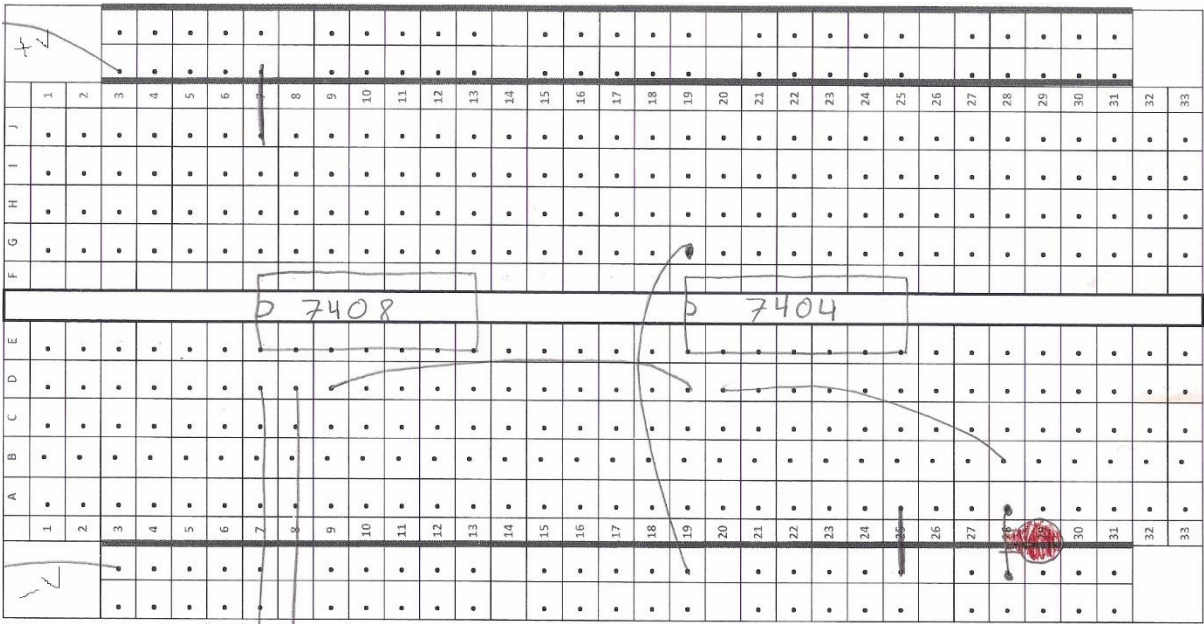
Addition:

	A	B	C	D	E	F	G	H
1								
2								
3								
4								
5	Carry		1	1	0	1	0	
6			1	1	1	0	1	1
7			1	0	1	0	1	0
8		1	1	0	0	1	0	1
9								

Subtraction:

	A	B	C	D	E	F	G	H	I
1									
2									
3									
4									
5		Carry	0	0	0	0	1		
6		Original Number	1	0	1	1	1	0	
7		1's Complement	0	1	0	0	0	1	
8	+		0	0	0	0	0	1	
9	=	2's Complement	0	1	0	0	1	0	
10									
11		Carry	1	1	1	1	0		
12		Original Number	1	0	0	1	1	0	
13	+	2's Complement	0	1	1	0	1	0	
14	=		1	0	0	0	0	0	
15									
16									

Circuit 7



Circuit 8

